Reachability in Parallel Programs Is Polynomial in the Number of Threads (Version with Proofs)

Alexander Malkis

Abstract

Reachability in parallel finite-state programs equipped with interleaving semantics is an important yet inherently difficult problem. Its complexity as a function of the number of threads *n*, while keeping the thread-local–memory size and the shared-memory size bounded by constants, has been explored only poorly. We significantly narrow this gap by measuring: (i) the *diameter*, i.e., the longest finite distance realizable in the transition graph of the program, (ii) the *local diameter*, i.e., the maximum finite distance from any program state to any thread-local state, and (iii) the computational complexity of finding bugs. We prove that all these are majorized by polynomials in *n* and, in certain cases, by linear, logarithmic, or even constant functions in *n*; we make the bounds explicit whenever possible. Our results shed new light on the widely expressed claim that one of the major obstacles to analyzing parallel programs is the exponential state explosion in the number of threads.

Keywords: multithreading, concurrency, transition graph, diameter, asynchronous execution, shared-memory communication, interleaving, complexity, exponential blow-up, state-space explosion, formal methods, threads, parallelism, operational semantics, formal languages, combinatorics, counting problems, graph theory, path problems.

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0. Introduction

Since 2004, the CPU-clock limit has been stagnating, which has immensely increased the demand for multithreading [85]. Conceptually, a multithreaded program consists of a batch of threads running in parallel; each thread can access only its private memory and the memory shared among the threads. The semantics of accessing the shared memory can be cumbersome [70, 79]; to simplify writing and analyzing multithreaded code, the code is typically written in such a way that every execution on a parallel machine can be viewed as an execution on a sequential machine that interleaves the steps of different threads. Even given this simplified framework, programming errors are widespread [45, 88], and the effects of failures can be devastating [5, 56, 73, 74]. In practice, almost every such failure can be viewed as a violation of a so-called safety property, which is, informally speaking, a property of the form "nothing bad happens in all executions."

We focus on the most basic safety properties of the form "a program state is not reachable from another program state"; any safety property can be reduced to such a state-to-state unreachability property [68]. Deciding such properties of programs in practice often incurs the infamous state-explosion problem, which is the phenomenon whereby the number of program states grows exponentially with the number of threads (hereinafter *n*) of the program analyzed [7, 19, 35, 40, 41, 76]. It is a practical "problem" because the program analyzer often runs out of resources while reasoning about these states and, much to a user's disappointment, fails to deliver a conclusive answer. In fact, reachability in finite-state multithreaded programs equipped with interleaving semantics is PSpace-complete (cf. § II.3). Since PSpace is a very robust class [81, § 8.2 and Exercise 8.4] containing a wealth of complete decision problems [46, 91], the PSpace-completeness characterization is rather unenlightening. In particular, it does not reveal too many details about the exponential blow-up with respect to *n* from a theoretical viewpoint (and the strictness of the inclusion PTime \subset PSpace is only a conjecture so far anyway).

To study the state-explosion phenomenon, we consider a parametrized setting with a variable number of threads n and two constant parameters: the size of the local memory per thread and the size of shared memory. In this setting, we ask how the following quantities asymptotically grow with n: the diameter and the local diameter (both of which we define later) and the complexity of two natural reachability problems. At the core of our work is the informal question of whether the growth is fast (as suggested by the aforementioned PSpace complexity and by the state explosion occurring in the tools) or slow (sometimes occurring in the parametrized-complexity field).

As we will show, the second case holds. For reachability tasks that can be formulated in the parametrized setting described, the aforementioned blow-up and high complexity can be asymptotically avoided; we will quantify this statement.

The variable number of threads and the bounded sizes of local memory per thread and of shared memory might be observed in several areas; here, we name three. The first of these areas is high-performance computing; we consider applications in which the threads themselves are fixed in size, whereas what changes is the number of threads executed in parallel when a program is moved from one supercomputer to another (or, to a lesser extent, from one GPU to another) or when a program goes from a test setup to a fully parallel setup. (At the time these lines were written, Web search [36] returned numerous occurrences of "char thread_id;" and "char threadId;" in real-world C code, which allocated 8 bits for the thread identifier. Such pieces of code are likely to be of limited use or even erroneous on a system with more than 256 threads.) The second area is the modeling of memory limitations in dynamic systems. (A typical bug example is a thread-identifier overflow [89]: a server starts a new thread upon a new query from a client while using fixed space for thread identifiers. After the server runs for a sufficiently long time, the thread-identifier variable overflows, wreaking havoc. Modeling dynamic thread creation in a static-threaded finite-state program would have exposed the issue.) The third area is the modeling of Edge Computing, in which parallel computations (e.g., in the cloud) are given to a variable number of small-size computational nodes (for instance, embedded and mobile devices) which access a single server. For the purpose of modeling, the small nodes can be viewed as threads, the server can be viewed as shared memory, and various types of message passing can be replaced with the interleaving shared-memory semantics.

To proceed, let us introduce some terminology. Intuitively, a *program state* is a valuation of all the variables of the program, including the control-flow counters of all the threads. The *distance* from a program state *s* to a program state *s'* is the minimal number of program steps needed to reach *s'* from *s* along an execution (or ∞ if *s'* is unreachable from *s*). The *diameter* of a program is the maximal finite distance present

in the program. If a bug finder outputs an error trace of the program analyzed, this trace is, in the worst case, at least as long as the program diameter. So the diameter of a program is a lower bound on the worst-case running-time of a bug finder on this program. At the same time, the diameter is equal to the number of steps that an ideal search (i.e., a search equipped with an oracle for the exact heuristic) would take to travel from a source program state to a target program state if these states are furthest apart but still connected. So the diameter is an upper bound on the running time for a successful, ideal search, in which the bug finder would always choose the right walk. These lower and upper bounds also apply to the searches in program models (and not only in original programs), e.g., models produced by predicate abstraction or in the inner loops of counterexample-guided abstraction refinement (CEGAR) schemes.

We are interested in the worst-case diameter among all the programs with the same number of threads (recall that the sizes of shared and local memory are fixed; to simplify our analysis, we assume that all threads have equally many local states). Thus, we concentrate on the function that, given a natural number n, returns the largest diameter over all programs with n threads. We call this function diamax. To the best of our knowledge, nothing is known about this function yet (except [60, 62, 64]). Certainly, diamax is majorized by the size of the state space, which is singly exponential in n. In a more general context, the conventional wisdom so far has been that the dependence between the worst-case distances in the transition graph of a (not necessarily multithreaded) program and the number of the components of the program (whether these are variables or threads) is exponential. In place of accepting this informal, conventional piece of wisdom, we prove a (much better) linear lower bound and a polynomial upper bound for multithreaded programs. Further, we demonstrate a stronger, linear upper bound for a certain subclass of programs. Moreover, we prove that, for a rather general class of probability distributions, the diameter of a random program is asymptotically almost surely at most linear. We also show that the program-state-to-program-state (non-)reachability problem belongs to the complexity class $NSpace(\log n)$.

As the above notion of the (maximal) diameter is based on the program-state-toprogram-state distance, this notion targets "nonlocal" properties concerning more than one thread, such as deadlock freedom or mutual exclusion. Still, many interesting nonreachability properties (of, for instance, real operating-system code) are "local," meaning that they are, simply put, of the following form: "a particular state of a given thread does not occur in any execution" (hereinafter, a state of a thread, shortly, a *thread state*, stands for a valuation of all the variables that the thread can access directly, including its control-flow counter). Such a property could, e.g., be specified by an **assert** statement of the programming language C (such a statement can refer to the shared and the thread-own variables). Moreover, program transformations and modeling may turn local properties into nonlocal ones or vice versa, sometimes producing intricate objects, e.g., internal models generated by automatic CEGAR loops.

Therefore, we also consider a related notion, the so-called *local diameter*. Roughly speaking, the local diameter of a program is the length of the shortest counterexample to any of the worst (i.e., hardest to refute, but still refutable) local safety properties. (A formal definition appears in § II.2.) If a bug finder outputs an error trace to a thread state, this output is, in the worst case, at least as long as the local diameter of the program. So the local diameter of a program is a lower bound on the worst-case time for finding local bugs in that program. At the same time, the local diameter is the number of steps that an ideal search (i.e., a search equipped with an oracle for the exact heuristic) would take to arrive from a source program state at a target thread state of a target thread, target thread

state). So the local diameter is an upper bound on the running time for the successful, ideal thread-state search in which the bug finder always chooses the fastest walk. Again, these lower and upper bounds also apply to searches for local bugs in program models (and not only in original programs), e.g., the models produced by predicate abstraction or in the inner loops of the CEGAR schemes.

We show that the maximum local diameter for *n*-threaded programs is bounded above by a value independent of *n*, and that the least upper bound can be explicitly constructed (meaning, we can actually write, to any level of detail, an algorithm computing the least upper bound). Though it is possible to derive the boundedness through a careful interpretation and extension of results in the literature (cf. § I), explicit computability is not immediate. In contrast to this, we propose a mostly self-contained proof in § IV, parts of which are general enough to be potentially reusable in other contexts. Moreover, we show that the program-state–to–thread-state (non-)reachability problem belongs to the complexity class NC¹ [81, Definition 10.38] (again, considering the shared and thread-local memories bounded).

The fact that the lower bound on the diameter asymptotically exceeds the upper bound on the local diameter is, to the best of our knowledge, among the strongest non-algorithmic, asymptotic, formal arguments supporting the conventional wisdom that local safety properties are easier to deal with than nonlocal ones.

Summarizing, our *major contributions* are as follows:

- The definitions of the diameter and the local diameter of a multithreaded program (extending [64]).
- Constructively bounding the maximum local diameter from above for the parametrized case by an explicitly computable value independent of *n* (Definition and Corollary IV.3.8).
- Bounding diamax between a linear and a polynomial function in general (Theorems V.1.3 and V.2.1.22). The polynomial is of lower degree than previously known ones (Note V.2.1.23).
- A class of programs for which we show a linear upper bound in *n* on the diameter (Theorem V.2.2.3), and which matches the lower bound in certain cases (Note V.2.2.5).
- For rather general probability distributions on thread transitions, the diameter of a program is asymptotically almost surely at most linear in *n* (Theorem V.2.3.2).
- Deciding whether a program state is reachable from another program state is possible in NSpace(log *n*) (Theorem VI.1).
- Deciding whether a state of a specified thread is reachable from a program state is possible (assuming that *n* is the only variable parameter) in NC¹ (Theorem VI.2).

The picture would be imperfect without reviewing a contrasting result in our context: (non-)reachability for finite-state interleaving-semantics multithreaded programs is PSpace-complete in the whole input length, i.e., for the unparametrized case, with respect to logspace reductions (Corollary II.3.3).

We conclude by discussing the growth of the diamax function (§ VII), including the theoretical benefits of its low growth to verification and bug finding.

Limitations

First, it is not the goal of this paper to empirically measure or directly improve contemporary techniques for verification or finding bugs. Rather, the paper contributes to the *classification* of the asymptotic complexity of search, with and without an oracle for the exact heuristic, in the parametrized setting in which the contribution of the variable number of threads n is singled out. For this purpose, we measure the distances

in the transition graphs and use traditional complexity classes. (The classification in the parametrized-complexity class hierarchy is a separate topic by itself and is thus relegated to a later report.) Since we are interested in the dependency on n, the expressions that do not depend on n ("constants," especially those hidden in the asymptotic notation) are of minor importance here. Still, we sometimes get these constants for free and, as a service to the interested reader, we track or even optimize them if the corresponding proof methods easily permit this. Whether these constants are "optimal enough" (which would depend on the purpose and is often subjective) is not essential for our classification efforts: tooling and practicable techniques are orthogonal to the goals of this paper.

Second, it is not our intention to consider program families created from thread templates for which the sizes of shared and thread-local state spaces depend on parameters such as the number of threads n (e.g., [87]). For thread-template–based families, there is no standard dependency of the sizes of shared and thread-local state spaces on n (depending on the example, the dependency may not exist [65, §§ 7, 8] or be anywhere between linear [66, Example 13] and, for instance, $n^{O(n)}$ [92]). Moreover, some parametrized programs (e.g., Readers-Writers) come with two or more independent variable parameters which together determine n and the sizes of shared and local state spaces. Investigations of such families would necessarily be more family-specific; results obtained for one family may not transfer to others. In contrast, this paper aims to deal with only one variable parameter and generically with the whole class of multithreaded programs rather than with particular families of multithreaded programs.

Third, the main purpose of §§ V.2.2 and V.2.3 is not large applicability areas (though we mention certain application scenarios in the two sections). The main purpose is, rather, a demonstration that the possibility of the existence of an elementary, explicit, and low-degree polynomial, even a linear one, as a general upper bound on diamax cannot be excluded.

I. Related work

In the narrowest sense, the complexity of deciding reachability in finite-state sharedmemory multithreaded programs equipped with interleaving semantics has to the best of our knowledge not yet been determined with a formal proof. The (presumably) closest-related result published with a formal proof is the PSpace-completeness of the emptiness of the intersection of regular languages given by finite automata [51, Lemma 3.2.3]. The author claimed the relevance of the result on regular-language intersection as early as 2006 (cf. [66, 67]) and stated the PSpace-completeness of deciding reachability in multithreaded programs in 2010 (cf. [59]) without proof. Some other researchers mentioned vaguely the same result without proof (we are aware only of later claims, e.g., [11, p. 4]) or stated related results with proofs (e.g., on LTL for concurrent programs [82]).

As for singling out the contribution of the number of threads n, [58, p. 126] already observed that "symmetrization reduces the growth of [the description of the transition graph] from exponential with respect to [n] to polynomial with respect to [n]." To our knowledge, the polynomial degree has never been determined precisely in general; that is where we step in. In the special case that all the threads of a program execute the same code, it has been observed [9, 10, 28] that the symmetrized program has roughly n^L program states, where L is the number of the local states.

In a wider sense, a concise review of the reachability in parametrized concurrent programs based on only one template is given in [29, § 3.2]. Relaxing the communication

model leads us to [20, 83, 84] (for rendezvous communication) or [13] (for token ring) among replicated processes.

Our results on local reachability are grounded in the finite-basis and Petri-net theories [39, 48]. The set of programs can be seen as a single well-structured transition system [1], yielding the decidability of thread-state reachability via a constant bound on the distances to a target state. However, [1] fixes the target state (not the initial state) and is formulated abstractly rather than in terms of shared-memory multithreaded programs equipped with interleaving semantics; therefore, additional work has to be invested in order to prove that the bound does not depend on the state we commence with (whether initial or target; both depend on *n* themselves), to transfer their results into the local-diameter graph-theoretic terminology for programs, and to prove the explicit computability of the local diameter.

Related practical reachability deciders employ symmetry reductions, which work well if n > L, and counter abstractions of various kinds. Symmetrization is possible, e.g., in the context of plain search [26] or in combination with a CEGAR loop and context inference [38]. Introducing counters is the main way symmetrization is implemented; sometimes the counter is abstracted to reduce the size of the state space [72]. For parametrized programs, cutoff techniques are applied. For example, [27] considers a computation model in which multiple copies of multiple process templates are concurrently executed, the shared state is absent, but each process executes specifically guarded transitions of restricted forms depending on the local state of other processes. Dynamically detecting cutoffs for identical copies of a single thread template starting in a fixed set of initial states is possible [47]; searching in the parallel composition of infinitely many copies can be reduced to searching in the parallel composition of a finite number of copies. The authors of [47] do not generalize their technique to copies of more than one template in their paper but mention that such generalizations are possible. A (non-counterexample-based) abstraction refinement for parametrized multithreaded programs consisting of copies of one thread is described in [2]. Assuming a bounded number of shared and local states as well as thread-local error states, we conjecture that the number of refinements from [2, Theorem 1] and the cutoff from [47] can roughly be bounded from above by the maximum local diameter. Searching in multithreaded programs can also be reduced to Petri nets [93, p. 14].

In general, all practical reachability deciders face the state-space-explosion problem (in the sense that the state space blows up exponentially with the number of threads); they exhaust space or time limits in a tool-dependent way or return an inconclusive answer. (However, on a set of carefully chosen templates, the tools performing symmetrization sometimes achieve exponential reductions.) We see claims such as, "The main obstacle to finite-state verification of concurrent systems is the state explosion problem: the number of states a concurrent system can reach is, in general, exponential in the number of concurrent processes in the system" [80] and "For fundamental reasons, we cannot avoid the exponential explosion in the number of threads" [32] spread throughout research-level texts. Such claims, taken literally, lead to asking whether the variable number of threads is in the exponent of a mathematical expression related to the computational difficulty of a reachability problem, rather than to the running time of a particular tool or to the size of a particular representation of the state space. This question is resolved in this paper in the negative, thereby positively resolving Open Problem 1.12.3 in [59]. We also show that several other straightforward mathematical formulations of the above quotations are all embarrassingly wrong: in these formulations, n is never in the exponent of the expressions obtained.

We mention five works which are closest to this paper. The first of these is [64],

which handles most topics of the present paper for the special case of binary programs (a *binary* program is a program with two shared states and two local states). The present paper eliminates the restriction to binary programs, considering *all* finite-state multithreaded programs equipped with interleaving semantics. Further, all results in this paper, when restricted to the binary case, are at least as strong as, or stronger than, those of [64]. The second work is [63], which is [64] with all proofs. Being a generalization, our paper intentionally follows [63, 64] concerning the structure, terminology, and certain examples and claims. The reader might find similar formulations which are, unless otherwise stated, to be interpreted here in the parametrized context rather than in the binary one.

The third work, [60], is the conference version of this very paper and its direct precursor. The present paper enhances and sharpens [60]:

- This paper contains all low-level details and background material, whereas [60] contains a high-level, abridged presentation.
- We prove the PSpace-completeness of reachability in the unparametrized case w.r.t. the logspace reductions in Corollary II.3.3, whereas [60] only refers to [51, Lemma 3.2.3].
- We tighten the upper bound on diamax in Theorem V.2.1.22 by improving the degree by the factor of *L* compared to [60, Thm. IV.2.1.10] (Theorem IV.2.1.13 in the technical report accompanying [60]).
- We strengthen the upper bound on the diameter of programs from the special class in § V.2.2 compared to [60, § IV.2.2].
- We slightly extend the probabilistic analysis in § V.2.3 compared to [60, § IV.2.3].
- The fourth work is an ongoing experiment [62] which motivates the research presented. The fifth work, [61], is a short, higher-level version of this very paper containing proof ideas rather than proof details.

Broadly speaking, our solutions have been partially inspired and influenced by the insights from symmetry reduction [28], process replication [84], finite-base arguments [39], vector addition systems and Petri nets [55], counter abstraction [72], low-complexity arguments in case the shared memory cannot read-and-write as a single atomic operation [30], pattern-based verification [31], linear interfaces [52], and other research directions [6, 15, 16, 24, 25, 71].

Outside formal methods, the diameter is sometimes defined as the maximal distance, being infinity for not strongly-connected graphs [90], whereas we consider the maximal *finite* distance, since the transition graphs need not be strongly connected. Defined more widely, the notion of graph diameter is important in, e.g., [3] (standard algorithms on graphs), [17] (an old survey on diameter-related problems mostly for undirected graphs), [18] (spectral graph theory, restricted to strongly connected graphs), [22] (strongly connected Eulerian directed graphs without 2-cycles), [54] (networks), [0, 43] (centrality computation), and [23] (the best-case performance of the simplex method in linear programming).

II. Preliminaries

We now introduce the formal notation used throughout the paper.

II.1. General conventions

As an aid for the reader, we sometimes put an exclamation mark above the relation sign of a claim that yet has to be proven. For instance, $X \stackrel{!}{\subseteq} Y$ means: we claim $X \subseteq Y$ and

proceed to prove it.

We write $\frac{1}{2}$ to indicate a contradiction.

In a Boolean formula, the underscore _ denotes an innermost existentially quantified anonymous variable; different underscores correspond to different variables, e.g., " $\varphi(_, 1) \land \psi(_, 0)$ " means " $(\exists x : \varphi(x, 1)) \land (\exists x : \psi(x, 0))$." Similarly, in textual claims that are not formally typeset as formulas, an underscore denotes a value that does not have to be specified in the context. For example, "the triple is of the form $(0, _, _)$ " means "there are l_1 and l_2 such that the triple is $(0, l_1, l_2)$."

We write \mathbb{N}_+ for the set of positive integers, $\mathbb{N}_{\geq 0}$ for the set of nonnegative integers, \mathbb{Q} for the set of rationals, and $\mathbb{Q}_{\geq 0}$ for the set of nonnegative rationals. Unless otherwise stated, the implicit universe of variables is $\mathbb{N}_{\geq 0}$. To simplify the notation, we view natural numbers as ordinals, so $\forall i, j: i < j \Leftrightarrow i \in j$. (We thus escape additional notation for the set of thread identifiers such as Tid often seen in the literature [33]: Tid gets unnecessary, since the number of threads *n* can be viewed as the set of thread identifiers $\{0, \ldots, n-1\}$. Moreover, formulas such as " $\forall j \in n \setminus \{i\}: \ldots$ " are simpler than " $\forall j \in \mathbb{N}_{\geq 0}: (j < n \land j \neq i) \Rightarrow \ldots$ ")

Our map-constructor is right-associative, meaning that $X \rightarrow Y \rightarrow Z$ (where X, Y, and Z are variables representing arbitrary sets) is read as $X \rightarrow (Y \rightarrow Z)$, which is the set of functions mapping each element of X to some function from $Y \rightarrow Z$. Maps are sometimes written in λ -notation [8]; e.g., $\lambda x \in X$. $\lambda y \in Y$. z is a particular element of $X \rightarrow Y \rightarrow Z$ assuming $z \in Z$. We write $X \rightarrow Y$ for the set of partial maps from X to Y, $X \rightarrow Y$ for the set of injective (in other terminology, one-to-one) maps from X to Y, $X \rightarrow Y$ for the set of bijections (in other terminology, one-to-one correspondences) from X to Y. The inverse of a bijection f is written as f^{-1} .

Given a partial map $f: X \dashrightarrow Y$, we write

dom $f \stackrel{\text{def}}{=} \{x \in X \mid \exists y \in Y \colon (x, y) \in f\}$ and $\operatorname{img} f \stackrel{\text{def}}{=} \{y \in Y \mid \exists x \in X \colon (x, y) \in f\}$

for the *domain* and *image* of f, respectively.

Given a map f and some elements a, b, we write $f[a \mapsto b]$ for the map which returns the value b for the argument a and behaves like f for all other arguments. Formally:

$$f[a \mapsto b] \stackrel{\text{def}}{=} \lambda x \in (\text{dom } f) \cup \{a\}. \begin{cases} f(x), & \text{if } x \neq a, \\ b, & \text{if } x = a. \end{cases}$$

For finite functions mapping to some set of numbers (naturals, rationals, ...), $\|\cdot\|_1$ denotes the 1-norm and $\|\cdot\|_{\infty}$ the maximum norm: $\|f\|_1 \stackrel{\text{def}}{=} \sum_{i \in \text{dom } f} |f(i)|$ and $\|f\|_{\infty} \stackrel{\text{def}}{=} \max\{|f(i)| \mid i \in \text{dom } f\}$, where $\max \emptyset \stackrel{\text{def}}{=} 0$.

For a sequence σ with an index set I and $i \in I$, we mostly use the right subscript or the right superscript in square brackets to write the i^{th} element as σ_i or $\sigma^{[i]}$; the whole sequence is written as $(\sigma_i)_{i \in I}$ or $(\sigma^{[i]})_{i \in I}$, respectively. If the index set is some initial segment of natural numbers, we may write i < n or $i \leq n$ instead of $i \in n$ or $i \in n+1$ in the right subscript position of the closing paren; the version with the weak inequality " \leq " additionally implies the nonemptiness of σ . If the index set of a sequence σ is a product $I \times J$ for some sets I and J, we sometimes opt for double indexing, writing the $(i, j)^{\text{th}}$ element $\sigma_{(i,j)}$ as $\sigma_i^{[j]}$ and $(\sigma_i^{[j]})_{\substack{i \in I \\ j \in J}}$ for σ . The context determines which notation is most convenient.

The *cardinality* of a sequence is the number of elements in it:

$$|(s_i)_{i < k}| \stackrel{\text{def}}{=} k$$
.

For an ordinal *n* and a set *X*, the set of *n*-tuples over *X* is denoted by X^n , which is simply another expression for $n \rightarrow X$. Thus we maintain the convention that the indexes of the components of a tuple start with 0.

By a slight abuse of notation, a plain number in the right superscript position of a symbol denotes the power of that symbol (where the multiplication operation is understood from the context), e.g., $3^2 = 9$ or $X^3 = (X \times X \times X)$.

For a set *X*, we write $\operatorname{id}_{X} \stackrel{\text{def}}{=} \{(x, x) \mid x \in X\}$ for the identity relation on *X*.

Given binary relations $f \subseteq X \times Y$ and $g \subseteq Y \times Z$, we write $g \circ f$ to denote the *right composition* "*g* after *f*," which is the relation

$$g \circ f \stackrel{\text{def}}{=} \{(x, z) \mid \exists y \colon (x, y) \in f \land (y, z) \in g\}.$$

"Right" refers to the fact that the right symbol is applied first. In a context where f and g are even maps, $g \circ f$ is called the *functional composition* of g and f.

If \rightarrow is a binary relation, we write \rightarrow^* for its reflexive-transitive closure (on a set taken from the context).

A *preorder* on a set *X* is a binary relation \leq on *X* such that \leq is reflexive on *X* and transitive. A *preordered set* is a pair (X, \leq) of a set *X* and a preorder \leq on *X*. A function $f: X \rightarrow X$ on a preordered set (X, \leq) is *antitone* iff $\forall x, y \in X: x \leq y \Rightarrow f(y) \leq f(x)$.

Given an equivalence relation \sim on a set X, we write

$$[x]_{\sim} \stackrel{\text{def}}{=} \{ y \in X \mid x \sim y \}$$

for the equivalence class of x with respect to $\sim (x \in X)$ and X_{\sim} for the set of equivalence classes. The *index of an equivalence relation* is the number of the equivalence classes.

The term $\log x$ denotes the logarithm of x in base 2.

II.2. Program notation

For $n \in \mathbb{N}_+$, an (*n*-threaded) program is a tuple

$$p = (\mathsf{Glob}, \mathsf{Loc}, \to_0, \dots, \to_{n-1}) \tag{1}$$

such that Glob and Loc are finite nonempty sets and

$$\forall i < n: \rightarrow_i \subseteq (\mathsf{Glob} \times \mathsf{Loc})^2.$$

(Adapted from [34].) Such a program is called *binary* iff |Glob| = |Loc| = 2. Elements of Glob are called *shared states* (also called *global states*), elements of Loc *local states*, elements of Glob×Loc *thread states*, elements of $\bigcup_{i < n} \rightarrow_i$ thread transitions. A program state is an element of

State
$$\stackrel{\text{def}}{=}$$
 Glob × Locⁿ.

For $(g, l) \in$ State, we call g its *shared part* and l_i its *i*th *local part* (i < n). The *transition* graph induced by p is a directed graph (State, \rightarrow) where

$$(g,l) \longrightarrow (g',l') \stackrel{\text{def}}{\longleftrightarrow} \exists i < n \colon ((g,l_i) \to_i (g',l'_i) \land \forall j \in n \setminus \{i\} \colon l_j = l'_j)$$

for all $(g, l), (g', l') \in State$. A *walk* in the graph is a sequence $(s_i)_{i < m}$ of states connected by program transitions, i.e., such that $s_i \longrightarrow s_{i+1}$ for all *i* with i+1 < m; a *path* is an injective walk, i.e., a walk $(s_i)_{i < m}$ satisfying $\forall i, j \in m : s_i = s_j \implies i = j$ (adapted

from [54, 90]). The *length* of a nonempty walk is the number of times the walk takes an edge of the transition graph:

$$\operatorname{length}((s_i)_{i \leq m}) \stackrel{\text{def}}{=} m$$

where " \leq " in the subscript imposes an implicit requirement that the walk is nonempty, containing s_0 . The *distance* from a program state *s* to a program state *s'* in the transition graph is the length of a shortest walk from *s* to *s'* (or infinity, if *s'* is unreachable from *s*):

$$d(s,s') \stackrel{\text{der}}{=} \min\{m \mid \exists \text{ walk } (s_0,\ldots,s_m) \text{ in } (\mathsf{State},\longrightarrow) \text{ such that } s_0 = s \land s_m = s'\}$$

where $\min \emptyset \stackrel{\text{def}}{=} \infty$. The *local distance* from a program state *s* to a thread state (g, a) of a thread *i* in the transition graph is the length of the shortest walk from *s* to a program state with local part *a* of thread *i* and shared part *g* (or infinity, if no such walk exists):

$$\mathsf{d}^{\mathsf{loc}}(s, i, (g, a)) \stackrel{\text{def}}{=} \min \left\{ m \left| \begin{array}{c} \exists \, l \in \mathsf{Loc}^n, \, \mathsf{walk} \, (s_0, \dots, s_m) \text{ in } (\mathsf{State}, \longrightarrow) : \\ l_i = a \, \land \, s_0 = s \, \land \, s_m = (g, l) \end{array} \right\},$$

where again min $\emptyset \stackrel{\text{def}}{=} \infty$.

From now on, if the program referred to in the above definitions is unclear from the context, we add a right subscript to specify the program; e.g., $d_p(...)$ is the distance in program p, and $d_p^{\text{loc}}(...)$ is the local distance in program p.

II.3. PSpace-completeness of (unparametrized) reachability w.r.t. logspace reductions

Now we are going to show that reachability of program states and thread states in a multithreaded program (in the full, unparametrized case) is PSpace-complete with respect to logspace reductions.

(For the sake of clarity: no proofs of the PSpace-completeness of the above problems have been published so far to the best of our knowledge, but similar results have become folklore (cf. \S I) at least as far as the schoolbook definition of PSpace-completeness via polynomial-time reductions (cf. [42, \S 11] and [81, \S 8.3]) is concerned. Therefore, \S II.3 is merely provided for purpose of the logical consistency of the paper as a service to the reader, rather than as our outstanding contribution, and may be skipped without ill effects on first reading.)

We will provide direct and full-fledged proofs. Though one way to prove this would be to reuse [51, Lemma 3.2.3], we do not do this, since Kozen leaves checking that the reduction is logspace to the reader, and further, reusing Kozen's result would provide no significant savings.

For the purpose of defining the aforementioned reachability problems as formal languages and for the sake of simplicity, we make, without loss of generality, the following pretty ordinary assumptions within this section:

- In a program, the set of shared states is a subset of $\mathbb{N}_{\geq 0}$.
- In a program, the set of local states is a subset of $\mathbb{N}_{\geq 0}$.
- A natural number is encoded in binary.
- A finite set of items (e.g., a thread transition relation) is stored as a list of items (in some order) between the braces { and }.
- The separator inside a list of items is the usual comma,.
- A tuple is stored as a list between parens ().

In particular, we let the alphabet Σ contain 0, 1, a comma, a left paren, a right paren, a left brace, and a right brace. We expect that minor variations (e.g., making the shared states and local states tuples of numbers instead of numbers) would not change the complexity result.

$$\begin{split} & \mathsf{Reach} \stackrel{\mathrm{def}}{=} \left\{ (p,s,s') \mid p \text{ is a program } \land s,s' \in \mathsf{State}_p \land \mathsf{d}_p(s,s') < \infty \right\}, \\ & \mathsf{NonReach} \stackrel{\mathrm{def}}{=} \left\{ (p,s,s') \mid p \text{ is a program } \land s,s' \in \mathsf{State}_p \land \mathsf{d}_p(s,s') = \infty \right\}, \\ & \mathsf{Reach}^{\mathsf{loc}} \stackrel{\mathrm{def}}{=} \left\{ (p,s,i,\tau) \mid \begin{array}{l} p \text{ is a program } \land s \in \mathsf{State}_p \land i < n \\ \land \tau \in \mathsf{Glob} \times \mathsf{Loc} \land \mathsf{d}_p^{\mathsf{loc}}(s,i,\tau) < \infty \right\}, \\ & \mathsf{NonReach}^{\mathsf{loc}} \stackrel{\mathrm{def}}{=} \left\{ (p,s,i,\tau) \mid \begin{array}{l} p \text{ is a program } \land s \in \mathsf{State}_p \land i < n \\ \land \tau \in \mathsf{Glob} \times \mathsf{Loc} \land \mathsf{d}_p^{\mathsf{loc}}(s,i,\tau) = \infty \right\}. \end{split}$$

First, we present an upper bound:

Lemma II.3.1. Reach, NonReach, Reach^{loc}, NonReach^{loc} ∈ PSpace.

Proof. A decider of any of the four problems initially performs the corresponding syntax check and, if the input does not have the required form, stops. So, from now on, let us assume that the syntax is valid.

For Reach and Reach^{loc}, the decider performs a nondeterministic search in the program transition graph, starting with the given source program state and nondeterministically choosing a successor at each step. Within this search, the decider maintains storage for only the current state and the auxiliary counters used for navigating within the input; the search history should not be remembered. For Reach, the decider accepts when the target program state is encountered. For Reach^{loc}, the decider accepts as soon as the target thread state of the target thread is seen.

For NonReach (or NonReach^{loc}, respectively), the decider applies Savitch's procedure [77] to search for the target program state (or the target thread state of the target thread, respectively). After the search is over, the decider accepts if no target state has been found.

Now we present a lower bound:

Lemma II.3.2. Reach, NonReach, Reach^{loc}, and NonReach^{loc} are PSpace-hard with respect to logspace reductions.

Proof. We start with Reach.

Let *L* (without loss of generality, over the same alphabet Σ) be a language decided by a Turing machine *T* in space bounded above by a polynomial function. Choose a polynomial upper bound *p* that is logspace-computable and satisfies $p(x) \ge 2$ for all $x \in \mathbb{N}_{\ge 0}$, e.g., $p = \lambda x \in \mathbb{N}_{\ge 0}$. (min $\{2^{c_1} \mid 2^{c_1} \ge x\}$)^{2^{c_2}} for constants $c_1, c_2 \in \mathbb{N}_+$. We provide a logspace reduction from *L* to Reach.

We assume that the machine T consists of the following components:

- Q: the set of states.
- $q_0 \in Q$: the starting state.
- $q_{\text{accept}} \in Q$: the accepting state.
- $\Box \notin \Sigma$: the blank symbol.
- $\Gamma \supseteq \Sigma \cup \{ \sqcup \}$: the tape alphabet.
- $\delta: (Q \setminus \{q_{\text{accept}}\}) \times \Gamma \to Q \times \Gamma \times \{L, R\}$: the transition function.

Let

Without loss of generality, we assume further that T works on a semi-infinite tape open to the right [81, p. 168]; in particular, when T tries to move the head to the left of the leftmost cell, the symbol and the control state change as usual, but the head stays at the leftmost cell (though δ indicates L). We also assume, without loss of generality, that T erases the tape right before acceptance from right to left and leaves the head above the leftmost cell (so that the accepting configuration is unique).

The reduction maps a string w to a triple (P, s, s') of a program P and two of its states s and s' such that the transition graph of P has a walk from s to s' iff T accepts w. The program P will simulate T within bounded space, s will correspond to the initial configuration, and s' will correspond to the final configuration.

Given w, the reduction would output the following program, in which the i^{th} thread simulates changes to the i^{th} cell on the tape (i < n), enumerated from 0:

•
$$n = p(|w|),$$

- Glob = $Q \times n$,
- Loc = Γ ,

•
$$\rightarrow_0 = \left\{ \left(((q,0),\gamma), ((q',0),\gamma') \right) \middle| \delta(q,\gamma) = (q',\gamma',\mathbf{L}) \right\} \cup \left\{ \left(((q,0),\gamma), ((q',1),\gamma') \right) \middle| \delta(q,\gamma) = (q',\gamma',\mathbf{R}) \right\},$$

•
$$\rightarrow_i = \left\{ \left(((q,i),\gamma), ((q',i-1),\gamma') \right) \mid \delta(q,\gamma) = (q',\gamma',\mathbf{L}) \right\} \cup \left\{ \left(((q,i),\gamma), ((q',i+1),\gamma') \right) \mid \delta(q,\gamma) = (q',\gamma',\mathbf{R}) \right\}$$
for each positive $i < n-1$,

•
$$\rightarrow_{n-1} = \left\{ \left(((q, n-1), \gamma), ((q', n-2), \gamma') \right) \mid \delta(q, \gamma) = (q', \gamma', \mathbf{L}) \right\},\$$

•
$$P = (\text{Glob}, \text{Loc}, (\rightarrow_i)_{i < n})$$

Let the source program state be $s = ((q_0, 0), w \sqcup \cdots \sqcup)$ and the target program state be $s' = ((q_{\text{accept}}, 0), _, _, _)$. (Here, the string characters are indexed from the left, starting

n times

with 0.)

A run of T from the initial configuration $q_0 w_{\sqcup \sqcup} \cdots$ to the final configuration $q_{\text{accept} \sqcup \sqcup} \cdots$ is simulated by a walk in the transition graph of P from s to s', and vice versa. The corresponding bijection maps a configuration of T of the form vqv' to the program state $((q, |v|), vv' \sqcup \cdots \sqcup)$ $(q \in Q, v, v' \in \Gamma^*$ such that |vv'| < n.

$$n - |vv'|$$
 times

Computing the reduction starts with computing |w| on the working tape, computing n, n-1, and n-2 on the working tape, and printing $(Q \times n, \Gamma, (\rightarrow_0, \ldots, \rightarrow_{n-1})), ((q_0, \ldots, \rightarrow_{n-1}))$ 0), $w \sqsubseteq \cdots \sqsupseteq$, $((q_{\text{accept}}, 0), \sqsubseteq \cdots \sqsupseteq)$ to the output tape as follows: n-|w| times n times

• Encoding a shared state $(q, i) \in Q \times n$ as a natural number can be done by first viewing q as a natural number (which is trivial, since Q is fixed) and then computing qn + i, which takes $O(\log n)$ space (again, since Q is fixed). Printing $Q \times n$ requires, in addition to the aforementioned auxiliary space for converting a single shared state, one counter to iterate from 0 till n-1.

- We can view Γ as a set of natural numbers, so each local state is a binary constant. Printing Γ amounts to printing a constant string anyway.
- Printing the transition relations requires an additional counter j on the working tape to iterate through the members of the ordinal n and space to compute $j \pm 1$ whenever the result lies in the interval [0, n-1].
- Printing the initial program state requires encoding the shared state as above, copying the input, and a counter to iterate from |w| to n 1.
- Printing the target program state requires encoding the shared state as above and a counter to iterate from 0 to *n*.

Altogether, such computations need $O(\log n)$ space on the working tape.

For Reach^{loc}, we adapt the above procedure by outputting (P, s, i, τ) for the target thread i = 0 and the thread state $\tau = ((q_{\text{accept}}, 0), \sqcup)$ instead of outputting (P, s, s').

For NonReach and NonReach^{loc}, the above is applied to a polynomial-space-bounded Turing machine accepting the complement of L instead of T (such a machine exists due to PSpace=coPSpace).

We combine Lemmas II.3.1 and II.3.2:

Corollary II.3.3. Reach, NonReach, Reach^{loc}, and NonReach^{loc} are PSpace-complete with respect to logspace reductions.

Since every logarithmic-space reduction is a polynomial-time reduction, this result implies PSpace-completeness with respect to the (slightly more schoolbook) polynomial-time reductions.

II.4. Diameter and local diameter

The *diameter* of a program is the largest finite distance realizable in the transition graph:

diam
$$(p) \stackrel{\text{def}}{=} \max((\operatorname{img} d_p) \setminus \{\infty\})$$

daf

The *local diameter* of a program is the largest finite local distance realizable in the program's transition graph:

diam^{loc}(p)
$$\stackrel{\text{def}}{=} \max((\operatorname{img} d_p^{\text{loc}}) \setminus \{\infty\}).$$

Since we are interested in the dependency of the measured quantities on *n*, we fix Glob, Loc, $G \stackrel{\text{def}}{=} |\text{Glob}|$, and $L \stackrel{\text{def}}{=} |\text{Loc}|$ for the rest of the paper, unless explicitly stated otherwise. We thus write programs such as *p* in (1) more shortly as

 $p = (\rightarrow_0, \dots, \rightarrow_{n-1})$ or, even more compactly, $p = (\rightarrow_i)_{i < n}$.

The maximal possible diameter for an *n*-threaded program is denoted by

diamax(n) $\stackrel{\text{def}}{=}$ max{diam(p) | p is an n-threaded program}.

The maximal possible local diameter for an *n*-threaded program is denoted by

diamax^{loc}(n) $\stackrel{\text{def}}{=}$ max{diam^{loc}(p) | p is an n-threaded program}.

We say that a program $h = (\sim_i)_{i < m}$ is a *subprogram* of a program $p = (\rightarrow_i)_{i < n}$ if there is an injective map $f: m \hookrightarrow n$ such that $\forall i < m: \sim_i = \rightarrow_{f(i)}$; every such map is called an *embedding* of *h* into *p*.

Lemma II.4.1. The subprogram relation is a preorder on the set of programs.

Proof. Let \mathcal{P} be the set of programs. We show two claims about the subprogram relation.

Reflexivity on \mathcal{P} . The identity $\operatorname{id}_n : n \hookrightarrow n$ is injective. Moreover, for each $(\rightarrow_i)_{i < n} \in \mathcal{P}$ we have $\forall i < n : \rightarrow_i = \rightarrow_{\operatorname{id}_n(i)}$.

Transitivity. Let $(\rightsquigarrow_0, \ldots, \rightsquigarrow_{m-1})$ be a subprogram of $(\rightarrow_0, \ldots, \rightarrow_{n-1})$ and $(\rightarrow_0, \ldots, \rightarrow_{n-1})$ be a subprogram of $(\rightarrowtail_0, \ldots, \rightarrowtail_{k-1})$. Then there are injective maps $f: m \hookrightarrow n$ and $g: n \hookrightarrow k$ such that $\forall i < m: \rightsquigarrow_i = \rightarrow_{f(i)}$ and $\forall j < n: \rightarrow_j = \rightarrowtail_{g(j)}$. Then the map $g \circ f$ is injective, and for all i < m we have $\rightsquigarrow_i = \rightarrow_{f(i)} = \rightarrowtail_{g(f(i))} = \rightarrowtail_{(g \circ f)(i)}$.

III. Examples

Before turning to the main results of this paper, we present some small examples (mostly taken from [64]). For simplicity, we show a few binary *n*-threaded programs whose diameter is diamax(n) for $n \in \{1, 2, 3\}$.

We therefore give some names to the shared and local states, e.g., $Glob = \{\alpha, \beta\}$ and $Loc = \{0, 1\}$, where $\alpha \neq \beta$ are some literals. For brevity in the binary case, we sometimes omit commas and parens when writing thread or program states: we occasionally typeset thread states $(g, l) \in Glob \times Loc$ as gl and program states $(g, (l_0, ..., l_{n-1})) \in Glob \times Loc^n$ as strings $gl_0 \cdots l_{n-1}$.

The (local) diameters of following examples are all computed directly from the definitions; double-checking the numbers is an exercise for the reader.

III.1. n = 1

Consider the program with exactly one thread and transitions $\alpha 0 \rightarrow_0 \alpha 1$, $\alpha 1 \rightarrow_0 \beta 1$, and $\beta 1 \rightarrow_0 \beta 0$. The diameter and the local diameter of this program are both 3. The transition graph of the program is depicted below right.

Since the total number of program states is 4, no single-threaded program can have a diameter larger than 3. In total, there are six programs that have diameter 3 and exactly three transitions and whose diameter-realizing paths start in $\alpha 0$.

III.2. n = 2

Now we depict some two-threaded programs of maximal diameter.

In the following tables, we omit the index at the arrows in thread transitions. In each transition graph, solid arrows constitute a shortest path from $\alpha 00$ to a state at the largest distance, dashed arrows are program transitions that do not contribute to the path, and dotted gray lines simply help to visualize the set of states as a geometric cube.

A program with a total of 5 thread transitions is depicted below.



It has diameter $7 = d(\alpha 00, \beta 11)$, and, since the total number of states in any two-threaded program is 8, no two-threaded program can have a larger diameter. Omitting any thread transition would yield a program with a lower diameter. Adding copies of existing threads does not, in general, produce programs with a maximal diameter: $diam(\rightarrow_0, \rightarrow_0, \rightarrow_1) = diam(\rightarrow_0, \rightarrow_1, \rightarrow_1) = 8$ and $diam(\rightarrow_0, \rightarrow_0, \rightarrow_1, \rightarrow_1) = 9$, which, as we will see, are less than the lower bound from Theorem V.1.3. The local diameter is 6 = $d^{loc}(\alpha 00, 0, \beta 1)$. Note that for each thread every thread state occurs twice as part of some program states (e.g., the thread state $\alpha 1$ of thread 1 occurs in $\alpha 01$ and $\alpha 11$). Thus, for any source program state, the two occurrences cannot both have distance 7 from this source: if any of these occurrences is reachable from the source, at least one of these occurrences must have distance not exceeding 6. Thus, no two-threaded program can have a local diameter exceeding 6.

We give names to programs for convenient referencing. Figures 1 and 2 depict some programs with a total of 6 and 7 thread transitions, respectively. These programs have diameter 7. Omitting any thread transition from these programs would yield programs with a lower diameter.



Figure 1: Some two-threaded binary programs with 6 thread transitions.

Adding thread transitions or copies of existing threads may change the diameter, but this is not always the case.

Let us consider some cases:

In N2T6A, adding $\alpha 0 \rightarrow_0 \beta 0$ shrinks the diameter. Instead, adding any combination of the thread transitions $\alpha 1 \rightarrow_0 \alpha 0$, $\beta 0 \rightarrow_0 \alpha 0$, and $\beta 1 \rightarrow_0 \alpha 0$ does not change the diameter. We have diam $(\rightarrow_0, \rightarrow_0, \rightarrow_1) = 7$ and diam $(\rightarrow_0, \rightarrow_1, \rightarrow_1) = 8 = \text{diam}(\rightarrow_0, \rightarrow_0, \rightarrow_1, \rightarrow_1)$. The local diameter is $6 = d^{\text{loc}}(\alpha 00, 1, \beta 1)$.

The transition graph of N2T6B is strongly connected. Adding any combination of the thread transitions $\beta 1 \rightarrow_0 \alpha 1$, $\alpha 1 \rightarrow_0 \alpha 0$, $\beta 1 \rightarrow_0 \alpha 0$, and $\beta 0 \rightarrow_0 \alpha 0$ would not change the diameter, but adding $\alpha 1 \rightarrow_0 \beta 0$ would shrink it. Duplicating the threads slightly increases the diameter: diam $(\rightarrow_0, \rightarrow_0, \rightarrow_1) = \text{diam}(\rightarrow_0, \rightarrow_1, \rightarrow_1) = 8$ and diam $(\rightarrow_0, \rightarrow_0, \rightarrow_1, \rightarrow_1) = 9$. The local diameter is $6 = d^{\text{loc}}(\alpha 00, 1, \beta 0)$.

In N2T6C, adding any combination of the transitions $\alpha 1 \rightarrow_0 \alpha 0$, $\beta 0 \rightarrow_0 \alpha 0$, and $\beta 1 \rightarrow_0 \alpha 0$ would shrink the diameter. Adding the thread transition $\beta 1 \rightarrow_0 \alpha 1$ would not

change the diameter. We have diam $(\rightarrow_0, \rightarrow_0, \rightarrow_1) = 7$, diam $(\rightarrow_0, \rightarrow_1, \rightarrow_1) = 8$, and diam $(\rightarrow_0, \rightarrow_0, \rightarrow_1, \rightarrow_1) = 9$. The local diameter is $5 = d^{\text{loc}}(\alpha 00, 1, \beta 0)$.



Figure 2: Some two-threaded binary programs with 7 transitions.

The transition graph of N2T7B is strongly connected. Three paths realize distance 7: $\alpha 00 \longrightarrow^* \alpha 01$ (denoted by thick arrows), $\alpha 01 \longrightarrow^* \beta 00$, and $\beta 01 \longrightarrow^* \alpha 00$. Adding any combination of $\alpha 1 \rightarrow_0 \alpha 0$, $\beta 1 \rightarrow_0 \alpha 0$, and $\beta 0 \rightarrow_0 \alpha 0$ would retain the diameter 7, but the only path realizing the longest distance would be $\alpha 01 \longrightarrow^* \beta 00$. Instead, adding $\beta 0 \rightarrow_1 \beta 1$ would keep all three paths realizing the longest distance. Thread duplication increases the diameter in some cases: diam $(\rightarrow_0, \rightarrow_0, \rightarrow_1) = 7$ and diam $(\rightarrow_0, \rightarrow_1, \rightarrow_1)$ = 8 = diam $(\rightarrow_0, \rightarrow_0, \rightarrow_1, \rightarrow_1)$. The local diameter is 6 = d^{loc}($\alpha 01, 1, \beta 0$).

III.3. n = 3

Consider the program N3T9 from Figure 3.

Thread 0:	Thread 1:	Thread 2:
$\alpha 0 \leftarrow \alpha 1$	$\alpha 0 \rightarrow \alpha 1$	α0 α1
$\beta 0 \longrightarrow \beta 1$	β0 β1	$\beta 0 \rightarrow \beta 1$

Figure 3: A three-threaded binary program.

Its transition graph is depicted in Figure 4.

This program has local diameter $9 = d^{loc}(\alpha 000, 2, \alpha 1)$ and diameter $13 = d(\alpha 000, \beta 011)$. The transition graph is strongly connected. Adding a copy of each thread would shrink the diameter: diam $(\rightarrow_0, \rightarrow_0, \rightarrow_1, \rightarrow_1, \rightarrow_2, \rightarrow_2) = 11$. It can be shown (cf. [63, Theorem 6.2.1.1] or [64, Theorem 5.1.1]) that N3T9 has the maximal diameter among all three-threaded binary programs.

IV. Local diameter

In this section we prove that the local diameter is bounded above by a constant independent of the number of threads, and that the least upper bound is computable by an explicit algorithm. Informally, this means that the shortest counterexamples to violated local safety properties are short, and, given enough computational resources, we can even say how short. In theory, a constant bound is far better than the trivial bound diamax^{loc}(*n*) \leq diamax(*n*) for all $n \in \mathbb{N}_+$.

(This section represents a strengthening and generalization of the corresponding result from [64].)



Figure 4: The transition graph of N3T9 representing a path realizing the longest distance.

From an intuitive standpoint, the proof relies on the observation that the local-distance function is antitone in the program argument (i.e., roughly speaking, adding threads to a program reduces the local distances), as well as on any of the known coverability procedures for Petri nets.

We start with well-known order-theoretic background, continue with new ordertheoretic results, and finally apply what we learned to programs.

IV.1. Preliminaries from general order theory

Informally, an antichain is a preorder in which no elements are comparable. More formally, given a preordered set (P, \leq) , a subset $A \subseteq P$ is called an *antichain* iff $\forall x, y \in A$: $(x \leq y \lor y \leq x) \Rightarrow x = y$.

Given an arbitrary set *I* and arbitrary posets (P_i, \leq_i) for $i \in I$, the *componentwise* partial order on the product $\prod_{i \in I} P_i$ is given by

$$x \leq y \quad \stackrel{\text{def}}{\Leftrightarrow} \quad \forall i \in I \colon x_i \leq_i y_i \qquad (x, y \in \prod_{i \in I} P_i).$$

We call a map $\varphi \colon X \to Y$ between preordered sets (X, \leq_X) and (Y, \leq_Y)

- an order-homomorphism iff $\forall x_1, x_2 \in X : x_1 \leq x_2 \Leftrightarrow \varphi(x_1) \leq \varphi(x_2)$,
- an *order-epimorphism* iff φ is a surjective order-homomorphism, and
- an *order-isomorphism* iff φ is a bijective order-homomorphism. (In general, an isomorphism is defined as a bijective homomorphism such that its inverse is also a homomorphism [21]. In the case of order-isomorphisms, both definitions are equivalent; cf. [14, Lemma 2.2].)
- If a bijective order-homomorphism between preordered sets (X, \leq_X) and (Y, \leq_Y) exists, we call these preordered sets *order-isomorphic* [37, p. 14, Ch. 0].

The following popular result is essential for the whole section:

Proposition IV.1.1 (Dickson's Lemma). Let $m \in \mathbb{N}_{\geq 0}$. Let the set $\mathbb{N}_{\geq 0}^m$ of m-tuples of natural numbers be equipped with componentwise partial order over the standard order on natural numbers. Then every antichain in $\mathbb{N}_{\geq 0}^m$ is finite.

This is a special case of Hilbert's basis theorem (cf. Chapitre III, § 2.10, Corollaire 2 in [12]). We give an independent proof below.

Proof. We prove the statement by induction on *m*. Let $m \in \mathbb{N}_{\geq 0}$ be arbitrary, and assume that for every l < m, all antichains in $\mathbb{N}_{\geq 0}^l$ are finite.

Case $m \in \{0, 1\}$. By definition of the empty product, $|\mathbb{N}_{\geq 0}^{0}| = 1$, and $\mathbb{N}_{\geq 0}^{1}$ is orderisomorphic to $\mathbb{N}_{\geq 0}$. Thus, every antichain in $\mathbb{N}_{\geq 0}^{m}$ is either empty or a singleton. Case $m \geq 2$. Let $A \subseteq \mathbb{N}_{\geq 0}^{m}$ be a nonempty antichain. Notice that for each $k \in \mathbb{N}_{\geq 0}$

Case $m \ge 2$. Let $A \subseteq \mathbb{N}_{\ge 0}^m$ be a nonempty antichain. Notice that for each $k \in \mathbb{N}_{\ge 0}$ and each i < m the set $\{b \in \mathbb{N}_{\ge 0}^{m-1} \mid (b_1, \dots, b_{i-1}, k, b_{i+1}, \dots, b_{m-1}) \in A\}$ is an antichain in $\mathbb{N}_{\ge 0}^{m-1}$, hence finite, so $A_{i,k} \stackrel{\text{def}}{=} \{a \in A \mid a_i = k\}$ is finite. Since A is nonempty, some $a \in A$ exists. Let $B = \bigcup \{A_{i,k} \mid i < m \land k \le a_i\}$. Then B is finite. If A were infinite, some $c \in A \setminus B$ would exist. By construction, a is less than c in the componentwise partial order on $\mathbb{N}_{\ge 0}^m$.

IV.2. Well-foundedness and antitone maps

We recall that a partial order \leq on a set *X* is *well-founded* iff each nonempty subset of *X* has a minimal element, i.e., $\forall Y \subseteq X \colon (Y \neq \emptyset \Rightarrow \exists y \in Y \forall z \in Y \colon (z \leq y \Rightarrow z = y))$. The following fact is also well known:

The following fact is also well known:

Proposition IV.2.1. For each $m \in \mathbb{N}_{\geq 0}$, the componentwise partial order on $\mathbb{N}_{\geq 0}^m$ is well-founded.

Proof. We write \leq for the componentwise partial order on $\mathbb{N}_{\geq 0}^m$. Let $Y \subseteq \mathbb{N}_{\geq 0}^m$ be nonempty. Then there is some $y \in Y$ with the minimal 1-norm. Let $z \in Y$ be arbitrary such that $z \leq y$. Then $z_i \leq y_i$ for all $i \in m$. Assume for the purpose of contradiction that $z \neq y$. Then there is some $j \in m$ such that $z_j \neq y_j$. Then $z_j < y_j$. Then $||z||_1 < ||y||_1$, in contradiction to the choice of y. Thus, our assumption was wrong and z = y.

We combine this fact with Proposition IV.1.1:

Lemma IV.2.2. Let $\mathbb{N}_{\geq 0}$ be equipped with the standard order on natural numbers. Let $m \in \mathbb{N}_{\geq 0}$, the set $\mathbb{N}_{\geq 0}^m$ be equipped with componentwise partial order, and $f : \mathbb{N}_{\geq 0}^m \dashrightarrow \mathbb{N}_{\geq 0}$ be an antitone partial map. Then:

a) img f is finite.

- b) Assume that explicit algorithms solving the following problems exist:
 - 1) Decide, given a tuple of pairs $(s_i, a_i)_{i < m} \in (\{`=`, `\geq`\} \times \mathbb{N}_{\geq 0})^m$, whether some $y \in \text{dom } f$ exists satisfying $\forall i < m : ((s_i = `=` \land y_i = a_i) \lor (s_i = `\geq` \land y_i \geq a_i)).$
 - 2) Evaluate f at a point of its domain.

Then there is an explicit algorithm determining whether img f is empty or not, and, in case of nonemptiness, computing max(img f).

Proof. a) If *f* is the empty partial map, its image is empty and, therefore, finite. Let us consider the case of a nonempty *f*. We write \leq for the componentwise partial order on $\mathbb{N}_{\geq 0}^m$. Let $A \stackrel{\text{def}}{=} \{x \in \text{dom } f \mid \forall y \in \text{dom } f : y \leq x \Rightarrow y = x\}$ be the set of the minimal elements of the domain of *f*. Then *A* is an antichain, hence finite by Proposition IV.1.1. The partial order \leq is well-founded by Proposition IV.2.1, and dom *f* is nonempty. By the definition of well-foundedness, *A* is nonempty. Therefore, $\{f(x) \mid x \in A\}$ is nonempty; let $M = \max\{f(x) \mid x \in A\}$. It suffices to show that *M* is the maximum value of *f*. So let $x \in \text{dom } f$ be arbitrary. Then the set $B \stackrel{\text{def}}{=} \{y \in \text{dom } f \mid y \leq x\}$ is nonempty, so it has a minimal element *z*. Now we show that *z* lies in *A*. From $z \in B$ we get $z \in \text{dom } f$. Let $y \in \text{dom } f$ be arbitrary such that $y \leq z$. From $z \in B$ we get $z \leq x$. By transitivity, $y \leq x$. From $y \in \text{dom } f$ and $y \leq x$, we obtain $y \in B$. From $y \in B$, $y \leq z$, and the minimality of *z*, we obtain y=z. We have shown $z \in \text{dom } f \land \forall y \in \text{dom } f: y \leq z \Rightarrow y=z$. Hence, $z \in A$. From $z \in A$ we obtain $f(z) \leq M$. From $z \leq x$ we obtain $f(x) \leq f(z)$. By transitivity, $f(x) \leq M$.

We have shown that img $f \subseteq (M+1)$, which is a finite ordinal.

- b) We will reuse the definitions of *A* and *M* given in the proof of part a). Notice that the algorithm from 1) allows, in particular,
 - i) deciding, given an arbitrary set $I \subseteq m$ and a sequence of pairs $(s_i, a_i)_{i \in I} \in (\{`=`\} \times \mathbb{N}_{\geq 0} \cup \{`\geq`\} \times \mathbb{N}_+)^I$, whether some $y \in \text{dom } f$ exists satisfying $\bigwedge_{i \in I} ((s_i = `=` \land I)_{i \in I})^I$.

 $y_i = a_i$ \lor $(s_i = i \ge i \land y_i \ge a_i)$ (by reusing $(s_i, a_i)_{i \in I}$ and additionally letting $s_i = i \ge i$ and $a_i = 0$ for all $i \in m \setminus I$);

- ii) deciding whether dom *f* is empty (by choosing $s_i = i \ge i$ and $a_i = 0$ for all i < m);
- iii) deciding, given a vector $a \in \mathbb{N}_{\geq 0}^m$, the membership $a \in \text{dom } f$ (by choosing $s_i =$ '=' for all i < m); and
- iv) computing any y from i) if such a y exists (by enumerating all the tuples from $\mathbb{N}_{\geq 0}^{m}$ and, for each tuple, evaluating the constraint $\bigwedge_{i \in I} (\ldots)$ and, if the constraint is

satisfied, checking for membership in dom f). Consider Algorithm 1.

Algorithm 1: Finding the maximum value of an antitone function $\mathbb{N}_{\geq 0}^m \to \mathbb{N}_{\geq 0}$ **Output:** "empty" or M Program variables: D, x, grow, F, d, y // in this case dom $f \neq \emptyset$ 1 **if** any $x \in \text{dom } f$ exists **then** 2 choose any such x; $D := \{x\};$ // D always stores an antichain in dom f3 repeat 4 5 grow := false; F := a simplification of a disjunctive normal form of 6 $\bigwedge_{a \in \mathbb{D}} \bigvee_{\substack{i,j < m \\ i \neq j}} \left(\left(\bigvee_{c < a_i} (y_i = c) \right) \land y_j \ge a_j + 1 \right);$ while $F \neq false \land F$ is a nonempty disjunction $\land \neg grow do$ 7 d := any disjunct of F;8 Remove d from F; 9 // using the algorithm from i) if $\exists y \in \text{dom } f$: d then 10 Compute any such y; 11 grow := true; 12 $D := D \cup \{y\}$ 13 until \neg grow; // after the loop, D is a maximal antichain in dom f14 return $\max\{f(a) \mid a \in \text{dom } f \land \exists b \in D: a \leq b\} // \text{ computable, finite set}$ 15 16 else return "empty" // in this case dom $f = \emptyset$

The algorithm first checks whether dom $f = \emptyset$; if so, img f is empty. In this case,

the procedure returns "empty".

Otherwise, a maximal (with respect to subset inclusion) antichain in dom f is constructed, beginning with an arbitrary element $x \in \text{dom } f$. This is done as follows. Given the current antichain stored in the container D, a formula representing the statement "variable y is incomparable with each member of D" is constructed; the equivalent disjunctive normal form (DNF) is stored in F.

During conversion to DNF, we simplify F arithmetically and logically:

- Empty disjuncts of the form $\bigvee (y_i = c)$ are replaced with false.
- Conjunctions of the form $y_i \ge c \land y_i \ge c'$ are replaced with $y_i \ge \max\{c, c'\}$ (for $c, c' \in \mathbb{N}_+$ and i < m).
- Conjunctions of the form $y_i \ge c \land y_i = c'$ are replaced with $y_i = c'$ (if $c \le c'$) or false (if c > c') (for $c \in \mathbb{N}_+$, $c' \in \mathbb{N}_{\ge 0}$, and i < m).
- Conjunctions of the form $y_i = c \land y_i = c'$ are replaced with $y_i = c$ (if c' = c) or false (if $c' \neq c$) (for $c, c' \in \mathbb{N}_{\geq 0}$ and i < m).
- The laws of absorption and annihilation (for the Boolean constant false) and associativity and commutativity are used to achieve maximal simplification.

After this simplification, in each disjunct of F, each variable y_i occurs at most once. Each resulting disjunct of F is now of the form $y_{i_1}(= o \ge) a_{i_1} \land \cdots \land y_{i_k}(= o \ge) a_{i_k}$ (for some $k \ge 1$, pairwise disjoint $i_1, \ldots, i_k < m$, and some $a_{i_1}, \ldots, a_{i_k} \in \mathbb{N}_{\ge 0}$ such that the right-hand sides of all the \ge -inequalities are positive). Among these disjuncts, we search, using i), for a disjunct that can be satisfied by some $y \in \text{dom } f$. If such a disjunct is found, we extend the antichain (stored in D) with any corresponding $y \in \text{dom } f$ and restart the outer loop from line 5. If no such disjunct is found, D already represents a maximal antichain. The outer loop, which extends D to a maximal antichain, terminates because of Proposition IV.1.1.

After the construction of a maximal antichain is finished, we notice that each $a \in A$ must be comparable to some $b \in D$ (since otherwise D could be extended to a larger antichain) and (because A contains the minimal elements of dom f) be less than or equal to this b. So $A \subseteq \{a \in \text{dom } f \mid \exists b \in D : a \leq b\}$, and the proof of a) implies that evaluating $f|_{\{a \in \text{dom } f \mid \exists b \in D : a \leq b\}}$ suffices to find the maximum of f. The evaluation of $f|_{\{a \in \text{dom } f \mid \exists b \in D : a \leq b\}}$ can be done, e.g., by looping through all $b \in D$ and, for each such b, looping through all $a \leq b$. Since we thereby particularly evaluate $f|_A$, we necessarily encounter the maximal value of f in the process.

IV.3. Application of order theory to programs

In the following, let \mathcal{P} be the set of all programs, equipped with the subprogram preorder (cf. Lemma II.4.1). In Lemmas IV.3.1 and IV.3.2, we discuss some structural properties of P.

Lemma IV.3.1. There is some $k \in \mathbb{N}_+$ and some order-epimorphism $\varphi \colon \mathcal{P} \twoheadrightarrow \mathbb{N}_{\geq 0}^k \setminus \{k \times \{0\}\}$, where the codomain is equipped with the componentwise partial order.

(Since k is an ordinal, the above notation $k \times \{a\}$ means, set-theoretically, simply $\{0, \dots, k-1\} \times \{a\} = \{(0, a), \dots, (k-1, a)\} = (a, \dots, a)$, a vector of k copies of a. If programs with no threads

were allowed, the image of
$$\varphi$$
 would include the all-zeros vector.)

Proof. Since a thread transition relation is a subset of $(\text{Glob} \times \text{Loc})^2$, there are $k \stackrel{\text{def}}{=} 2^{G^2L^2}$ different thread transition relations; we choose some enumeration t_0, \ldots, t_{k-1} of

these relations. Let

$$\varphi \colon \mathcal{P} \to \mathbb{N}_{\geq 0}^{k} \setminus \left\{ k \times \{0\} \right\}, \quad (\to_{0}, \ldots, \to_{n-1}) \mapsto \left(\left| \left\{ i < n \mid \to_{i} = t_{r} \right\} \right| \right)_{r < k}$$

be the map that, loosely speaking, counts how many copies of each of the *k* thread transition relations there are in a given program and then returns the vector of these counts. Since every program has at least one thread, and since t_0, \ldots, t_{k-1} counts all the thread transition relations, the image of φ does indeed not include the zero vector.

Now we prove the claims of the lemma. We write \leq for the componentwise partial order on $\mathbb{N}_{\geq 0}^k \setminus \{k \times \{0\}\}$.

" φ is an order-homomorphism." By definition, φ is total. Now let $p = (\rightsquigarrow_0, \ldots, \rightsquigarrow_{m-1})$ and $q = (\rightarrow_0, \ldots, \rightarrow_{n-1})$ be arbitrary members of \mathcal{P} . We are going to show:

- "If *p* is a subprogram of *q*, then $\varphi(p) \leq \varphi(q)$." Let *p* be a subprogram of *q* via an embedding $f: m \hookrightarrow n$. Let r < k be arbitrary. The restriction $f|_{\{i < m | \to_i = t_r\}}$ is still injective, and its image is a subset of $\{i < n | \to_i = t_r\}$. Hence, $|\{i < m | \to_i = t_r\}| \leq |\{i < n | \to_i = t_r\}|$. Thus, $(\varphi(p))_r \leq (\varphi(q))_r$. Since *r* was arbitrary, we obtain $\varphi(p) \leq \varphi(q)$.
- "If $\varphi(p) \leq \varphi(q)$, then *p* is a subprogram of *q*." Suppose $\varphi(p) \leq \varphi(q)$. For each r < k let $S_r = \{i < m \mid \rightarrow_i = t_r\}$ and $S'_r = \{i < n \mid \rightarrow_i = t_r\}$. By the assumption, $|S_r| \leq |S'_r|$ (r < k). Thus, for each r < k there is an injection $f_r: S_r \hookrightarrow S'_r$. We view each map f_r as a set of pairs (r < k) and define $f = \bigcup_{r < k} f_r$. Note that the sets S_r for r < k are pairwise disjoint and that $\bigcup_{r < k} S_r = m$. Thus, *f* is a mapping $m \to n$. Note that the sets S'_r for r < kare also pairwise disjoint. Thus, *f*: $m \hookrightarrow n$ is injective.

Now we prove that $\forall i < m : \rightsquigarrow_i = \rightarrow_{f(i)}$. Let i < m be arbitrary. Then there is some r < k such that $\rightsquigarrow_i = t_r$. So $i \in S_r$. Then $f(i) = f_r(i)$. From img $f_r \subseteq S'_r$ we obtain $\rightarrow_{f(i)} = t_r$. Combining, we obtain $\rightsquigarrow_i = \rightarrow_{f(i)}$.

" φ is surjective." For each nonzero vector $(a_0, a_1, \dots, a_{k-1}) \in \mathbb{N}_{\geq 0}^k$, one can always create a program with a_0 copies of t_0, a_1 copies of t_1, \dots, a_{k-1} copies of t_{k-1} . ■

Moreover, all order-epimorphisms as in Lemma IV.3.1 are of the same form:

Lemma IV.3.2. Let $\varphi: \mathcal{P} \twoheadrightarrow \mathbb{N}_{\geq 0}^k \setminus \{k \times \{0\}\}\)$ be an order-epimorphism, where the codomain is equipped with the componentwise partial order. Then $k = 2^{G^2L^2}$, and there is an enumeration t_0, \ldots, t_{k-1} of thread transition relations such that $\varphi((\rightarrow_i)_{i < n}) = \left(\left|\{i < n \mid \rightarrow_i = t_r\}\right|\right)_{r < k}$ for all programs $(\rightarrow_i)_{i < n}$. Moreover, φ is computable, and the preimage of each vector under φ is finite and computable.

Proof. In the following, we write \leq for the componentwise partial order on $\mathbb{N}_{\geq 0}^k \setminus \{k \times \{0\}\}\)$ and < for its irreflexive version. We write e^r for the unit vector $\begin{pmatrix} 0, & \text{if } i \neq r \\ 1, & \text{if } i = r \\ i < k \end{pmatrix}$.

As a preliminary step, we will show:

Claim 1: For all $n \in \mathbb{N}_+$ and $p, q \in \mathcal{P}$, if p is n-threaded and q is an (n-1)-threaded subprogram of p, then there is some r < k such that $\varphi(q) + e^r = \varphi(p)$.

To prove this, let $n \in \mathbb{N}_+$, p be an n-threaded program, and q be an (n-1)-threaded subprogram of p via some embedding f. Since φ is an order-homomorphism, $\varphi(q) \leq \varphi(p)$. If we had $\varphi(p) \leq \varphi(q)$, then p would be a subprogram of q, implying the existence of an injective map $n \hookrightarrow n-1$, $\frac{1}{r}$. Therefore, $\varphi(q) < \varphi(p)$. Thus, there is some r < k such that $\varphi(q) + e^r \leq \varphi(p)$. Since φ is onto, there is some program q' such

that $\varphi(q') = \varphi(q) + e^r$. From $\varphi(q) < \varphi(q')$ we obtain that q is a subprogram of q', and that q' is not a subprogram of q. Therefore, q' has strictly more than n-1 threads, i.e., at least n threads. From $\varphi(q') \leq \varphi(p)$ we obtain that q' is a subprogram of p via some embedding g, and, therefore, q' cannot have more than n threads. Then $g: n \hookrightarrow n$, so, g is a bijection. Therefore, p is a subprogram of q' via the embedding g^{-1} . So $\varphi(p) \leq \varphi(q')$. Therefore $\varphi(p) = \varphi(q')$, i.e., $\varphi(p) = \varphi(q) + e^r$, and Claim 1 is proven.

Now we consider another auxiliary statement:

Claim 2: φ maps single-threaded programs to unit vectors.

To prove this, assume for the sake of contradiction that some single-threaded $p \in \mathcal{P}$ is mapped to a non-unit vector. Since the zero vector is not in the image of φ , there must be i, j < k such that $i \neq j$ and $(\varphi(p))_i \ge 1 \le (\varphi(p))_j$. Then $e^i < \varphi(p)$. Since φ is onto, some $q \in \mathcal{P}$ satisfies $\varphi(q) = e^i$. From $\varphi(q) < \varphi(p)$ we obtain that q is a subprogram of p, but not vice versa. But p is single-threaded, so, q must be zero-threaded, which we explicitly excluded in § II.2. Thus, our assumption was wrong, and $\varphi(p)$ is a unit vector, which completes the proof of Claim 2.

Combining Claims 1 and 2, we obtain by induction:

$$\forall n \in \mathbb{N}_+, n \text{-threaded } p \in \mathcal{P} \colon \|\varphi(p)\|_1 = n.$$
(2)

Since there are exactly $k' \stackrel{\text{def}}{=} \left| \mathfrak{P}((\text{Glob} \times \text{Loc})^2) \right| = 2^{G^2 L^2}$ thread transition relations, there are exactly this many single-threaded programs; they are all incomparable. Thus, the φ -images of the single-threaded programs comprise exactly k' pairwise incomparable unit vectors in $\mathbb{N}_{\geq 0}^k \setminus \{k \times \{0\}\}$. Therefore, $k \geq k'$. If k were strictly greater than k', then $\mathbb{N}_{\geq 0}^k \setminus \{k \times \{0\}\}$ would have a unit vector outside the image of single-threaded programs under φ , which, given the fact that φ is onto, would contradict (2). So k=k', and the restriction of φ to single-threaded programs is a bijection to the set of unit vectors of $\mathbb{N}_{\geq 0}^k \setminus \{k \times \{0\}\}$.

For each r < k we define t_r as the unique thread transition relation for which $\varphi((t_r)_{i<1}) = e^r$. Now we are going to prove by induction that $\varphi((\rightarrow_i)_{i< n}) \stackrel{!}{=} \left(\left| \{i < n \mid \rightarrow_i = t_r\} \right| \right)_{r < k}$ for all programs $(\rightarrow_i)_{i< n}$ for all $n \in \mathbb{N}_+$.

To this end, let $n \in \mathbb{N}_+$ be arbitrary, and assume (by inductive hypothesis) that the statement is true for all positive m < n. Let $p = (\rightarrow_i)_{i < n} \in \mathcal{P}$ be arbitrary.

Case n=1. Choose s < k such that $t_s = \to_0$. Then $\varphi(p) = e^s$. The s^{th} component of e^s is $1 = |\{i < 1 \mid \to_i = t_s\}|$, and every other r^{th} component of e^s for $r \neq s$ is $0 = |\{i < 1 \mid \to_i = t_r\}|$.

Case $n \ge 2$. Let $q = (\rightarrow_i)_{i < n-1}$. The induction hypothesis implies

$$\varphi(q) = \left(\left| \{i < n-1 \mid \rightarrow_i = t_r\} \right| \right)_{r < k}.$$
(3)

By Claim 1, there is some s < k such that $\varphi(p) = \varphi(q) + e^s$. There is also some s' < k such that $\rightarrow_{n-1} = t_{s'}$. We will show that *s* and *s'* coincide.

Since the single-threaded program $(t_{s'})_{i<1}$ is a subprogram of p, we have $\varphi((t_{s'})_{i<1}) \leq \varphi(p)$, and, therefore, $e^{s'} \leq \varphi(p)$. Let $v = \varphi(p) - e^{s'}$. Note that $||v||_1 = ||\varphi(p)||_1 - 1 \stackrel{(2)}{=} n-1$. Since φ is onto, $\varphi(q') = v$ for some $q' \in \mathcal{P}$. Due to (2), q' must be (n-1)-threaded. Let $(\sim_i)_{i< n-1} = q'$. Note that $v_s = (\varphi(q'))_s = [$ induction hypothesis] $|\{i < n-1 \mid \sim_i = t_s\}|$. From $\varphi(q') < \varphi(p)$ we obtain that q' is a subprogram of p. So p contains at least v_s copies of t_s .

Now, assume for the purpose of contradiction that $s \neq s'$. Then $t_s \neq \rightarrow_{n-1}$. Together with the definition of q, we obtain that q still has at least v_s copies of *t_s*. By (3), $(\varphi(q))_s \ge v_s$. The definition of *s* implies $(\varphi(p))_s \ge v_s + 1$. The definition of *v* implies $v_s = (\varphi(p) - e^{s'})_s = (\varphi(p))_s - e^{s'}_s \ge (v_s + 1) - 0 = v_s + 1$, $\frac{1}{2}$.

Thus, our assumption is wrong, and s=s'. Therefore, $\rightarrow_{n-1} = t_s$. Then $(\varphi(p))_s = (\varphi(q))_s + 1 \stackrel{(3)}{=} |\{i < n-1 \mid \rightarrow_i = t_s\}| + 1 = |\{i < n \mid \rightarrow_i = t_s\}|$. For $r \in k \setminus \{s\}$, we have $(\varphi(p))_r = (\varphi(q))_r + 0 \stackrel{(3)}{=} |\{i < n-1 \mid \rightarrow_i = t_r\}| = [\text{since } t_r \neq t_s] |\{i < n \mid \rightarrow_i = t_r\}|$.

We have proved for all $n \in \mathbb{N}_+$ and all *n*-threaded $(\rightarrow_i)_{i < n} \in \mathcal{P}$ the equality

$$\varphi\bigl((\rightarrow_i)_{i < n}\bigr) = \left(\left|\{i < n \mid \rightarrow_i = t_r\}\right|\right)_{r < k}.$$

The map φ is computable by a loop over all r < k.

To show that the preimage of each vector under φ is finite, assume $a \in \mathbb{N}_{\geq 0}^{k} \setminus \{k \times \{0\}\}$. Each preimage $p \in \varphi^{-1}(a)$ is $||a||_1$ -threaded due to (2). For each *n* the number of *n*-threaded programs is bounded from above by k^n . Therefore, *a* has no more than $k^{||a||_1}$ preimages.

To show that the preimage of a vector under φ is computable, assume again $a \in \mathbb{N}_{\geq 0}^k \setminus \{k \times \{0\}\}$. Construct an arbitrary program $(\rightarrow_i)_{i < n}$, where $n = ||a||_1$, with a_0 copies of t_0 , a_1 copies of t_1, \ldots, a_{k-1} copies of t_{k-1} . For each permutation σ of n, construct the program $(\rightarrow_{\sigma(i)})_{i < n}$. All thus created programs together form exactly $\varphi^{-1}(a)$.

(As an aside, notice that Lemma IV.3.2 says that the order-epimorphisms from \mathcal{P} onto the set of non-all-zeros tuples of natural numbers of the same dimension are the same up to permuting the components. This is slightly more general than needed by the following proofs. We added the above characterization to show that it might be difficult to work out a real-world implementation of the algorithm of this section by reducing the dimension.)

The above preparations imply:

Theorem IV.3.3. Let $f: \mathcal{P} \dashrightarrow \mathbb{N}_{\geq 0}$ be an antitone partial map. Then:

a) img f is finite

- b) Assume that there are explicit algorithms solving the following problems:
 - *Membership of a given program in* dom *f*.
 - Decide, given functions s: $\mathfrak{P}((\mathsf{GlobxLoc})^2) \to \{ :=', :\geq' \}$ and $a: \mathfrak{P}((\mathsf{GlobxLoc})^2) \to \mathbb{N}_{\geq 0}$, whether some $(\to_i)_{i < n} \in \mathsf{dom}\ f$ exists satisfying $\forall \to \subseteq (\mathsf{Glob} \times \mathsf{Loc})^2:$ $(s(\to) = :=' \land |\{i < n \mid \to_i = \to\}| = a(\to)) \lor (s(\to) = :\geq' \land |\{i < n \mid \to_i = \to\}| \ge a(\to)).$
 - Evaluate f at a point of dom f.

Then there is an explicit algorithm determining whether img f is empty or not, and, in the case of nonemptiness, computing max(img f).

Proof. Let $f : \mathcal{P} \dashrightarrow \mathbb{N}_{\geq 0}$ be an antitone partial map.

a) Lemma IV.3.1 implies the existence of some k ∈ N₊ and some order-epimorphism φ: P → N^k_{≥0} \ {k × {0}}, where the codomain is equipped with the componentwise partial order, which we write as ≤.

Let
$$A = \{\varphi(p) \mid p \in \text{dom } f\}.$$

For $a \in A$, we write $\varphi^{-1}(a) = \{p \in \mathcal{P} \mid \varphi(p) = a\}$ for the preimage of *a* under φ till the end of this proof.

We are going to show that for each $a \in A$ the map f is constant on $(\text{dom } f) \cap \varphi^{-1}(a)$. For that, let $p, q \in \text{dom } f$ with $\varphi(p) = \varphi(q)$ be arbitrary. Then $\varphi(p) \leq \varphi(q)$ and $\varphi(p) \geq \varphi(q)$. Since φ is an order-homomorphism, p is a subprogram of q and vice versa. Since f is antitone, $f(p) \geq f(q)$ and $f(p) \leq f(q)$, implying f(p) = f(q). Since p and q were arbitrary, we have shown that for each $a \in A$ the map $f|_{(\text{dom } f) \cap \varphi^{-1}(a)}$ is constant.

Thus, the map

$$g: A \to \mathbb{N}_{\geq 0}, \quad a \mapsto f(p) \text{ for any } p \in (\operatorname{\mathsf{dom}} f) \cap \varphi^{-1}(a)$$

is well defined. It is also antitone, so, by Lemma IV.2.2a), $\operatorname{img} g$ is finite. Since $\forall p \in \operatorname{dom} f : f(p) = g(\varphi(p))$, we obtain

$$\operatorname{img} f \subseteq \operatorname{img} g \,. \tag{4}$$

Hence, img f is also finite.

- b) Consider k, φ, g , and its domain $A = \operatorname{img}(\varphi|_{\operatorname{dom} f})$ from above. The very definition of g implies $\operatorname{img} g \subseteq \operatorname{img} f$, so, using (4), $\operatorname{img} g = \operatorname{img} f$. We will apply the algorithm from Lemma IV.2.2b) to g. To this end, it suffices to supply the algorithms solving the following problems:
 - Decide, given a tuple of pairs $(s_i, a_i)_{i < m} \in (\{`=`, `>`\} \times \mathbb{N}_{\geq 0})^m$, whether some $y \in \text{dom } g$ exists satisfying $\forall i < m : ((s_i = `=` \land y_i = a_i) \lor (s_i = `>` \land y_i \geq a_i))$. According to Lemma IV.3.2, there is some enumeration of thread transition relations t_0, \ldots, t_{k-1} such that $\varphi((\rightarrow_i)_{i < n}) = (|\{i < n \mid \rightarrow_i = t_r\}|)_{r < k}$ for all programs $(\rightarrow_i)_{i < n}$.

So assume that a sequence of pairs $(s_i, a_i)_{i < m} \in (\{`=`, `\geq`\} \times \mathbb{N}_{\geq 0})^m$ is given. Define $s' : \mathfrak{P}((\operatorname{Glob} \times \operatorname{Loc})^2) \to \{`=`, `\geq`\}$ and $a' : \mathfrak{P}((\operatorname{Glob} \times \operatorname{Loc})^2) \to \mathbb{N}_{\geq 0}$ by $s'(t_i) = s_i$ and $a'(t_i) = a_i$ for all i < m. Notice that there is some $y \in (\operatorname{dom} g) = \operatorname{img}(\varphi_{|\operatorname{dom} f})$ satisfying $\forall i < m : ((s_i = `=` \land y_i = a_i) \lor (s_i = `\geq` \land y_i \geq a_i))$ iff there is some $(\to_i)_{i < n} \in \operatorname{dom} f$ satisfying $\forall \to \subseteq (\operatorname{Glob} \times \operatorname{Loc})^2 : (s'(\to) = `=` \land |\{i < n \mid \to_i = \to\}| = a'(\to)) \lor (s'(\to) = `\geq` \land |\{i < n \mid \to_i = \to\}| \geq a'(\to)):$

"⇒": Given a *y* as stated, let (→_{*i*})_{*i*<*n*} be an arbitrary member of (dom *f*) ∩ $\varphi^{-1}(y)$. "⇐": Given a program (→_{*i*})_{*i*<*n*} as stated, let $y = \varphi((→_i)_{i < n})$.

An algorithm answering whether, given functions $s': \mathfrak{P}((\mathsf{Glob} \times \mathsf{Loc})^2) \to \{`=`, ``>`\}$ and $a': \mathfrak{P}((\mathsf{Glob} \times \mathsf{Loc})^2) \to \mathbb{N}_{\geq 0}$, there is any $(\to_i)_{i < n} \in \mathsf{dom} f$ satisfying $\forall \to \subseteq (\mathsf{Glob} \times \mathsf{Loc})^2: (s'(\to) = `=` \land |\{i < n \mid \to_i = \to\}| = a'(\to)) \lor (s'(\to) = `=` \land |\{i < n \mid \to_i = \to\}| = a'(\to)) \lor (s'(\to) = `=` \land |\{i < n \mid \to_i = \to\}| \geq a'(\to))$ is provided by the assumption of this theorem.

• Evaluate g at a point of its domain. Since the preimage of a vector under φ is computable according to Lemma IV.3.2, the problem is solvable by Algorithm 2.

In the following, states of a particular form play an important role. We call a program state $(g, l) \in \text{Glob} \times \text{Loc}^n$ of any *n*-threaded program *uniform* if all components of *l* are the same, i.e., $\forall i, j \in n$: $l_i = l_j$.

Now we instantiate a particular antitone map in Theorem IV.3.3:

Lemma IV.3.4. For all $g, g' \in \text{Glob}$, $a, a' \in \text{Loc}$, and $\rightarrow \subseteq (\text{Glob}\times\text{Loc})^2$ there is some $c \in \mathbb{N}_{\geq 0}$ satisfying the following property: for all $n \in \mathbb{N}_+$, all n-threaded

Input: $a \in A$ Output: g(a)foreach $p \in \varphi^{-1}(a)$ do $\ \ if p \in \text{dom } f$ then return f(p)

programs $(\rightarrow_0, \ldots, \rightarrow_{n-1})$, and all i < n, if $\rightarrow_i = \sim$ and $d^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty$, then $d^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \leq c$. Moreover, the function mapping a tuple $(g, g', a, a', \sim) \in \text{Glob} \times \text{Glob} \times \text{Loc} \times \text{Loc} \times \mathfrak{P}((\text{Glob} \times \text{Loc})^2)$ to the smallest c satisfying the above property possesses an explicit algorithm.

Proof. Fix arbitrary $g, g' \in \text{Glob}, a, a' \in \text{Loc}, \text{ and } \sim \subseteq (\text{Glob} \times \text{Loc})^2$. We define $f: \mathcal{P} \to \mathbb{N}_{\geq 0}$ with dom $f = \left\{ (\to_i)_{i < n} \in \mathcal{P} \mid \exists i < n : \to_i = \to \land d_{(\to_i)_{i < n}}^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \\ < \infty \right\}, (\text{dom } f) \ni (\to_i)_{i < n} \mapsto \min \left\{ d_{(\to_i)_{i < n}}^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \mid i < n \land \to = \to_i \right\}.$ (The notation $d_p^{\text{loc}}(\ldots)$ for a program $p = (\to_i)_{i < n}$ has been defined in § II.2.)

Now we will show that *f* is antitone. Let $p, q \in \text{dom } f$ such that *p* is a subprogram of *q*. Let $(\rightarrow_i)_{i < n} = p$ and $(\rightarrow_i)_{i < m} = q$. Then there is an injective map $h: n \hookrightarrow m$ such that $\rightarrow_i = \rightarrow_{h(i)}$ for all i < n. Since $p \in \text{dom } f$, there is some i < n such that $\rightarrow = \rightarrow_i$ and a path σ in the transition graph of *p* such that $\sigma_0 = (g, n \times \{a\})$, the last state of σ is (g', l')for some $l' \in \text{Loc}^n$ such that $l'_i = a'$, and length $(\sigma) = f(p)$. Informally, we will now lift the path σ from *p* to *q* by adding threads that always stay at the local state *a*. In the following, let $h^{-1}: (\text{img } h) \hookrightarrow n$ be the inverse of *h* on its image. We define a sequence

$$\hat{\sigma} = (\hat{\sigma}_k)_{k \le f(p)} \in (\mathsf{Glob} \times \mathsf{Loc}^m)^{f(p)+1} \text{ by } \hat{\sigma}_k \stackrel{\text{def}}{=} \begin{pmatrix} \check{g}^{[k]}, \\ g^{[k]}, \\ a, & \text{otherwise} \end{pmatrix}_{j < m} \end{pmatrix}$$

where $(\check{g}^{[k]}, \check{l}^{[k]}) \stackrel{\text{def}}{=} \sigma_k$, for all $k \leq f(p)$. Then $\hat{\sigma}$ is a path in the transition graph of q such that $\hat{\sigma}$ starts in $(g, m \times \{a\})$ and ends in a program state (g', l'') for some $l'' \in \text{Loc}^m$ satisfying $l''_{h(\hat{i})} = l'_{\hat{i}} = a'$. So $d_q^{\text{loc}}((g, m \times \{a\}), h(\hat{i}), (g', a')) < \infty$. Notice that $\rightsquigarrow = \rightarrow_{\hat{i}} = \rightarrowtail_{h(\hat{i})}$, so $q \in \text{dom } f$ and $f(q) \leq f(p)$. Since p and q were arbitrary, we have shown that f is antitone.

Theorem IV.3.3a) implies that *f* is bounded from above by some $c \in \mathbb{N}_{\geq 0}$.

Now let an arbitrary $n \in \mathbb{N}_+$, an arbitrary *n*-threaded program $p = (\rightarrow_i)_{i < n}$, an arbitrary i < n such that $\rightarrow_i = \rightarrow$ and $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty$ be given. Then $p \in \text{dom } f$. The definition of f implies the existence of some $\hat{i} < n$ such that $\rightarrow_i = \rightarrow$ and of some path σ of length f(p) such that σ starts in $(g, n \times \{a\})$ and ends in (g', l') for some $l' \in \text{Loc}^n$ satisfying $l'_i = a'$. Note that threads i and \hat{i} have the same thread transition relation. Swap the steps of these threads: consider the sequence $\hat{\sigma} = (\hat{\sigma}_k)_{k \leq f(p)} \in \mathcal{O}_k^{(X|k)}$

$$(\mathsf{Glob} \times \mathsf{Loc}^n)^{f(p)+1} \text{ of program states, defined by } \hat{\sigma}_k \stackrel{\text{def}}{=} \left(\underbrace{\check{g}^{[k]}}_{i}, \left(\begin{cases} \tilde{l}_j^{[k]}, & \text{if } j \notin \{i, i\}, \\ \tilde{l}_i^{[k]}, & \text{if } j = i, \\ \tilde{l}_i^{[k]}, & \text{if } j = i \end{cases} \right)_{j < n} \right)$$

where $(\check{g}^{[k]}, \check{l}^{[k]}) \stackrel{\text{der}}{=} \sigma_k$, for all $k \leq f(p)$. Then $\hat{\sigma}$ is a path in the transition graph of p of length f(p) that starts in $(g, n \times \{a\})$ and ends in (g', l'') for some $l'' \in \text{Loc}^n$ such that $l''_i = a'$. Since length $(\hat{\sigma}) = f(p) \leq c$, we obtain $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \leq c$.

To prove the existence of an explicit algorithm computing the minimal such *c* as required, let us assume a tuple $(g, g', a, a', \rightsquigarrow)$ from the finite (!) set $S = \text{Glob} \times \text{Glob} \times \text{Loc} \times \text{Loc} \times \mathbb{P}((\text{Glob} \times \text{Loc})^2)$ and define *f* as above. We will apply Theorem IV.3.3b).

To this end, it suffices to supply the algorithms solving the following problems:

• Membership of a given program in dom f.

Let $p = (\rightarrow_i)_{i < n} \in \mathcal{P}$ be arbitrary. To test whether *p* belongs to dom *f*, we first search for an i < n such that $\rightarrow_i = \rightarrow$ and return "false" if we find none. If one is found, we check for this *i* whether $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty$ by searching in the transition graph of *p* for a program state that is reachable from $(g, n \times \{a\})$, has the shared part *g'*, and has the local part *a'* of thread *i*. We return "true" if we find such a program state and "false" otherwise.

• Decide, given functions $s: \mathfrak{P}((\mathsf{Glob}\times\mathsf{Loc})^2) \to \{ `=', `\geq' \}$ and $b: \mathfrak{P}((\mathsf{Glob}\times\mathsf{Loc})^2) \to \mathbb{N}_{\geq 0}$, whether some $(\to_i)_{i < n} \in \text{dom } f$ exists satisfying $\forall \to \subseteq (\mathsf{Glob}\times\mathsf{Loc})^2 \colon (s(\to)) = `=' \land |\{i < n \mid \to_i = \to\}| = b(\to)) \lor (s(\to) = `\geq' \land |\{i < n \mid \to_i = \to\}| \ge b(\to)).$

We let $k = |\mathfrak{P}((\text{Glob}\times\text{Loc})^2)|$ and choose any enumeration t_0, \ldots, t_{k-1} of the thread transition relations.

Let functions $s: \mathfrak{P}((\mathsf{Glob}\times\mathsf{Loc})^2) \to \{ =', \geq' \}$ and $b: \mathfrak{P}((\mathsf{Glob}\times\mathsf{Loc})^2) \to \mathbb{N}_{\geq 0}$ be given as input.

We now create a Petri net simulating multithreaded programs as follows.

Without loss of generality, suppose $(k \times Loc) \cap Glob = \emptyset$; otherwise we rename the members of Loc or Glob to attain this disjointness. Choose a fresh individual start \notin $(k \times Loc) \cup Glob$. We construct the set of the Petri net's places as the disjoint union $(k \times Loc) \cup Glob \cup \{\text{start}\}.$

Initially, the Petri net has one token in start and $b(t_j)$ tokens in (j, a) for each j < k; there are no other tokens in the initial marking.

The set of transitions is formed as a union of three pairwise disjoint sets. To define the first of these sets, let $I = \{ \rightarrow \subseteq (\text{Glob} \times \text{Loc})^2 \mid s(\rightarrow) = ` \ge ` \}$; the first Petri-net– transition set has |I| transitions indexed by I, and for each j < k such that $t_j \in I$, the transition with index t_j consumes one token from start, puts one token back into start, and adds one token to the place (j, a). The second Petri-net–transition set comprises a single transition that completely leaves start: this transition consumes a token from start and adds a token to the place g. The third Petri-net–transition set comprises the transitions actually simulating the threads: for each j < k and each thread transition $((x, y), (x', y')) \in t_j$, the Petri net contains a transition removing one token from (j, y) and one token from x and then placing one token to (j, y') and one token to x'.

After having constructed such a Petri net, we check for the coverability of the marking in which the place g' has one token, for the unique j < k satisfying $t_j = \rightarrow$ the place (j, a') has one token, and all the other places have no tokens. Any coverability procedure (e.g., [48]) would do.

To see the correctness of the construction, notice that the aforementioned Petri net simulates programs with at least $b(\rightarrow)$ copies of each $\rightarrow \subseteq (\text{Glob} \times \text{Loc})^2$ with $s(\rightarrow) = `\geq`$ and exactly $b(\rightarrow)$ copies of each $\rightarrow \subseteq (\text{Glob} \times \text{Loc})^2$ with $s(\rightarrow) = `=`$.

• Evaluate f at a point of dom f.

Given an *n*-threaded program in dom f, perform a breadth-first search from $(g, n \times \{a\})$. At each level, watch for the program states with shared part g' and check whether in such a program state any of the threads with the transition relation \rightarrow has a' as the local part. As soon as such a program state is found, stop and return the level number. (Here, we start counting the level numbers of our breadth-first–search tree by assigning 0 to the root.)

Since *S* is finite, the combined algorithm that takes a member of *S* and starts the sub-algorithm for the corresponding f is also explicit.

Since there are only finitely many shared states, local states, and thread transition relations, a maximal c from Lemma IV.3.4 can be computed:

Corollary IV.3.5. There is some $c \in \mathbb{N}_{\geq 0}$ such that, for all $g, g' \in \text{Glob}$, $a, a' \in \text{Loc}$, $n \in \mathbb{N}_{+}$, all *n*-threaded programs p, and all i < n, if $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty$, then $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty$, then $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \leq c$. Moreover, there is an explicit algorithm constructing the smallest such c.

Proof. According to Lemma IV.3.4, there is a map ζ : Glob×Glob×Loc×Loc× $\mathfrak{P}((Glob \times Loc)^2) \rightarrow \mathbb{N}_{\geq 0}$ such that, for all $g, g' \in Glob, a, a' \in Loc, \rightarrow \subseteq (Glob \times Loc)^2, n \in \mathbb{N}_+, n$ -threaded programs $p = (\rightarrow_i)_{i < n}$, and all i < n, if $\rightarrow_i = \rightarrow$ and $d_p^{loc}((g, n \times \{a\}), i, (g', a')) < \infty$, then $d_p^{loc}((g, n \times \{a\}), i, (g', a')) \leq \zeta(g, g', a, a', \rightarrow)$. We choose the pointwise smallest such ζ ; then an explicit algorithm computing ζ exists. Let $c = \max(\operatorname{img} \zeta)$. This value can be explicitly constructed by evaluating ζ at all the points of its finite domain and taking the maximal value.

Now consider an arbitrary program $p = (\rightarrow_i)_{i < n}$. For all $g, g' \in \text{Glob}, a, a' \in \text{Loc}$, and i < n such that $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty$ we have $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \leq \zeta(g, g', a, a', \rightarrow_i) \leq c$.

Thus, $\min \{ \hat{c} \in \mathbb{N}_{\geq 0} \mid \forall g, g' \in \text{Glob}, a, a' \in \text{Loc}, n \in \mathbb{N}_+, n\text{-threaded program } p, \}$

i < n: $d_p^{\mathsf{loc}}((g, n \times \{a\}), i, (g', a')) < \infty \Rightarrow d_p^{\mathsf{loc}}((g, n \times \{a\}), i, (g', a')) \leq \hat{c} \leq c$. To show that the last inequality is actually an equality, let \hat{c} be the minimum on the left-hand side. Since $c \in \operatorname{img} \zeta$, there are some $g, g' \in \operatorname{Glob}, a, a' \in \operatorname{Loc}, \operatorname{and} \rightarrow \subseteq (\operatorname{Glob} \times \operatorname{Loc})^2$ such that $\zeta(g, g', a, a', \rightarrow) = c$.

Case c = 0. Then $\hat{c} \ge c$ holds immediately.

Case c > 0. Since ζ was chosen pointwise minimal, its definition implies that there is an $n \in \mathbb{N}_+$, an *n*-threaded program $p = (\rightarrow_i)_{i < n}$, and an i < n such that $\rightarrow_i = \rightarrow$ and $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty$ and $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) = \zeta(g, g', a, a', \sim)$, i.e., $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) = c$. The definition of \hat{c} implies $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \leq \hat{c}$, i.e., $c \leq \hat{c}$.

In both cases, $\hat{c} \ge c$, proving $\min \left\{ \hat{c} \in \mathbb{N}_{\ge 0} \mid \forall g, g' \in \text{Glob}, a, a' \in \text{Loc}, n \in \mathbb{N}_+, n-threaded program <math>p, i < n : d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty \Rightarrow d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \alpha \Rightarrow d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \hat{c} \right\} = c.$

In Corollary IV.3.5, the source program state is uniform. Before we remove this restriction, we argue that local distances stay invariant under renaming of local states. More formally:

Lemma IV.3.6. Let $p = (\rightarrow_j)_{j < n}$ be a program. For each j < n let h_j be a permutation of Loc. Moreover, for each j < n let a thread transition relation $\rightsquigarrow_j \subseteq (\text{Glob} \times \text{Loc})^2$ be defined via

$$(g,l) \sim_j (g',l') \quad \stackrel{def}{\longleftrightarrow} \quad (g,h_j(l)) \to_j (g',h_j(l'))$$

for all $g, g' \in \text{Glob}$ and $l, l' \in \text{Loc}$; let $q = (\rightsquigarrow_j)_{j < n}$. Then, for all $(g, l) \in \text{State}_p$, i < n, $g' \in \text{Glob}$, and $b \in \text{Loc}$, we have

$$\mathsf{d}_{q}^{\mathsf{loc}}((g,l),i,(g',b)) = \mathsf{d}_{p}^{\mathsf{loc}}((g,(h_{j}(l_{j}))_{j < n}),i,(g',h_{i}(b))).$$

Proof. Let $(g, l) \in \text{State}_p$, $i < n, g' \in \text{Glob}$, and $b \in \text{Loc}$. We are going to show the equality in question by separating it into two inequalities.

- $$\label{eq:states} \begin{split} & \text{``d}_q^{\text{loc}}\big((g,l),i,(g',b)\big) \stackrel{!}{\leq} d_p^{\text{loc}}\big((g,(h_j(l_j))_{j < n}\big),i,(g',h_i(b))\big) \\ & \text{``:} \ \text{If the right-hand side is } \\ & \infty, \text{ the inequality holds trivially. Thus, let us assume from now on that the right-hand side is equal to some } k \in \mathbb{N}_{\geq 0}. \ \text{Then there is a path } \sigma = ((\hat{g}^{[r]}, \hat{l}^{[r]}))_{r \leqslant k} \\ & \text{ in the transition graph of } p \ \text{ such that } \hat{g}^{[0]} = g, \ \hat{l}^{[0]} = (h_j(l_j))_{j < n}, \ \hat{g}^{[k]} = g', \\ & \text{ and } \hat{l}_i^{[k]} = h_i(b). \ \text{There is a map } t: k \to n \ \text{that tells us which thread takes a step at each time point, i.e., such that for each <math>r < k \ we \ have \ (\hat{g}^{[r]}, \hat{l}^{[r]}_{t(r)}) \to_{t(r)} \\ & (\hat{g}^{[r+1]}, \hat{l}_{t(r)}^{[r+1]}) \ \text{and } \forall j \in n \setminus \{t(r)\}: \hat{l}_j^{[r]} = \hat{l}_j^{[r+1]}. \ \text{Then for each } r < k \ we \ have \ (\hat{g}^{[r]}, h_{t(r)}(\hat{l}^{[r]}_{t(r)})) \to_{t(r)} \\ & (\hat{g}^{[r+1]}, h_{t(r)}(\hat{l}^{[r+1]}_{t(r)})) \right) \to_{t(r)} \ (\hat{g}^{[r+1]}, h_{t(r)}(\hat{l}^{[r+1]}_{t(r)})), \ \text{which, using the assumption,} \\ & \text{implies } (\hat{g}^{[r]}, h_{t(r)}^{-1}(\hat{l}^{[r]}_{t(r)})) \to_{t(r)} \ (\hat{g}^{[r+1]}, h_{t(r)}(\hat{l}^{[r+1]}_{t(r)})), \ \text{Let } \check{l}^{[r]} = (h_j^{-1}(\hat{l}^{[r]}_{j}))_{j < n} \\ & \text{for all } r \leqslant k. \ \text{Then for each } r < k \ we \ have \ (\hat{g}^{[r]}, \tilde{l}^{[r+1]}_{t(r)}) \ \text{and} \\ & \forall j \in n \setminus \{t(r)\}: \ \check{l}_j^{[r]} = h_j^{-1}(\hat{l}^{[r]}_{j}) = h_j^{-1}(\hat{l}^{[r+1]}_{j}) = \check{l}_j^{[r+1]}. \ \text{Therefore, the sequence} \\ & \check{\sigma} \ \stackrel{\text{def}}{=} \left((\hat{g}^{[r]}, \tilde{l}^{[r]}_{j}) \right)_{r \leqslant k} \ \text{is a walk in the transition graph of } q. \ \text{The walk starts in} \\ & \left(\hat{g}^{[0]}, (h_j^{-1}(\hat{l}^{[0]}_{j}))_{j < n} \right) = \left(\hat{g}^{[0]}, (l_j)_{j < n} \right) = (g, l) \ \text{and ends in the program state} \ (\hat{g}^{[k]}, \\ & (h_j^{-1}(\hat{l}^{[k]}_{j}))_{j < n} \right) = \left(g', (h_j^{-1}(\hat{l}^{[k]}_{j}))_{j < n} \right), \ \text{whose local part of thread } i \ h_i^{-1}(\hat{l}^{[k]}_{i}) = b. \ \text{Note that length}(\check{\sigma}) = k. \ \text{Therefore, } d_q^{\text{loc}}((g, l), i, (g', b)) \leqslant k. \end{aligned}$$
- $\begin{aligned} & \text{``d}_q^{\text{loc}}((g,l),i,(g',b)) \stackrel{!}{\geq} d_p^{\text{loc}}((g,(h_j(l_j))_{j < n}),i,(g',h_i(b))) \\ & \text{``:} Analogously as follows. If the left-hand side is <math>\infty$, the inequality holds trivially. Thus, let us assume from now on that the left-hand side is equal to some $k \in \mathbb{N}_{\geq 0}$. Then there is a path $\sigma = ((\hat{g}^{[r]}, \hat{l}^{[r]}))_{r \leq k}$ in the transition graph of q such that $\hat{g}^{[0]} = g$, $\hat{l}^{[0]} = l$, $\hat{g}^{[k]} = g'$, and $\hat{l}_i^{[k]} = b$. There is a map $t: k \rightarrow n$ that tells us which thread takes a step at each time point, i.e., such that for each r < k we have $(\hat{g}^{[r]}, \hat{l}_{t(r)}^{[r]}) \sim_{t(r)} (\hat{g}^{[r+1]}, \hat{l}_{t(r)}^{[r+1]})$ and $\forall j \in n \setminus \{t(r)\} : \hat{l}_j^{[r]} = \hat{l}_j^{[r+1]}$. The assumption implies that for each r < k we have $(\hat{g}^{[r]}, \hat{l}_{t(r)}^{[r]}) \rightarrow_{t(r)} (\hat{g}^{[r+1]}, h_{t(r)}(\hat{l}_{t(r)}^{[r]}))$. Let $\check{l}^{[r]} = (h_j(\hat{l}_j^{[r]}))_{j < n}$ for each $r \leqslant k$. Then for each r < k we have $(\hat{g}^{[r]}, \check{l}_{t(r)}^{[r]}) \rightarrow_{t(r)} (\hat{g}^{[r+1]}, h_{t(r)}(\hat{l}_{t(r)}^{[r]}))$ and $\forall j \in n \setminus \{t(r)\} : \check{l}_j^{[r]} = h_j(\hat{l}_j^{[r]}) = h_j(\hat{l}_j^{[r+1]}) = \check{l}_j^{[r+1]}$. Therefore, the sequence $\check{\sigma} \stackrel{\text{def}}{=} ((\hat{g}^{[r]}, \check{l}^{[r]}))_{r \leqslant k}$ is a walk in the transition graph of p. The walk starts in $(\hat{g}^{[0]}, (h_j(\hat{l}_j^{[0]}))_{j < n}) = (g, (h_j(l_j))_{j < n})$ and ends in the program state $(\hat{g}^{[k]}, (h_j(\hat{l}_j^{[k]}))_{j < n}) = (g', (h_j(\hat{l}_j^{[k]}))_{j < n})$, whose local part of thread i is $h_i(\hat{l}_i^{[k]}) = h_i(b)$. Note that length($\check{\sigma}) = k$. Therefore, $d_p^{\text{loc}}((g, (h_j(l_j))_{j < n}), i, (g', h_i(b))) \leqslant k$.

Applying the above result, we obtain:

Theorem IV.3.7. There is some $c \in \mathbb{N}_{\geq 0}$ such that $\forall n \in \mathbb{N}_+$: diamax^{loc} $(n) \leq c$. Moreover, the smallest such c can be constructed by an explicit algorithm.

Proof. We fix *c* to be the smallest constant from Corollary IV.3.5. Let $n \in \mathbb{N}_+$. Let $p = (\rightarrow_i)_{i < n}$ be an *n*-threaded program with the set of program states State; we will show that the local diameter of *p* is bounded by *c*.

Let $(g, l) \in$ State, $i < n, g' \in$ Glob, and $b \in$ Loc be arbitrary such that $d_p^{\text{loc}}((g, l), i, (g', b)) < \infty$. It suffices to show that $d_p^{\text{loc}}((g, l), i, (g', b)) \stackrel{!}{\leq} c$. For each j < n we let

$$h_j: \operatorname{Loc} \hookrightarrow \operatorname{Loc}, \quad x \mapsto \begin{cases} x, & \text{if } x \notin \{l_i, l_j\} \\ l_i, & \text{if } x = l_j, \\ l_j, & \text{if } x = l_i \end{cases}$$

be the permutation that swaps l_i with l_j while leaving all other local states invariant. We define

$$(\tilde{g}, \tilde{l}) \rightsquigarrow_j (\tilde{g}', \tilde{l}') \stackrel{\text{def}}{\longleftrightarrow} (\tilde{g}, h_j(\tilde{l})) \rightarrow_j (\tilde{g}', h_j(\tilde{l}'))$$

for all $\tilde{g}, \tilde{g}' \in \text{Glob}, \tilde{l}, \tilde{l}' \in \text{Loc}, \text{ and } j < n.$ We set $q \stackrel{\text{def}}{=} (\rightsquigarrow_j)_{j < n}$. Then $d_p^{\text{loc}}((g, l), i, (g', b)) = d_p^{\text{loc}}(g, (h_j(h_j^{-1}(l_j)))_{j < n}), i, (g', h_i(h_i^{-1}(b)))) = [\text{by Lemma IV.3.6]} d_q^{\text{loc}}(g, (h_j^{-1}(l_j)))_{j < n}), i, (g', h_i^{-1}(b))) = d_q^{\text{loc}}((g, n \times \{l_i\}), i, (g', h_i^{-1}(b))) = [\text{since } h_i = \text{id}_{\text{Loc}}] d_q^{\text{loc}}(g, n \times \{l_i\}), i, (g', b))$. Therefore, $d_q^{\text{loc}}((g, n \times \{l_i\}), i, (g', b)) < \infty$. Corollary IV.3.5 implies $d_q^{\text{loc}}((g, n \times \{l_i\}), i, (g', b)) \leq c$.

Since *p* was arbitrary, we have shown max(img diamax^{loc}) $\leq c$. To show that the last inequality is actually an equality, assume for the purpose of contradiction that max(img diamax^{loc}) is strictly less than *c*. Thus, c > 0. Recall that $c = \min\{\tilde{c} \in \mathbb{N}_{\geq 0} | \forall g, g' \in \text{Glob}, a, a' \in \text{Loc}, n \in \mathbb{N}_+, n\text{-threaded program } p, i < n: d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty \Rightarrow d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \leq \tilde{c}\}$. Since c-1 does not belong to the set over which the minimum is taken, there is some $g, g' \in \text{Glob}, a, a' \in \text{Loc}, n \in \mathbb{N}_+, n\text{-threaded program } p, \text{ and } i < n \text{ such that } d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty \text{ but } d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \leq c-1$. Then $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \geq c$, contradicting our assumption. Thus, our assumption was wrong and max(img diamax^{loc}) = c.

Due to Theorem IV.3.7, the maximal local diameter for programs can be determined:

Definition and Corollary IV.3.8. $C \stackrel{def}{=} \max(\operatorname{img diamax}^{\operatorname{loc}})$ exists and is computable by an explicit algorithm.

The top level of computing *C* is "compute max(img ζ)" as Algorithm 3 demonstrates, where ζ : Glob×Glob×Loc×Loc× $\mathfrak{P}((Glob×Loc)^2) \rightarrow \mathbb{N}_{\geq 0}$ is taken from the proof of Corollary IV.3.5. The function ζ is the pointwise smallest function such that, for all g, $g' \in Glob$, $a, a' \in Loc$, and $\rightarrow \subseteq (Glob×Loc)^2$, we have: for all $n \in \mathbb{N}_+$, all *n*-threaded programs $(\rightarrow_0, \ldots, \rightarrow_{n-1})$, and all i < n, if $\rightarrow_i = \rightarrow$ and d^{loc} $((g, n \times \{a\}), i, (g', a')) < \infty$, then d^{loc} $((g, n \times \{a\}), i, (g', a')) \leq \zeta(g, g', a, a', \rightarrow)$.

Following the involved lemmas, this algorithm can be written out to any level of detail. The next level, i.e., evaluating ζ , is provided by Algorithm 4.

Algorithm 4 always terminates. Due to potentially unboundedly large antichains, its running time (and, consequently, the running time of Algorithm 3) is not known to

Algorithm 4: Evaluating ζ at a point.Input: $g, g' \in \text{Glob}, a, a' \in \text{Loc}, \rightarrow \subseteq (\text{Glob} \times \text{Loc})^2$ Output: $\zeta(g, g', a, a', \rightarrow)$ if $\exists program(\rightarrow_i)_{i < n} : \exists i < n : \rightarrow = \rightarrow_i \land d_{(\rightarrow_i)_{i < n}}^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty$ thenconstruct a maximal antichain D of such programs with respect to the subprogram
preorder;
return max{ $d_{(\rightarrow_i)_{i < n}}^{\text{loc}}((g, n \times \{a\}), i, (g', a')) \mid n \in \mathbb{N}_+ \land i < n \land \rightarrow_i = \rightarrow \land$
 $d_p^{\text{loc}}((g, n \times \{a\}), i, (g', a')) < \infty \land \exists p \in D : (\rightarrow_i)_{i < n} is a subprogram of p$ else return 0

have an elementary upper bound, even if we implement the algorithm in such a way that programs with the smallest number of threads (whenever there is a choice) enter the antichain.

Although this paper does not aim to determine the numerical values of *C* given numerical values of *G* and *L*, three remarks are to be made. First, "explicit" means that our algorithm computing *C* can be easily converted to a runnable implementation in some real-world programming language (although actually obtaining the numerical representation of *C* in acceptable time would require more effort); this is strictly better than a pure computability claim (in which case we would know that an algorithm exists but might not know what it is). Second, a general lower bound on *C* is GL - 1: we obtain it by considering a single-threaded program whose transition relation is $\{((g,l), (g, l+1)) \in (\mathbb{N}_{\geq 0} \times \mathbb{N}_{\geq 0})^2 \mid g < G \land l+1 < L\} \cup \{((g, L-1), (g+1, 0)) \mid g \in \mathbb{N}_{\geq 0} \land g+1 < G\}$. Third, a further lower bound stems from the example N3T9 in § III.3: from diam^{loc}(N3T9) = 9 we obtain that for G=L=2 we have $C \geq 9$.

V. Diameter

Now we provide a lower and an upper bound on diamax, an upper bound on the diameter of programs from a particular class, and an upper bound on the diameter of a randomly chosen program.

V.1. A lower bound on diamax

Within this section, without loss of generality, let the elements of Glob be $0, \ldots, G-1$ and the elements of Loc be $0, \ldots, L-1$.

We start with a few special cases.

Lemma V.1.1. If $G=1 \leq L$, then diamax(n) = (L-1)n for all $n \in \mathbb{N}_+$.

Proof. Assume $G=1 \leq L$. Let $n \in \mathbb{N}_{\geq 0}$. We split diamax $(n) \stackrel{!}{=} (L-1)n$ into two inequalities.

"≤": Take a walk σ of an n-threaded program that realizes the distance diamax(n), i.e., such that length(σ) = diamax(n) and σ is a shortest path between its end nodes. The shared state stays constant throughout σ, so, if somewhere in σ two

neighboring thread-transitions of different threads are applied, it is always possible to change the order of applying these thread transitions such that the length of the walk and the overall effect of these two thread transitions are retained. With finitely many exchanges of this kind, one can obtain a walk σ' such that, for all *i*, *j* with *i*<*j*<*n*, the thread transitions of thread *i* are used before the thread transitions of thread *j*, and the final state of σ' is the same as the final state of σ . In σ' , the number of used thread transitions of each thread does not exceed L-1 (otherwise σ' would have a self-intersection, and throwing the loop out would result in a shorter walk between the same end-nodes, contradicting the fact that σ is a shortest walk between its ends). Then length $(\sigma') \leq (L-1)n$. Thus, length $(\sigma) \leq (L-1)n$.

"≥": Consider the *n*-threaded program $(→_i)_{i < n}$ such that

$$\rightarrow_i \stackrel{\text{def}}{=} \left\{ \left((0,l), (0,l+1) \right) \middle| l+1 < L \right\} \qquad (i < n).$$

Each program transition increases some local component of a state by 1 and leaves the other components unchanged. Thus, traveling from $(0, n \times \{0\})$ to $(0, n \times \{L-1\})$ takes (L-1)n single steps. Therefore, diamax $(n) \ge (L-1)n$.

Lemma V.1.2. If $L=1 \leq G$, then diamax(n) = G-1 for all $n \in \mathbb{N}_+$.

Proof. Assume $L=1 \leq G$. Let $n \in \mathbb{N}_+$. We split diamax $(n) \stackrel{!}{=} G-1$ into two inequalities.

- "≤": Take a walk σ of an *n*-threaded program that realizes the distance diamax(*n*), i.e., such that length(σ) = diamax(*n*) and σ is a shortest path between its end points. All the local states stay constant throughout σ , so inside σ only the shared part changes. There are exactly *G* shared states; any walk with more than *G* shared parts would have a self-intersection and could be shortened. Thus, each shared state is used at most once in σ , implying length(σ) < *G*.
- "≥": Consider an *n*-threaded program $(\rightarrow_i)_{i < n}$ such that

$$\rightarrow_0 \stackrel{\text{def}}{=} \left\{ \left((g,0), (g+1,0) \right) \mid g+1 < G \right\} \text{ and } \rightarrow_i \stackrel{\text{def}}{=} \emptyset \text{ for } i \in n \setminus \{0\} .$$

Then $d((0, n \times \{0\}), (G-1, n \times \{0\})) = G-1.$

Theorem V.1.3. $\forall n \in \mathbb{N}_+$: diamax $(n) \ge (GL - L + 1)(L - 1)n + (2 - L)(G - 1)L$.

Proof. The cases L=1 or G=1 have been treated in Lemmas V.1.1 and V.1.2. From now on, consider the case $G, L \ge 2$.

Fix $n \ge 1$, and consider the following *n*-threaded program. We define the set of transitions of thread 0 as

$$\{ ((g,l) , (g,l+1)) \mid g < G \land l+1 < L \}$$
(5)

$$\cup \{ ((g, L-1), (g+1, 0)) \mid g+1 < G \} .$$
(6)

For example, for $G \ge 5$ and $L \ge 5$ these thread transitions can be visualized as follows:



Let the transitions of each thread with index from $n \setminus \{0\}$ be

$$\{((G-1,l), (0,l+1)) \mid l+1 < L\}.$$
(7)

For example, for $G \ge 3$ and $L \ge 5$ these thread transitions can be visualized as follows:



Note that for each $i \in \mathbb{N}_{\geq 0}$ there is a unique pair $(k, m) \in \mathbb{N}_{\geq 0}^2$ such that i = k((G-1)L + 1) + m and $m \leq (G-1)L$. Moreover, $G \geq 2$ implies $(\mathbb{N}_{\geq 0} \cap [0, (G-1)L]) = (\mathbb{N}_{\geq 0} \cap [0, L[) \cup (\mathbb{N}_{\geq 0} \cap [L, (G-1)L])$. Using these properties, we define sets $D_i \subseteq$ State for $i \in \mathbb{N}_{\geq 0}$ as follows. For $k \in \mathbb{N}_{\geq 0}$ and m < L, let

$$D_{k((G-1)L+1)+m} \stackrel{\text{def}}{=} \left\{ (0,l) \in \text{State} \middle| l_0 = m \land \sum_{t=1}^{n-1} l_t = k \right\}$$
(8)

$$\dot{\cup} \left\{ (G-1,l) \in \text{State} \mid l_0 = m+1 \land \sum_{t=1}^{n-1} l_t = k-1 \right\},$$
(9)

and, for $k, m \in \mathbb{N}_{\geq 0}$ such that $L \leq m \leq (G-1)L$, let

$$D_{k((G-1)L+1)+m} \stackrel{\text{def}}{=} \left\{ \left(\left\lfloor \frac{m}{L} \right\rfloor, l \right) \in \text{State} \middle| l_0 = (m \mod L) \land \sum_{t=1}^{n-1} l_t = k \right\}.$$
(10)

As a preparatory step, we claim that these sets are disjoint, i.e., $D_j \cap D_i$ is empty for different $i, j \in \mathbb{N}_{\geq 0}$. To prove the claim, let $i, j \in \mathbb{N}_{\geq 0}$ and $(g, l) \in D_i \cap D_j$ be arbitrary. We are going to show that i and j coincide. Dividing i and j with remainder by (G-1)L + 1, we obtain quotients $k, k' \in \mathbb{N}_{\geq 0}$ and remainders $m, m' \in \mathbb{N}_{\geq 0}$ such that i = k((G-1)L + 1) + m, j = k'((G-1)L + 1) + m', and $m, m' \leq (G-1)L$. Case m, m' < L. Then (g, l) is in the set (8) or (9).

Case g = 0 and $m = l_0 = m'$ and $k = \sum_{t=1}^{n-1} l_t = k'$. Then k((G-1)L+1) + m =

 $\tilde{k}'((G-1)L+1) + m'$, and, therefore, i = j.

Case 0 = g = G - 1. This would imply G = 1.

Case g = G-1 and $m+1 = l_0 = m'+1$ and $k-1 = \sum_{t=1}^{n-1} l_t = k'-1$. Then m=m' and k=k'. Therefore, i=j.

- Case $m < L \le m'$. By (10), $g = \lfloor \frac{m'}{L} \rfloor$ and $l_0 = (m' \mod L)$. From $m' \ge L$ we obtain g > 0. By (9), g = G-1 and $l_0 = m+1$. Then $m' \ge (G-1)L$ and $m+1 = (m' \mod L)$. The choice of m' implies m' = (G-1)L. Therefore, $(m' \mod L) = 0$. Thus, m+1 = 0. $\frac{1}{2}$
- Case $m' < L \le m$. By (10), $g = \lfloor \frac{m}{L} \rfloor$ and $l_0 = (m \mod L)$. From $m \ge L$ we obtain g > 0. By (9), g = G-1 and $l_0 = m'+1$. Then $m \ge (G-1)L$ and $m'+1 = (m \mod L)$. The choice of m implies m = (G-1)L. Therefore, $(m \mod L) = 0$. Thus, m'+1 = 0. $\frac{1}{2}$

Case $L \leq m, m'$. By (10), $\lfloor \frac{m}{L} \rfloor = g = \lfloor \frac{m'}{L} \rfloor$, $(m \mod L) = l_0 = (m' \mod L)$, and $k = \sum_{t=1}^{n-1} l_t = k'$. Then $m = L \lfloor \frac{m}{L} \rfloor + (m \mod L) = L \lfloor \frac{m'}{L} \rfloor + (m' \mod L) = m'$ and k = k'. Therefore, i = j.

Since *i*, *j*, and (g, l) were arbitrary, we have shown that the sets D_i for $i \in \mathbb{N}_{\geq 0}$ are pairwise disjoint:

$$\forall i, j \in \mathbb{N}_{\geq 0}: \quad D_i \cap D_j \neq \emptyset \implies i = j.$$

$$(11)$$

Now, consider the map

depth: State $\rightarrow \mathbb{N}_{\geq 0} \dot{\cup} \{\infty\}, (g, l) \mapsto d((0, n \times \{0\}), (g, l))$

providing the distance of a state from $(0, n \times \{0\})$. We claim that the sets D_i and depth⁻¹($\{i\}$) are equal $(i \in \mathbb{N}_{\geq 0})$ and will prove this claim by natural induction on *i*. So let $i \in \mathbb{N}_{\geq 0}$ be arbitrary, and assume (as our induction hypothesis) $\forall j < i: D_j = \text{depth}^{-1}(\{j\})$. There is a unique pair $(k, m) \in \mathbb{N}_{\geq 0}^2$ such that i = k((G-1)L + 1) + m and $m \leq (G-1)L$.

Case *i*=0. We have $D_0 = \{(0, n \times \{0\})\} = \text{depth}^{-1}(\{0\})$.

Case $i \ge 1$. We will prove $D_i \stackrel{!}{=} \text{depth}^{-1}(\{i\})$ by showing the left inclusion and the right inclusion separately.

"⊆". Let $(g, l) \in D_i$. From (11) we obtain $(g, l) \notin D_j$ for all j < i. The induction hypothesis implies

$$\forall j < i: (g, l) \notin \mathsf{depth}^{-1}(\{j\}). \tag{12}$$

As the next step, we are going to show that depth(g, l) does not exceed *i*. According to the definition of D_i , one of the following situations must hold:

- Case $(g, l) \in D_i$ due to (8), i.e., $m < L \land g = 0 \land l_0 = m \land \sum_{t=1}^{n-1} l_t = k$. Case m=0. Since $i \ge 1$, we must have k > 0. From $\sum_{t=1}^{n-1} l_t = k$ we obtain some \hat{t} such that $1 \leq \hat{t} < n$ and $l_{\hat{t}} > 0$. Let $\hat{l} = l[\hat{t} \mapsto l_{\hat{t}} - 1]$. Note that $G^{-1} = \lfloor \frac{(G^{-1})L}{L} \rfloor, \hat{l}_0 = 0 = (((G^{-1})L) \mod L), \text{ and } \sum_{t=1}^{n-1} \hat{l}_t =$ k-1. By (10), $(G-1, \hat{l}) \in D_{(k-1)((G-1)L+1)+(G-1)L} = D_{i-1}$. By the induction hypothesis, depth $(G-1, \hat{l}) = i-1$. By (7), $(G-1, \hat{l})$ $\rightarrow (0, l).$
 - Case $m \ge 1$. Let $\hat{l} = l[0 \mapsto m-1]$. From (8) we obtain $(0, \hat{l}) \in$ $D_{k((G-1)L+1)+m-1}$. The induction hypothesis implies depth $(0, \hat{l})$ = i-1. By (5), $(0, \hat{l}) \longrightarrow (0, l)$.

In both cases, depth $(g, l) \leq i$.

Case $(g, l) \in D_i$ due to (9), i.e., $m < L \land g = G - 1 \land l_0 = m + 1 \land \sum_{t=1}^{n-1} l_t =$ k-1. Let $\hat{l} = l[0 \mapsto m]$.

Case m=0. Note that
$$G-1 = \lfloor \frac{(G-1)L}{L} \rfloor$$
, $\hat{l}_0 = 0 = \left(\left((G-1)L \right) \right)$

- mod L), and $\sum_{t=1}^{n-1} \hat{l}_t = k-1$. According to (10), $(G-1, \hat{l}) \in$ $D_{(k-1)((G-1)L+1)+(G-1)L} = D_{i-1}.$
- Case $m \ge 1$. Then $\hat{l}_0 = m 1 + 1$ and $\sum_{t=1}^{n-1} \hat{l}_t = k 1$. By (9), $(G 1, \hat{l}) \in$

$$D_{k((G-1)L+1)+m-1} = D_{i-1}.$$

The induction hypothesis implies depth $(G-1, \hat{l}) = i-1$. By (5), $(G-1, \hat{l}) \longrightarrow (g, l)$. Therefore, depth $(g, l) \leq i$.

Case $(g, l) \in D_i$ due to (10), i.e., $L \leq m \land g = \lfloor \frac{m}{L} \rfloor \land l_0 = (m \mod L) \land$ $\sum_{t=1}^{n-1} l_t = k$. Note that $g \ge 1$.

Case $l_0=0$. Then m = gL. Let $\hat{g} = g-1$ and $\hat{l} = l[0 \mapsto L-1]$.

Then $(\hat{g}, \hat{l}) = (0, \hat{l}) \stackrel{\text{by (8)}}{\in} D_{k((G-1)L+1)+L-1} =$ Case $\hat{g}=0$. $D_{k((G-1)L+1)+gL-1}$.

Case $\hat{g} \ge 1$. Note that $\hat{g} = g + \lfloor -\frac{1}{L} \rfloor = \lfloor g - \frac{1}{L} \rfloor = \lfloor \frac{gL-1}{L} \rfloor$, $\hat{l}_0 = L-1 = ((g-1)L + L - 1) \mod L) = ((gL-1) \mod L)$, $\sum_{t=1}^{n-1} \hat{l}_t$ k = k, and $gL - 1 = (\hat{g} + 1)L - 1 \ge 2L - 1 \ge L$. By (10), $(\hat{g}, \hat{l}) \in I$ $D_{k((G-1)L+1)+gL-1}.$

From k((G-1)L+1) + gL - 1 = i-1 we obtain $(\hat{g}, \hat{l}) \in D_{i-1}$. The induction hypothesis implies depth(\hat{g}, \hat{l}) = *i*-1. By (6), (\hat{g}, \hat{l}) \longrightarrow (g, l).

Case $l_0 \ge 1$. Let $\hat{l} = l[0 \mapsto l_0 - 1]$. From $m = L\lfloor \frac{m}{L} \rfloor + (m \mod L)$ we $0 < \cdots < L$

obtain $m-1 = L\lfloor \frac{m}{L} \rfloor + (m \mod L) - 1$. Due to the uniqueness of

the quotient and the remainder, $\lfloor \frac{m-1}{L} \rfloor = \lfloor \frac{m}{L} \rfloor$ and $((m-1) \mod L)$ = $(m \mod L) - 1$. Thus, $g = \lfloor \frac{m-1}{L} \rfloor$ and $\hat{l}_0 = ((m-1) \mod L)$. Moreover, $\sum_{t=1}^{n-1} \hat{l}_t = k$. From $(m \mod L) > 0$ and $m \ge L$ we obtain m > L and therefore $m-1 \ge L$. By (10), $(g, \hat{l}) \in D_{k((G-1)L+1)+m-1}$ = D_{i-1} . The induction hypothesis implies depth $(g, \hat{l}) = i-1$. By $(5), (g, \hat{l}) \longrightarrow (g, l).$

In both cases we obtain depth $(g, l) \leq i$.

We have shown depth $(g, l) \leq i$. By (12), depth(g, l) = i.

- "⊇". Let $(g, l) \in \text{depth}^{-1}(\{i\})$ be arbitrary. There is some $(\hat{g}, \hat{l}) \in \text{depth}^{-1}(\{i-1\})$ such that $(\hat{g}, \hat{l}) \longrightarrow (g, l)$. In particular, there is some $\hat{t} < n$ such that (\hat{g}, \hat{l}_i) $\rightarrow_{\hat{t}} (g, l_{\hat{t}})$ and $\forall t \in n \setminus \{\hat{t}\}$: $\hat{l}_t = l_t$. The induction hypothesis implies $(\hat{g}, \hat{l}) \in D_{i-1}$. We distinguish the following cases.
 - Case m=0. We obtain $(\hat{g}, \hat{l}) \in D_{(k-1)((G-1)L+1)+(G-1)L}$. Since $G \ge 2$, we have $L \le (G-1)L$. By (10), $\hat{g} = G-1$, $\hat{l}_0 = 0$, and $\sum_{t=1}^{n-1} \hat{l}_t = k-1$. We consider all the choices of the thread \hat{t} and the corresponding thread transition that made the step:

Case (5), i.e., $\hat{t}=0 \land \hat{g} = g \land \hat{l}_0+1 = l_0$. Then g = G-1, $l_0=1$, and $\sum_{t=1}^{n-1} l_t = k-1$. By (9), $(g, l) \in D_{k((G-1)L+1)}$.

- Case (6), i.e., $\hat{t} = 0 \land \hat{g} + 1 = g \land \hat{l}_0 = L 1 \land l_0 = 0$. However, above we discovered $\hat{l}_0 = 0$. So 0 = L 1, contradicting $L \ge 2$.
- Case (7), i.e., $\hat{t} \ge 1 \land \hat{g} = G 1 \land \hat{l}_{\hat{t}} + 1 = l_{\hat{t}} \land g = 0$. Then $l_0 = 0$ and $\sum_{t=1}^{n-1} l_t = k$. By (8), $(g, l) \in D_{k((G-1)L+1)}$.
- Case 0 < m < L. We obtain $(\hat{g}, \hat{l}) \in D_{k((G-1)L+1)+m-1}$ with $0 \le m-1 < L$; this can happen only due to (8) or (9):
 - Case (8), i.e., $\hat{g} = 0 \land \hat{l}_0 = m-1 \land \sum_{t=1}^{n-1} \hat{l}_t = k$. We consider all the choices of the thread \hat{t} and the corresponding thread transition that made the step:

Case (5), i.e., $\hat{t}=0 \land \hat{g} = g \land \hat{l}_0+1 = l_0$. Then g = 0, $l_0 = m$, and $\sum_{t=1}^{n-1} l_t = k$. By (8), $(g, l) \in D_{k((G-1)L+1)+m}$.

Case (6), i.e., $\hat{t} = 0 \land \hat{g} + 1 = g \land \hat{l}_0 = L - 1 \land l_0 = 0$. Then m - 1 = L - 1, implying m = L, which contradicts the choice of m < L in this case of the case split.

Case (7), i.e., $\hat{t} \ge 1 \land \hat{g} = G - 1 \land \hat{l}_{\hat{t}} + 1 = l_{\hat{t}} \land g = 0$. Then $0 = \hat{g} = G - 1$, contradicting $G \ge 2$.

Case (9), i.e., $\hat{g} = G - 1 \land \hat{l}_0 = m \land \sum_{t=1}^{n-1} \hat{l}_t = k - 1$. We consider all the choices of the thread \hat{t} and the corresponding thread transition that made the step:

Case (5), i.e., $\hat{t}=0 \land \hat{g} = g \land \hat{l}_0+1 = l_0$. Then g = G-1, $l_0 = m+1$, and $\sum_{t=1}^{n-1} l_t = k-1$. By (9), $(g, l) \in D_{k((G-1)L+1)+m}$.

Case (6), i.e., $\hat{t}=0 \land \hat{g}+1 = g \land \hat{l}_0 = L-1 \land l_0 = 0$. Then G = g. \hat{f} Case (7), i.e., $\hat{t} \ge 1 \land \hat{g} = G-1 \land \hat{l}_{\hat{t}}+1 = l_{\hat{t}} \land g = 0$. Then $l_0 = m$ and $\sum_{t=1}^{n-1} l_t = k$. By (8), $(g, l) \in D_k((G-1)L+1)+m$.

Case m=L. We obtain $(\hat{g}, \hat{l}) \in D_{k((G-1)L+1)+m-1}$ where, due to $L \ge 2$, 0 < m-1 < L. Moreover, $(m-1) + 1 = L > \hat{l}_0$. By (9), $(G-1, \hat{l}) \notin D_{k((G-1)L+1)+m-1}$, and so $\hat{g} \neq G-1$. By (8), $\hat{g} = 0$, $\hat{l}_0 = L-1$, and $\sum_{t=1}^{n-1} \hat{l}_t = k$. We consider all the choices of the thread \hat{t} and the corresponding thread transition that made the step:

Case (5), i.e., $\hat{t}=0 \land \hat{g} = g \land \hat{l}_0+1 = l_0$. Then $l_0 = L$.

Case (6), i.e., $\hat{t}=0 \land \hat{g}+1 = g \land \hat{l}_0 = L-1 \land l_0 = 0$. Then $g = 1 = \lfloor \frac{m}{L} \rfloor$, $l_0 = 0 = (m \mod L)$, and $\sum_{t=1}^{n-1} l_t = k$. By (10), $(g, l) \in D_{k((G-1)L+1)+m}$.

Case (7), i.e., $\hat{t} \ge 1 \land \hat{g} = G - 1 \land \hat{l}_{\hat{t}} + 1 = l_{\hat{t}} \land g = 0$. Then 0 = G - 1, which contradicts $G \ge 2$.

Case m > L. We obtain $(\hat{g}, \hat{l}) \in D_{k((G-1)L+1)+m-1}$ where $L \leq m-1 < (G-1)L$. Then $\hat{g} = \lfloor \frac{m-1}{L} \rfloor$, $\hat{l}_0 = ((m-1) \mod L)$, and $\sum_{t=1}^{n-1} \hat{l}_t = k$. In particular, $\hat{g} \ge 1$. We consider all the choices of the thread \hat{t} and the corresponding thread transition that made the step:

- Case (5), i.e., $\hat{t}=0 \land \hat{g} = g \land \hat{l}_0+1 = l_0$. Note that $m = (m-1)+1 = \left(L\left\lfloor \frac{m-1}{L}\right\rfloor + \left((m-1) \mod L\right)\right) + 1 = L\hat{g} + \hat{l}_0 + 1 = Lg + l_0$. From $l_0 < L$ and the uniqueness of the quotient with the remainder we obtain $g = \left\lfloor \frac{m}{L} \right\rfloor$ and $l_0 = (m \mod L)$. Finally, $\sum_{t=1}^{n-1} l_t = k$. By (10), $(g, l) \in D_{k((G+1)L+1)+m}$.
- Case (6), i.e., $\hat{t}=0 \land \hat{g}+1 = g \land \hat{l}_0 = L-1 \land l_0 = 0$. Then m = (m-1)+1= $\left(L \lfloor \frac{m-1}{L} \rfloor + ((m-1) \mod L)\right) + 1 = (L\hat{g} + \hat{l}_0) + 1 = L\hat{g} + L - 1 + 1$ = $L(\hat{g}+1) = Lg$. The uniqueness of the quotient with the remainder implies $\lfloor \frac{m}{L} \rfloor = g$ and $(m \mod L) = 0 = l_0$. Finally, $\sum_{t=1}^{n-1} l_t = k$. By (10), $(g, l) \in D_k((G+1)L+1)+m$.
- Case (7), i.e., $\hat{t} \ge 1 \land \hat{g} = G 1 \land \hat{l}_{\hat{t}} + 1 = l_{\hat{t}} \land g = 0$. Then $G 1 = \lfloor \frac{m-1}{L} \rfloor$. Since m - 1 < (G - 1)L, we must have $\frac{m-1}{L} < G - 1$, and therefore $\lfloor \frac{m-1}{L} \rfloor < G - 1$.

We have now shown that, in every noncontradictory case, $(g, l) \in D_{k((G-1)L+1)+m} = D_i$.

We have proven

$$\forall i \in \mathbb{N}_{\geq 0}: D_i = \mathsf{depth}^{-1}(\{i\})$$

For

$$\hat{d} \stackrel{\text{def}}{=} ((n-1)(L-1)+1)((G-1)L+1)+L-2$$
(13)

we have $(G-1, n \times \{L-1\}) \in D_{\hat{d}}$ by (9). Therefore, $\hat{d} = \operatorname{depth}(G-1, n \times \{L-1\}) = d((0, n \times \{0\}), (G-1, n \times \{L-1\})) \leq \operatorname{diamax}(n)$. Note that $\hat{d} = (n-1)(L-1)((G-1)L+1)+(G-1)L+1+L-2 = (n-1)(L-1)(GL-L+1)+GL-L+L-1 = n(L-1)(GL-L+1)-(L-1)(GL-L+1)+GL-1 = n(L-1)(GL-L+1)+GL-1 = n(L-1)(GL-L+1)-(GL^2+L^2+L-GL+L-1)+GL-1 = n(L-1)(GL-L+1)-GL^2+L^2+2GL-2L = n(L-1)(GL-L+1)+(-L(G-1)+2(G-1))L = n(L-1)(GL-L+1)+(2-L)(G-1)L.$

Using Knuth's Big-Omega notation [50], we obtain: $L \ge 2 \Rightarrow \text{diamax}(n) = \Omega(n)$.

The proof of Theorem V.1.3 cannot be strengthened by better counting using the same program family:

Note V.1.4. The above proof computed the distance \hat{d} between two states of a particular *n*-threaded program ($n \ge 1$). It is worth asking whether there are states at a larger finite distance in this program. Now we show that the proof has reached its own limit, i.e., that it is impossible to obtain any larger finite distance in the transition graph of this program.

To this end, we first claim that, in the context of the above proof, each successor of each state in D_i lies in D_{i+1} ($i \in \mathbb{N}_{\geq 0}$). To prove this, let $i \in \mathbb{N}_{\geq 0}$ and $(g, l) \in D_i$ be arbitrary. Division with remainder gives us $k \in \mathbb{N}_{\geq 0}$ and $m \leq (G-1)L$ such that i = k((G-1)L + 1) + m. Let (g', l') be an arbitrary successor of (g, l). There is some j < n such that $(g, l_j) \rightarrow_j (g', l'_j)$ and $\forall j \in n \setminus \{j\}$: $l_j = l'_j$. We consider three cases concerning how $(g, l) \in D_i$ originated:

- Case (8), i.e., $m < L \land g = 0 \land l_0 = m \land \sum_{t=1}^{n-1} l_t = k$. Since only thread 0 has thread transitions starting with the shared state 0, we must have j=0. Thus, $l'_t = l_t$ for $t \in \mathbb{N}_+ \cap [1, n[$, and so $\sum_{t=1}^{n-1} l'_t = k$. Thread 0 has transitions of two kinds:
 - Case (5), i.e., $g' = g \land l'_0 = l_0 + 1$. Then g'=0 and $m+1 = l'_0 < L \le (G-1)L$. By (8), $(g', l') \in D_{k((G-1)L+1)+m+1}$.

Case (6), i.e., $g' = g+1 \land l_0 = L-1 \land l'_0 = 0$. From g' = 1 and $m+1 = l_0+1 = L \le (G-1)L$ we obtain $g' = \lfloor \frac{m+1}{L} \rfloor$, $l'_0 = ((m+1) \mod L)$. By (10), $(g', l') \in D_k((G-1)L+1)+m+1$.

In both cases above, $(g', l') \in D_{k((G-1)L+1)+m+1} = D_{i+1}$.

Case (9), i.e., $m < L \land g = G - 1 \land l_0 = m + 1 \land \sum_{t=1}^{n-1} l_t = k-1$. Since g + 1 = G, the transition (6) of thread 0 cannot be taken from (g, l), and so there may be only two cases for *j* and the thread transition taken:

Case (5), i.e., $j=0 \land g' = g \land l'_0 = l_0+1$. From g' = G-1, $(m+1)+1 = l'_0 \leq L \leq (G-1)L$, and $\sum_{t=1}^{n-1} l'_t = \sum_{t=1}^{n-1} l_t = k-1$ we obtain $(g', l') \in D_{k((G-1)L+1)+m+1}$ by (9).

Case (7), i.e., $j \ge 1 \land g' = 0 \land l'_j = l_j + 1$. From $m+1 = l'_0 < L$ and $\sum_{t=1}^{n+1} l'_t = 1 + \sum_{t=1}^{n+1} l_t = k$ we obtain $(g', l') \in D_{k((G-1)L+1)+m+1}$ by (8).

In both cases above, $(g', l') \in D_{k((G-1)L+1)+m+1} = D_{i+1}$.

- Case (10), i.e., $m \ge L \land g = \lfloor \frac{m}{L} \rfloor \land l_0 = (m \mod L) \land \sum_{t=1}^{n-1} l_t = k$. All three cases for *j* and the transition taken are possible:
 - Case (5), i.e., $j=0 \land g = g' \land l'_0 = l_0+1$. From j = 0 we obtain $\sum_{t=1}^{n-1} l'_t = k$. We distinguish two subcases:

Case m < (G-1)L. So $L < m+1 \leq (G-1)L$. Note that $m+1 = \left(L \lfloor \frac{m}{L} \rfloor + \right)$

 $(m \mod L)$ + 1 = $Lg + l_0 + 1 = Lg' + l'_0$. From $0 \le l'_0 < L$ and the uniqueness of the quotient with the remainder, we obtain $g' = \lfloor \frac{m+1}{L} \rfloor$ and $l'_0 = ((m+1) \mod L)$. By (10), $(g', l') \in D_{k((G-1)L+1)+m+1}$.

Case m = (G-1)L. Then $g = G - 1 \land l_0 = 0$. So $g' = G - 1 \land l'_0 = 1$. For k' = k+1 and m' = 0 we obtain $l'_0 = m'+1 \land \sum_{t=1}^{n-1} l'_t = k'-1$. By (9), $(g', l') \in D_{k'((G-1)L+1)+m'}$. Note that k'((G-1)L+1) + m' = (k+1)((G-1)L+1) + 0 = k((G-1)L+1) + (G-1)L + 1 = k((G-1)L + 1) + m + 1. Thus, $(g', l') \in D_{k((G-1)L+1)+m+1}$.

In both cases above, $(g', l') \in D_{k((G-1)L+1)+m+1} = D_{i+1}$.

- Case (6), i.e., $j=0 \land g+1 = g' \land l_0 = L-1 \land l'_0 = 0$. Since $(m \mod L) = L-1 \ge 1$, we obtain that *m* cannot be divisible by *L*. So m < (G-1)L. Let m' = m+1; then $m' \le (G-1)L$. Note that $m' = m+1 = \left(L \lfloor \frac{m}{L} \rfloor + (m \mod L)\right) + 1$ $= (Lg + l_0) + 1 = Lg + L - 1 + 1 = Lg + L = L(g+1) = Lg'$. Therefore, $g' = \lfloor \frac{m'}{L} \rfloor$ and $0 = (m' \mod L)$. Thus, $l'_0 = (m' \mod L)$. Moreover, from j = 0 we obtain $\sum_{t=1}^{n-1} l'_t = k$. By (10), $(g', l') \in D_{k((G-1)P+1)+m'} = D_{i+1}$.
- Case (7), i.e., $j \ge 1 \land g = G 1 \land g' = 0 \land l'_j = l_j + 1$. If *m* were smaller than (G-1)L, then we would have $\frac{m}{L} < G 1 = g = \lfloor \frac{m}{L} \rfloor \le \frac{m}{L}$, $\frac{1}{2}$. Therefore, m = (G-1)L. Thus, $l_0 = 0$. Let m' = 0 and k' = k+1; then $m' = l_0 = l'_0$ and $\sum_{t=1}^{n-1} l'_t = 1 + \sum_{t=1}^{n-1} l_t = k+1 = k'$. By (8), $(g', l') \in D_{k'((G-1)L+1)+m'} = [\text{since } k'((G-1)L+1) + m' = k'((G-1)L+1) = k((G-1)L+1) + (G-1)L+1 = k((G-1)L+1) + m+1 = i+1] D_{i+1}$.

In all three cases above, $(g', l') \in D_{i+1}$.

We have shown that successors of states of D_i lie in D_{i+1} ($i \in \mathbb{N}_{\geq 0}$), or, more formally:

$$\forall i \in \mathbb{N}_{\geq 0}, s \in D_i, s' \in \text{State}: s \longrightarrow s' \Rightarrow s' \in D_{i+1}.$$
(14)

Second, we claim that all states lie in $\bigcup_{i \in \mathbb{N}_{\geq 0}} D_i$. To prove this, let $(g, l) \in$ State be arbitrary. For each of the following cases, we find $k, m \in \mathbb{N}_{\geq 0}$ so that $m \leq (G-1)L$ and $(g',l')\in D_{k((G-1)L+1)+m}:$

Case g = 0. Choose $m = l_0$ and $k = \sum_{t=1}^{n-1} l_t$. Noting that m < L, apply (8). Case 0 < g < G-1. Choose $m = gL+l_0$ and $k = \sum_{t=1}^{n-1} l_t$. Note that $L \le m \le (G-2)L+L-1 = (G-1-1)L+L-1 = (G-1)L-L+L-1 = (G-1)L-1 < (G-1)L$ and apply (10).

Case $g = G - 1 \wedge l_0 = 0$. Choose m = gL and $k = \sum_{t=1}^{n-1} l_t$. Note that $L \leq m = (G-1)L$ and apply (10).

Case $g = G - 1 \land l_0 \ge 1$. Choose $m = l_0 - 1$ and $k = 1 + \sum_{t=1}^{n-1} l_t$. Note that m < L and apply (9).

We have shown

State
$$\subseteq \bigcup_{i \in \mathbb{N}_{\geq 0}} D_i$$
. (15)

Third, we obtain $D_{k((G-1)L+1)+m} = \emptyset$ (at least) for the following $(k, m) \in \mathbb{N}_{\geq 0}^2$ such that $m \leq (G-1)L$:

• k = (L-1)(n-1) + 1 and m = L-1 according to (8) and (9),

• k = (L-1)(n-1) + 1 and $m \ge L$ according to (10),

• $k \ge (L-1)(n-1) + 2$ according to (8), (9), and (10).

Therefore:

$$D_i = \emptyset$$
 for all $i \ge ((L-1)(n-1)+1)((G-1)L+1)+L-1$. (16)

Now, let $s, s' \in$ State be arbitrary such that $s \longrightarrow^* s'$. By (15), there are some $i, j \in \mathbb{N}_{\geq 0}$ such that $s \in D_i$ and $s' \in D_j$. By (13) and (16), $i, j \leq \hat{d}$. By (14), d(s, s') = 1 $i-i \leq i \leq \hat{d}$. Since s, s' were arbitrary, the diameter of the program is at most \hat{d} . Since the proof of Theorem V.1.3 provided us with two states at this distance, \hat{d} is the exact value of the diameter of the program. П

Of course, (GL-L+1)(L-1)n + (2-L)(G-1)L is only a lower bound and in general not the exact value of the diamax function. We already saw in § III.2 examples, such as N2T6B or N2T7A, of diameter $7 > 6 = 3n = (2 \cdot 2 - 2 + 1)(2 - 1)n + (2 - 2)(2 - 1) \cdot 2$ for n = G = L = 2. Duplicating existing threads in these examples did not raise the diameter above the lower bound. As we will see in Corollary V.2.2.4, adding arbitrarily many arbitrary threads to these examples would not raise the bound beyond 3n + 1 anyway. A few more exceptions, i.e., *n*-threaded programs (for small *n*; in the binary case, for $n \leq 4$) for which the diameter exceeds the lower bound, are known. No such exception is known to have generalizations for infinitely many n that could asymptotically improve Theorem V.1.3. Further, on a set of over $6 \cdot 10^{24}$ *n*-threaded binary programs with $n \ge 5$, no deviations from the lower bound have been observed [62].

V.2. Upper bounds

V.2.1. An upper bound on diamax

We are going to show that diamax is asymptotically majorized by a polynomial function.

We start the proof by fixing an arbitrary program $(\rightarrow_i)_{i \le n}$ and an arbitrary state $(g, l) \in$ State of this program until (but not including) Proposition V.2.1.21. We are going to prove that $d((g, l), s) \stackrel{!}{=} O(n^c)$ for all $s \in State$, where c > 1, and neither *c* nor the constant hidden in the O-notation depend on *g*, *l*, *s*, *n*, or the program. From a high-level view, our proof will exploit symmetries between the threads.

As a first step, we "confuse" thread indexes if the local parts corresponding to these thread indexes in the initial state are equal and the threads' transition relations are equal up to self-loops. Formally:

Definition V.2.1.1. Consider the *diagonal* $D \stackrel{\text{def}}{=} \text{id}_{\text{Glob}\times\text{Loc}}$. Fix for each i < n a permutation ι_i : Loc \rightarrow Loc such that $\iota_i(l_i) = l_0$. (For example, for each i < n choose the transposition $\lambda \ a \in \text{Loc}$. if $a = l_0$ then l_i elif $a = l_i$ then l_0 else a; we call such a choice *standard*.) Let *the i*th normalized thread transition relation be $\sim_i \stackrel{\text{def}}{=} \{((x, \iota_i(y)), (x', \iota_i(y'))) \mid ((x, y), (x', y')) \in \rightarrow_i \backslash D\} (i < n)$. Thread identifiers i, j < n are called *confusable*, written $i \sim j$, iff $\sim_i = \sim_j$.

Note that \sim is an equivalence relation on *n*.

Next, we define which program states should be considered indistinguishable for our purposes: such states result from each other by re-indexing threads with confusable identifiers, provided that the local parts corresponding to these thread identifiers match properly. Formally:

Definition V.2.1.2. A map $\varphi: n \to n$ is called *confusion-invariant*, or shortly *~-invariant*, iff $\forall i < n: i \sim \varphi(i)$. Vectors of local states $\hat{l}, \check{l} \in \text{Loc}^n$ of dimension *n* are called *confusable*, written $\hat{l} \approx \check{l}$, iff there is a *~*-invariant $\varphi \in (n \hookrightarrow n)$ such that $\forall i < n: \iota_i(\hat{l}_i) = \iota_{\varphi(i)}(\check{l}_{\varphi(i)})$. Program states $(\hat{g}, \hat{l}), (\check{g}, \check{l})$ are called *confusable*, written $(\hat{g}, \hat{l}) \approx (\check{g}, \check{l})$, iff $\hat{g} = \check{g} \land \hat{l} \approx \check{l}$.

Intuitively speaking, φ in the above definition re-indexes threads with confusable identifiers provided the corresponding local parts match properly.

Example V.2.1.3. Consider the binary case Glob = { α , β } and Loc = {0, 1} and a two-threaded program ({(α 0, α 1), (α 1, α 1)}, {(α 1, α 0), (β 1, β 1)}). Let the initial state be (g, l) = (α , (0, 1)). Choose $\iota_0 = id_{Loc}$ and $\iota_1 = \lambda a \in Loc. 1-a$. Then $\sim_0 = \{(\alpha 0, \alpha 1)\}$ and $\sim_1 = \{(\alpha 0, \alpha 1)\}$. So $0 \sim 1$ and $1 \sim 0$. The transposition $\varphi = \lambda i < 2$. 1-i, which swaps the indexes of the threads, is ~-invariant. Let (\hat{g}, \hat{l}) = (β , (0, 0)) and (\check{g}, \check{l}) = (β , (1, 1)). From $\iota_0(\hat{l}_0) = 0 = 1-1 = \iota_1(\check{l}_1) = \iota_{\varphi(0)}(\check{l}_{\varphi(0)})$ and $\iota_1(\hat{l}_1) = 1-0 = 1 = \iota_0(\check{l}_0) = \iota_{\varphi(1)}(\check{l}_{\varphi(1)})$ we get $\hat{l} \approx \check{l}$. As $\hat{g} = \check{g}$, the program states (\hat{g}, \hat{l}) and (\check{g}, \check{l}) are confusable. Here, \approx is not a classic bisimulation [7, Definition 7.1]: (\check{g}, \check{l}) has a successor program state (namely, itself), whereas (\hat{g}, \hat{l}) has no successors.

The identity on thread indexes is ~-invariant:

Lemma V.2.1.4. $id_n: n \hookrightarrow n \text{ is } \sim \text{-invariant.}$

Proof. For any *i*<*n*, we have $i \sim i = id_n(i)$.

The inverses of \sim -invariant permutations of *n* are \sim -invariant themselves:

Lemma V.2.1.5. If $\varphi: n \hookrightarrow n$ is ~-invariant, so is φ^{-1} .

Proof. Let φ : $n \hookrightarrow n$ be ~-invariant.

Let i < n be arbitrary. Let $j \stackrel{\text{def}}{=} \varphi^{-1}(i)$. Then $j \sim \varphi(j)$. Therefore, $i = \varphi(\varphi^{-1}(i)) = \varphi(j)$ [since ~ is symmetric] $j = \varphi^{-1}(i)$.

Summarizing, $i \sim \varphi^{-1}(i)$ for all i < n.

Composing ~-invariant maps yields a ~-invariant map:

Lemma V.2.1.6. If $\varphi, \psi : n \rightarrow n$ are \sim -invariant, so is $\psi \circ \varphi$.

Proof. For any *i*<*n*, we have $i \sim \varphi(i) \sim \psi(\varphi(i))$ by Definition V.2.1.2, and, therefore, $i \sim \psi(\varphi(i))$.

Confusion of *n*-vectors of local states is an equivalence relation:

Lemma V.2.1.7. \asymp *is an equivalence relation on* Loc^{n} .

Proof. We show:
"≍ is reflexive on Locⁿ": Due to Lemma V.2.1.4.
"≍ is symmetric": Let î × l. Choose a ~-invariant φ: n→n such that ∀i < n: ι_i(î_i) = ι_{φ(i)}(l_{φ(i)}). Lemma V.2.1.5 implies that φ⁻¹ is ~-invariant. Also, ∀i < n: ι_{φ⁻¹(φ(i))}(l_{φ⁻¹(j)}) = ι_{φ(i)}(l_{φ(i)}). The surjectivity of φ implies that ∀j < n: ι_{φ⁻¹(j)}(l_{φ⁻¹(j)}) = ι_j(l_j). So l× l.
"≍ is transitive": Let î × l × l. There are ~-invariant φ, ψ: n→n such that ∀i < n: ι_i (l_i) = ι_{φ(i)}(l_{φ(i)}) and ∀j < n: ι_j(l_j) = ι_{ψ(j)}(l_{ψ(j)}). By Lemma V.2.1.6, ψ∘φ is

 $(l_i) = \iota_{\varphi(i)}(l_{\varphi(i)})$ and $\forall j < n : \iota_j(l_j) = \iota_{\psi(j)}(l_{\psi(j)})$. By Lemma V.2.1.6, $\psi \circ \varphi$ is \sim -invariant. Also, for all i < n we have $\iota_i(\hat{l}_i) = \iota_{\varphi(i)}(\check{l}_{\varphi(i)}) = \iota_{\psi(\varphi(i))}(\bar{l}_{\psi(\varphi(i))})$. Therefore, $\hat{l} \times \bar{l}$.

To show that confusion of program states is an equivalence relation, we recall a well-known fact:

Lemma V.2.1.8. Let X and Y be sets, = an equivalence relation on X, = an equivalence relation on Y, $Z = X \times Y$, and = a binary relation on Z defined via $(x_1, y_1) = (x_2, y_2)$ $\stackrel{def}{\Leftrightarrow} x_1 = x_2 \land y_1 = y_2 (x_1, x_2 \in X, y_1, y_2 \in Y)$. Then:

a) \Rightarrow is an equivalence relation on Z, and b) $|Z_{\pm}| = |X_{\pm}||Y_{\pm}|$.

Proof. The definition immediately implies that \doteq is reflexive on Z, symmetric and transitive, implying part a). The map $\varphi: \mathbb{Z}_{\doteq} \to \mathbb{X}_{\geq} \times \mathbb{Y}_{\doteq}, [(x, y)]_{\doteq} \mapsto ([x]_{=}, [y]_{=})$ is well defined and a bijection, implying part b).

Lemmas V.2.1.7 and V.2.1.8a) imply directly:

Corollary V.2.1.9. \approx *is an equivalence relation on* State.

For computing distances, the self-loops in the transition graph are irrelevant, which motivates the following notion:

Definition V.2.1.10. A *simple* thread transition relation is a member of $E \stackrel{\text{def}}{=} \mathfrak{P}((\text{Glob} \times \text{Loc})^2 \setminus D)$.

Above, E stands for "edges". Using this shorthand for simple thread transition relations, we can express the notion of state confusion differently; instead of mentioning bijections we can say that certain sets are equally large:

Lemma V.2.1.11. For all $\hat{l}, \check{l} \in \text{Loc}^n$, we have $\hat{l} \asymp \check{l}$ iff $\forall a \in \text{Loc}, \Rightarrow \in E : |\{t < n \mid \\ \rightsquigarrow_t = \Rightarrow \land \iota_t(\hat{l}_t) = a\}| = |\{t < n \mid \\ \rightsquigarrow_t = \Rightarrow \land \iota_t(\check{l}_t) = a\}|.$

Proof. Let $\hat{l}, \check{l} \in Loc^n$ be arbitrary. We show the two directions of the aforementioned bi-implication separately.

" \Rightarrow ": We assume the left-hand side $\hat{l}=\check{l}$. Then a ~-invariant $\varphi: n \hookrightarrow n$ exists such that

$$\forall i < n: \ \iota_i(\hat{l}_i) = \iota_{\varphi(i)}(\hat{l}_{\varphi(i)}).$$
(17)

The ~-invariance of φ means

$$\forall i < n: \ i \sim \varphi(i) \,. \tag{18}$$

Let $a \in \text{Loc}$ and $\Rightarrow \in E$. We claim that the map $\overline{\varphi}$: $\{t < n \mid \rightsquigarrow_t = \Rightarrow \land \iota_t(\hat{l}_t) = a\}$ $\rightarrow \{t < n \mid \rightsquigarrow_t = \Rightarrow \land \iota_t(\check{l}_t) = a\}, t \mapsto \varphi(t)$ is well defined and a bijection. We prove this claim now:

" $\bar{\varphi}$ is well defined": Let $t \in n$ be such that $\rightsquigarrow_t = \Rightarrow \land \iota_t(\hat{l}_t) = a$. From (17) we obtain $\iota_{\varphi(i)}(\check{l}_{\varphi(t)}) = a$. From (18) we obtain $\rightsquigarrow_t = \rightsquigarrow_{\varphi(t)}$, implying $\rightsquigarrow_{\varphi(t)} = \Rightarrow$. Therefore, $\varphi(t) \in \{t < n \mid \rightsquigarrow_t = \Rightarrow \land \iota_t(\check{l}_t) = a\}$. " $\bar{\varphi}$ is injective": Follows from the injectivity of φ .

" $\overline{\varphi}$ is surjective": Let t < n be given such that $\rightarrow_t = \Rightarrow \land \iota_t(\check{t}_t) = a$. Then $\varphi^{-1}(t) \in n$, and $\rightarrow_{\varphi^{-1}(t)} = [$ due to (18) $] \rightarrow_{\varphi(\varphi^{-1}(t))} = \rightarrow_t = \Rightarrow$, and $\iota_{\varphi^{-1}(t)}(\hat{l}_{\varphi^{-1}(t)}) = [$ due to (17) $] \iota_{\varphi(\varphi^{-1}(t))}(\check{l}_{\varphi(\varphi^{-1}(t))}) = \iota_t(\check{t}_t) = a$. So $\varphi^{-1}(t) \in \text{dom } \overline{\varphi}$. Also, $\overline{\varphi}(\varphi^{-1}(t)) = t$.

We have shown that $\bar{\varphi}$ is a well-defined bijection. Therefore, $|\operatorname{dom} \bar{\varphi}| = |\operatorname{img} \bar{\varphi}|$. " \Leftarrow ": We assume the right-hand side, i.e., $\forall a \in \operatorname{Loc}$, $\Rightarrow \in E : |\{t < n \mid \rightsquigarrow_t = \Rightarrow \land \iota_t(\hat{l}_t) = a\}| = |\{t < n \mid \rightsquigarrow_t = \Rightarrow \land \iota_t(\tilde{l}_t) = a\}|$. Then for each pair $(\Rightarrow, a) \in E \times \operatorname{Loc}$ there is a bijection $\gamma_{\rightsquigarrow,a} : \{t < n \mid \rightsquigarrow_t = \Rightarrow \land \iota_t(\hat{l}_t) = a\} \hookrightarrow \{t < n \mid \rightsquigarrow_t = \Rightarrow \land \iota_t(\check{l}_t) = a\}$. These bijections have pairwise disjoint domains. The images of these bijections are also pairwise disjoint. The union of the domains is *n*, and the union of the images is also *n*. Consider the map

$$\varphi \stackrel{\text{def}}{=} \bigcup_{\substack{\longrightarrow \in E, a \in \text{Loc}}} \gamma_{\stackrel{\longrightarrow}{\to},a}$$

Then φ is a permutation of *n*. If i < n is arbitrary, then $\varphi(i) = \gamma_{\rightarrow_i, \iota_i(\hat{l}_i)}(i)$, and so $\sim_i = \sim_{\gamma_{\rightarrow_i, \iota_i(\hat{l}_i)}(i)} = \sim_{\varphi(i)}$. So φ is ~-invariant. Moreover, $\forall i < n : \iota_i(\hat{l}_i) = \iota_{\gamma_{\rightarrow_i, \iota_i(\hat{l}_i)}(i)}(\check{l}_{\gamma_{\rightarrow_i, \iota_i(\hat{l}_i)}(i)}) = \iota_{\varphi(i)}(\check{l}_{\varphi(i)})$. Thus, $\hat{l} \asymp \check{l}$.

Lemma V.2.1.11 implies directly:

Corollary V.2.1.12. For all $(\hat{g}, \hat{l}), (\check{g}, \check{l}) \in$ State we have $(\hat{g}, \hat{l}) \approx (\check{g}, \check{l})$ iff $\hat{g} = \check{g} \land (\forall a \in \text{Loc}, \Rightarrow \in E : |\{t < n \mid \leadsto_t = \Rightarrow \land \iota_t(\hat{l}_t) = a\}| = |\{t < n \mid \leadsto_t = \Rightarrow \land \iota_t(\check{l}_t) = a\}|$. \Box

So program states are confusable iff they are the same up to renumbering the threads with confusable identifiers and renaming local states according to the permutations ι_i (*i* < *n*).

In the proofs of the following claims, both views of state confusion come in handy.

The next lemma, which says that confusable program states have the same distance from the fixed one, is crucial for the whole section.

Lemma V.2.1.13. $\forall s, s' \in \text{State}: s \approx s' \implies d((g, l), s) = d((g, l), s').$

Proof. We will show the claim by induction on $\min\{d((g, l), s), d((g, l), s')\}$, proving $\forall m \in \mathbb{N}_{\geq 0} \cup \{\infty\}$: $\forall s, s' \in \text{State}$: $(s \approx s' \land \min\{d((g, l), s), d((g, l), s')\} = m) \Rightarrow d((g, l), s) = d((g, l), s')$.

So let an arbitrary $m \in \mathbb{N}_{\geq 0} \cup \{\infty\}$ be given, and assume that $\forall m' < m \colon \forall s, s' \in$ State: $(s \approx s' \land \min\{d((g, l), s), d((g, l), s')\} = m') \Rightarrow d((g, l), s) = d((g, l), s')$. Let $s, s' \in$ State be given such that $s \approx s'$ and $\min\{d((g, l), s), d((g, l), s')\} = m$. Three cases can occur: *m* is zero, *m* is a positive natural number, or *m* is infinity. Case m=0.

Case d((g, l), s) = 0. Then s = (g, l). Let $(\check{g}, \check{l}) = s'$. From $s \approx s'$ we obtain $g = \check{g}$ and some \sim -invariant permutation $\varphi : n \hookrightarrow n$ such that $\forall i < n : \iota_i(l_i) = \iota_{\varphi(i)}(\check{l}_{\varphi(i)})$. The definition of ι_i (i < n) implies $\forall i < n : \iota_i(l_i) = l_0 = \iota_{\varphi(i)}(l_{\varphi(i)})$. Therefore, $\forall i < n : \iota_{\varphi(i)}(\check{l}_{\varphi(i)}) = \iota_{\varphi(i)}(l_{\varphi(i)})$. Since $\iota_{\varphi(i)}$ is injective for all i < n, $\forall i < n : \check{l}_{\varphi(i)} = l_{\varphi(i)}$. Since φ is onto, $\forall j < n : l_j = \check{l}_j$. So $l = \check{l}$. Thus, s = s'. Hence, d((g, l), s) = d((g, l), s').

Case d((g, l), s') = 0. We have s' = (g, l). Now, let $(\check{g}, \check{l}) = s$. From $s \approx s'$ we obtain $\check{g}=g$ and some ~-invariant permutation $\varphi: n \hookrightarrow n$ such that $\forall i < n: \iota_i(\check{l}_i) = \iota_{\varphi(i)}(l_{\varphi(i)})$. The definition of ~ implies $\forall i < n: \iota_i(l_i) = l_0 = \iota_{\varphi(i)}(l_{\varphi(i)})$. Therefore, $\forall i < n: \iota_i(\check{l}_i) = \iota_i(l_i)$. Since ι_i is injective for all $i < n, \forall i < n: \check{l}_i = l_i$. So $\check{l}=l$. Thus, s=s'. Hence, d((g,l), s) = d((g,l), s').

Case $0 < m < \infty$. Let $(\check{g}, \check{l}) = s$ and $(\hat{g}, \hat{l}) = s'$. From $s \approx s'$ we obtain $\check{g} = \hat{g}$ and some ~-invariant permutation $\varphi : n \hookrightarrow n$ such that $\forall i < n : \iota_i(\check{l}_i) = \iota_{\varphi(i)}(\hat{l}_{\varphi(i)})$.

Case d((g, l), s) = m. Choose a predecessor (\check{g}', \check{l}') of (\check{g}, \check{l}) such that

$$d((g,l), (\check{g}', \check{l}')) = m - 1.$$
(19)

Let $\hat{l}' = \lambda i \in n$. $\iota_i^{-1}(\iota_{\varphi^{-1}(i)}(\check{l}'_{\varphi^{-1}(i)}))$. Then $\forall j < n : \iota_{\varphi(j)}(\hat{l}'_{\varphi(j)}) = \iota_{\varphi(j)}(\iota_{\varphi(j)})(\check{l}'_{\varphi^{-1}(\varphi(j))})) = \iota_j(\check{l}'_j)$. So $(\check{g}',\check{l}') \approx (\check{g}',\hat{l}')$. The induction hypothesis implies $d((g, l), (\check{g}', \hat{l}')) = m - 1$. Choose some $t \in n$ such that $(\check{g}', \check{l}'_t) \rightarrow_t (\check{g}, \check{l}_t) \land \forall \bar{t} \in n \setminus \{t\} : \check{l}'_t = \check{l}_t$. Due to (19) and the distance condition in this branch of the case split, $(\check{g}', \check{l}'_t) \neq (\check{g}, \check{l}_t)$. Therefore, $(\check{g}', \iota_t(\check{l}'_t)) \sim_t (\check{g}, \iota_t(\check{l}_t))$. Since $t \sim \varphi(t)$, we have $\rightsquigarrow_t = \rightsquigarrow_{\varphi(t)}$. So $(\check{g}', \iota_t(\check{l}'_t))$. Knowing in addition that $\hat{l}'_{\varphi(t)} = \iota_{\varphi(t)}^{-1}(\iota_{\varphi(t)}(\check{\ell}_{\varphi(t)})) \rightarrow_{\varphi(t)} (\check{g}, \iota_{\varphi(t)}^{-1}(\iota_t(\check{l}_t)))$. Knowing in addition that $\hat{l}'_{\varphi(t)} = \iota_{\varphi(t)}^{-1}(\iota_{\varphi(t)}) = \hat{\ell}_{\varphi(t)} (\iota_{\varphi(t)}(\check{\ell}_{\varphi(t)})) = \hat{\ell}_{\varphi(t)} (\check{g}, \hat{\ell}_{\varphi(t)}) \rightarrow_{\varphi(t)} (\check{g}, \hat{\ell}_{\varphi(t)})$. Moreover, $\forall \bar{t} \in n \setminus \{\varphi(t)\} : \hat{l}'_t = \iota_t^{-1}(\iota_{\varphi^{-1}(\varphi(t)})) = [from \bar{t} \neq \varphi(t)$ we get $\varphi^{-1}(\bar{t}) \neq t] \iota_t^{-1}(\iota_{\varphi(t)}(\check{\ell}_{\varphi^{-1}(\bar{t})})) = \iota_t^{-1}(\iota_t(\hat{\ell}_t))) = \iota_t^{-1}(\iota_t(\hat{\ell}_t))$. Thus, $d((g, l), (\hat{g}, \hat{l})) \leqslant m = \min\{d((g, l), (\check{g}, \check{l})), d((g, l), (\hat{g}, \hat{l}))\} \leqslant d((g, l), (\hat{g}, \hat{l}))$. Therefore, d((g, l), (g, l)).

Case d((g, l), s') = m. Analogously as follows. Choose a predecessor (\hat{g}', \hat{l}') of (\hat{g}, \hat{l}) such that

$$d((g,l),(\hat{g}',\hat{l}')) = m-1.$$
(20)

Let $\check{l}' = \lambda i \in n$. $\iota_i^{-1}(\iota_{\varphi(i)}(\hat{l}'_{\varphi(i)}))$. Due to Lemma V.2.1.5, φ^{-1} is ~-invariant. Moreover, $\forall j < n$: $\iota_{\varphi^{-1}(j)}(\check{l}'_{\varphi^{-1}(j)}) = \iota_{\varphi^{-1}(j)}(\iota_{\varphi^{-1}(j)}^{-1}(\iota_{\varphi(\varphi^{-1}(j))}(\hat{l}'_{\varphi(\varphi^{-1}(j))})))$ $= \iota_j(\hat{l}'_j)$. So $(\hat{g}', \hat{l}') \approx (\hat{g}', \check{l}')$. The induction hypothesis implies $d((g, l), (\hat{g}', \check{l}')) = m-1$. Choose some $t \in n$ such that $(\hat{g}', \hat{l}'_t) \to_t (\hat{g}, \hat{l}_t)$ and $\forall \bar{t} \in n \setminus \{t\}$: $\hat{l}'_t = \hat{l}_t$. Due to (20) and the distance condition in this branch of the case split, $(\hat{g}', \hat{l}'_t) \neq (\hat{g}, \hat{l}_t)$. Therefore, $(\hat{g}', \iota_t(\hat{l}'_t)) \rightsquigarrow_t (\hat{g}, \iota_t(\hat{l}_t))$. Note that $t \sim \varphi^{-1}(t)$; thus, $\rightsquigarrow_t = \leadsto_{\varphi^{-1}(t)}$. So $(\hat{g}', \iota_t(\hat{l}'_t)) \leadsto_{\varphi^{-1}(t)} (\hat{g}, \iota_t(\hat{l}_t))$. Thus, $\begin{aligned} & (\hat{g}', \iota_{\varphi^{-1}(t)}^{-1}(\iota_{t}(\hat{l}'_{t}))) \to_{\varphi^{-1}(t)} (\hat{g}, \iota_{\varphi^{-1}(t)}^{-1}(\iota_{t}(\hat{l}_{t}))). \text{ Knowing in addition that} \\ & \check{l}'_{\varphi^{-1}(t)} = \iota_{\varphi^{-1}(t)}^{-1}(\iota_{\varphi(\varphi^{-1}(t))}(\hat{l}'_{\varphi(\varphi^{-1}(t))})) = \iota_{\varphi^{-1}(t)}^{-1}(\iota_{t}(\hat{l}'_{t})) \text{ and } \iota_{\varphi^{-1}(t)}^{-1}(\iota_{t}(\hat{l}_{t})) \\ &= \iota_{\varphi^{-1}(t)}^{-1}(\iota_{\varphi(\varphi^{-1}(t))}(\hat{l}_{\varphi(\varphi^{-1}(t))})) = \iota_{\varphi^{-1}(t)}^{-1}(\iota_{\varphi^{-1}(t)}(\check{l}_{\varphi^{-1}(t)})) = \check{l}_{\varphi^{-1}(t)}^{-1}(\iota_{\varphi(t)}(\hat{l}_{\varphi(t)})) \\ &= \iota_{q}^{-1}(\iota_{\varphi(t)}(\hat{l}'_{\varphi(t)})) \to_{\varphi^{-1}(t)} (\hat{g}, \check{l}_{\varphi^{-1}(t)}). \text{ Moreover, } \forall \bar{t} \in n \setminus \{\varphi^{-1}(t)\}: \check{l}'_{\bar{t}} = \iota_{\bar{t}}^{-1}(\iota_{\varphi(t)}(\hat{l}'_{\varphi(t)})) = [\text{since } \bar{t} \neq \varphi^{-1}(t), \text{ we have } \varphi(\bar{t}) \neq t] \iota_{\bar{t}}^{-1}(\iota_{\varphi(\bar{t})}(\hat{l}_{\varphi(\bar{t})})) = \\ &\iota_{\bar{t}}^{-1}(\iota_{\bar{t}}(\check{l}_{\bar{t}})) = \check{l}_{\bar{t}}. \text{ Combining, } (\hat{g}', \check{l}') \longrightarrow (\hat{g}, \check{l}) = (\check{g}, \check{l}). \text{ Thus, } d((g, l), (\check{g}, \\ \check{l})) \leqslant m = \min\{d((g, l), (\check{g}, \check{l})), d((g, l), (\hat{g}, \hat{l}))\} \leqslant d((g, l), (\check{g}, \check{l})). \text{ Therefore,} \\ &d((g, l), (\check{g}, \check{l})) = m = d((g, l), (\hat{g}, \hat{l})). \end{aligned}$

Case $m=\infty$. Then both d((g, l), s) and d((g, l), s') must be ∞ . In particular, they are equal.

From this result we directly conclude:

Lemma V.2.1.14. $\forall s \in \text{State: } d((g, l), s) < \infty \Rightarrow d((g, l), s) < |\frac{\text{State}}{2}|$.

Proof. Let $s \in S$ tate have a finite distance from (g, l). Take a shortest walk $(\sigma^{[0]}, \ldots, \sigma^{[k]})$ in the program's transition graph such that $\sigma^{[0]} = (g, l)$ and $\sigma^{[k]} = s$. Then k = d((g, l), s). Note that $\forall i \leq k : d((g, l), \sigma^{[i]}) = i$. Lemma V.2.1.13 implies that $\sigma^{[i]} \neq \sigma^{[j]}$ for all $i, j \in \mathbb{N}_{\geq 0}$ such that $i, j \leq k$ and $i \neq j$. Thus, the relation \approx has at least k+1 equivalence classes. That is, $|S_{tate} \neq d((g, l), s) + 1 > d((g, l), s)$.

Computing the index of \approx can be reduced to computing the index of \approx :

Lemma V.2.1.15. $|\text{State}_{\approx}| = G|\text{Loc}^n/\text{K}|$.

Proof. Follows from Definition V.2.1.2 and Lemma V.2.1.8b).

To get a polynomial upper bound on the index of \asymp , we establish an injection from the set of equivalence classes to another set for which the cardinality will be easy to determine.

Let *V* be the set of the maps $f: E \to Loc \to \mathbb{N}_{\geq 0}$ such that, for each simple thread transition relation \Rightarrow , the sequence of values of $f(\Rightarrow)$ forms a partition of the number of all the threads that have \Rightarrow as the normalized thread transition relation. Formally:

$$V \stackrel{\text{def}}{=} \left\{ f \in (E \to \text{Loc} \to \mathbb{N}_{\geq 0}) \mid \forall \rightrightarrows \in E \colon ||f(\rightrightarrows)||_1 = \left| \{t < n \mid \rightsquigarrow_t = \rightrightarrows \} \right| \right\}.$$
(21)

Lemma V.2.1.16. There is an injection from $\operatorname{Loc}^n_{\nearrow}$ to V.

Proof. Let $\varphi \colon \operatorname{Loc}^n \to E \to \operatorname{Loc} \to \mathbb{N}_{\geq 0}, \ \hat{l} \mapsto \lambda \rightrightarrows \in E \cdot \lambda \ a \in \operatorname{Loc}. |\{t < n \mid \rightsquigarrow_t = \rightrightarrows \land \iota_t(\hat{l}_t) = a\}|.$

First, we show that $\operatorname{img} \varphi \stackrel{!}{\subseteq} V$. For that, let $\hat{l} \in \operatorname{Loc}^n$ be arbitrary; it suffices to show that $\varphi(\hat{l}) \stackrel{!}{\in} V$. Note that $\varphi(\hat{l}) \in (E \to \operatorname{Loc} \to \mathbb{N}_{\geq 0})$. Then $\|\varphi(\hat{l})(\rightrightarrows)\|_1 = \sum_{a \in \operatorname{Loc}} |\{t < n \mid \rightsquigarrow_t = \rightrightarrows \land \iota_t(\hat{l}_t) = a\}| = |\{t < n \mid \rightsquigarrow_t = \rightrightarrows\}|$ for all $\rightrightarrows \in E$. So $\varphi(\hat{l}) \in V$. Since \hat{l} was arbitrary,

$$\operatorname{img} \varphi \subseteq V. \tag{22}$$

Lemma V.2.1.11 implies

 $\forall \, \hat{l}, \check{l} \in \mathsf{Loc}^n \colon \; \hat{l} \asymp \check{l} \; \Leftrightarrow \; \varphi(\hat{l}) = \varphi(\check{l}) \, .$

Due to this fact and (22), the map

$$\psi\colon \overset{\mathsf{Loc}''}{\swarrow}\to V, \ [\bar{l}]_{\asymp}\mapsto \varphi(\bar{l})$$

is well defined and an injection.

To estimate the cardinality of V, we employ a generalization of the binomial coefficients in which the upper argument can take arbitrary rational values:

$$\binom{x}{m} \stackrel{\text{def}}{=} \prod_{i < m} \frac{x - i}{i + 1} \quad \text{for } x \in \mathbb{Q} \text{ and } m \in \mathbb{N}_{\geq 0},$$

with the convention that the empty product evaluates to 1.

We need a standard combinatorial lemma:

Proposition V.2.1.17. There are $\binom{k+m-1}{k}$ ways to arrange k indistinguishable balls into m distinguishable baskets. Formally:

$$\left| \left\{ g \colon m \to \mathbb{N}_{\geq 0} \mid \sum_{c < m} g(c) = k \right\} \right| = \binom{k + m - 1}{k}.$$

(Remark: in the above formulation, g(c) is the number of balls in basket c, where the baskets are enumerated by integers from 0 up to but not including m.)

Proof. Any arrangement of *k* indistinguishable balls into *m* distinguishable baskets can be written as a string over \odot and |, e.g., $\odot \odot |\odot| |\odot \odot \odot || |\odot|$, where \odot denotes a ball, | separates the baskets, and there are *k* symbols \odot and *m*-1 delimiters |. The example string above, processed from left to right, shows two balls in the first basket, one ball in the second, none in the third, three in the fourth, none in the sixth, none in the seventh, one in the eighth, and none in the ninth. This mapping of arrangements to strings is injective. Moreover, this mapping is also surjective: each string over *k* symbols \odot and *m*-1 symbols | can be interpreted as an arrangement of *k* balls into *m* baskets. Therefore, it suffices to count the number of such strings, which is $\binom{k+m-1}{k}$.

The definition of V in (21) depends on the transitions of the threads. Now we bound |V| by an expression over G, L, and n that does not involve this dependency:

Lemma V.2.1.18. $|V| \leq \max \{\prod_{i < 2^{GL(GL-1)}} \binom{k_i + L - 1}{L - 1} \mid k_0, \dots, k_{2^{GL(GL-1)} - 1} \in \mathbb{N}_{\geq 0} \land \sum_{i < 2^{GL(GL-1)}} k_i = n \}.$

Proof. Let $k_{\Rightarrow} \stackrel{\text{def}}{=} |\{t \in n \mid \rightsquigarrow_t = \Rightarrow\}|$ for each $\Rightarrow \in E$. Proposition V.2.1.17 implies that $|\{g \in (\text{Loc} \rightarrow \mathbb{N}_{\geq 0}) \mid \sum_{c \in \text{Loc}} g(c) = k_{\Rightarrow}\}| = \binom{k_{\Rightarrow}+L-1}{k_{\Rightarrow}} = \binom{k_{\Rightarrow}+L-1}{L-1}$ for each $\Rightarrow \in E$. Thus,

$$\prod_{\exists \in E} \left| \left\{ g \in (\mathsf{Loc} \to \mathbb{N}_{\geq 0}) \mid \sum_{c \in \mathsf{Loc}} g(c) = k_{\exists} \right\} \right| = \prod_{\exists \in E} \binom{k_{\exists} + L - 1}{L - 1}.$$
 (23)

The left-hand side of (23) is equal to $\left| \prod_{\exists \in E} \{g \in (\mathsf{Loc} \to \mathbb{N}_{\geq 0}) \mid \sum_{c \in \mathsf{Loc}} g(c) = k_{\exists} \} \right| = |\{f \in (E \to \mathsf{Loc} \to \mathbb{N}_{\geq 0}) \mid \forall \exists \in E : \sum_{c \in \mathsf{Loc}} f(\exists)(c) = k_{\exists} \} = |V|. \text{ Since } \sum_{\substack{c \in \mathsf{Loc}}} k_{\exists} = n, \text{ the right-hand side of (23) is bounded above by } \max \{\prod_{\substack{c \in E \\ L-1}} (\hat{k}_{\exists})_{\exists \in E} \in (\mathbb{N}_{\geq 0})^E \land d_{z} \} = 0 \}$

$$\begin{split} &\sum_{\substack{\Rightarrow \in E \\ |\hat{k}_{i}| < 2^{GL(GL-1)} \in (\mathbb{N}_{\geq 0})^{2^{GL(GL-1)}} \wedge \sum_{i < 2^{GL(GL-1)} \in (\mathbb{N}_{\geq 0})^{2^{GL(GL-1)}} \wedge \sum_{i < 2^{GL(GL-1)} \hat{k}_{i} = n} } \max \left\{ \prod_{i < 2^{GL(GL-1)} \in (\mathbb{N}_{\geq 0})^{2^{GL(GL-1)}} \wedge \sum_{i < 2^{GL(GL-1)} \in (\mathbb{N}_{\geq 0})^{2^{GL(GL-1)}} \wedge \sum_{i < 2^{GL(GL-1)} \hat{k}_{i} = n} } \right\}. \quad \text{Therefore, } |V| \leq \max \left\{ \prod_{i < 2^{GL(GL-1)} \binom{\hat{k}_{i} + L - 1}{L - 1}} \left| (\hat{k}_{i})_{i < 2^{GL(GL-1)}} \in (\mathbb{N}_{\geq 0})^{2^{GL(GL-1)}} \wedge \sum_{i < 2^{GL(GL-1)} \hat{k}_{i} = n} \right\}. \end{split}$$

Now we prepare to simplify the maximum from Lemma V.2.1.18. Recall that a product of constantly many nonnegative integers with a constant sum is maximal when these integers coincide. We require a similar result:

Lemma V.2.1.19. Let $n,m \in \mathbb{N}_{\geq 0}$ and $t \in \mathbb{N}_+$. Then $\max\left\{\prod_{i < t} \binom{k_i + m}{m} \mid (k_i)_{i < t} \in (\mathbb{N}_{\geq 0})^t\right\}$ $\wedge \sum_{i < t} k_i = n \Big\} \leq \binom{n/t + m}{m}^t.$

Proof. We are going to prove a generalization of the claim (namely, where k_i are relaxed to be nonnegative rationals) by induction on $|\{i < t \mid k_i \neq \frac{n}{t}\}|$. More precisely, we are going to show $\forall r \in \mathbb{N}_{\geq 0}$: $\forall (k_i)_{i < t} \in \mathbb{Q}_{\geq 0}^t$: $(|\{i < t \mid k_i \neq \frac{n}{t}\}| = r \land \sum_{i < t} k_i = n) \Rightarrow$

 $\begin{aligned} \prod_{i < t} {k_i + m \choose m} &\leq {n/t + m \choose m}^t. \\ \text{So let } r \in \mathbb{N}_{\geq 0} \text{ and } (k_i)_{i < t} \in \mathbb{Q}_{\geq 0}^t \text{ be given such that } |\{i < t \mid k_i \neq \frac{n}{t}\}| = r \text{ and} \\ \sum_{i < t} k_i = n, \text{ and the induction hypothesis is satisfied, i.e., } \forall \tilde{r} < r \colon \forall (\tilde{k}_i)_{i < t} \in \mathbb{Q}_{\geq 0}^t : \end{cases} \end{aligned}$ $\begin{aligned} &(|\{i < t \mid \tilde{k}_i \neq \frac{n}{t}\}| = \tilde{r} \land \sum_{i < t} \tilde{k}_i = n) \Rightarrow \prod_{i < t} {\binom{\tilde{k}_i + m}{m}} \leq {\binom{n/t + m}{m}}^t. \\ &\text{If } r = 0, \text{ then } \forall i < t \colon k_i = \frac{n}{t}, \text{ and so } \prod_{i < t} {\binom{k_i + m}{m}} = {\binom{n/t + m}{m}}^t. \\ &\text{So we assume from now on that } r > 0. \text{ Since } \exists i < t \colon k_i \neq \frac{n}{t} \text{ and } \sum_{i < t} k_i = n, \text{ there} \end{aligned}$

must be i, j < t such that $k_i < \frac{n}{t} < k_j$. Let

$$\delta \stackrel{\text{def}}{=} \min\left\{\frac{n}{t} - k_i, \, k_j - \frac{n}{t}\right\} \quad \text{and} \quad \tilde{k}_h \stackrel{\text{def}}{=} \begin{cases} k_h, & \text{if } i \neq h \neq j, \\ k_h + \delta, & \text{if } h = i, \\ k_h - \delta, & \text{if } h = j \end{cases} \quad (h < t).$$

Then $\sum_{h < t} \tilde{k}_h = n$ and $|\{h < t \mid \tilde{k}_h \neq \frac{n}{t}\}| < r$. The induction hypothesis implies

$$\prod_{h < t} {\tilde{k}_h + m \choose m} \leq {n/t + m \choose m}^t.$$
(24)

Note that
$$\frac{\binom{k_i+m}{k_i}\binom{k_j+m}{m}\binom{k_j+m}{m}}{\binom{k_i+m}{m}\binom{k_j+m}{m}}$$
 is well defined and equal to
$$\frac{\left(\prod_{h
Therefore, $\frac{\binom{k_i+m}{m}\binom{k_j+m}{(k_i+m)\binom{k_j+m}{m}}}{\binom{k_i+m}{k_m}\binom{k_j+m}{k_m}} \le 1$, and so $\prod_{h$$$

Now we can bound the index of \approx by a less complicated term:

Corollary V.2.1.20. $\left| \overset{\text{State}}{\sim} \right| \leq G {\binom{n/t+L-1}{L-1}}^t$ where $t = 2^{GL(GL-1)}$.

 $\begin{array}{l} Proof. \ \left| \begin{array}{c} \mathsf{State}_{\approx} \right| = [\operatorname{according to Lemma V.2.1.8b})] \ G \left| \begin{array}{c} \mathsf{Loc}^{n} \\ \swarrow \end{array} \right| \leq [\operatorname{according to Lemma V.2.1.8b})] \ G \left| \begin{array}{c} \mathsf{Loc}^{n} \\ \swarrow \end{array} \right| \leq [\operatorname{according to Lemma V.2.1.8b})] \ G \left| \begin{array}{c} \mathsf{Loc}^{n} \\ \sim \end{array} \right| \leq [\operatorname{according to Lemma V.2.1.16b}] \ G \left| \begin{array}{c} \mathsf{Loc}^{n} \\ \mathsf{L-1} \end{array} \right| \ k_{0}, \ldots, \\ k_{2^{GL(GL-1)}-1} \in \mathbb{N}_{\geq 0} \land \sum_{i < 2^{GL(GL-1)}} k_{i} = n \\ \mathsf{s} \in [\operatorname{applying Lemma V.2.1.19 to } m = L-1 \\ \mathsf{and} \ t = 2^{GL(GL-1)}] \ G \left(\begin{array}{c} \frac{n}{2^{GL(GL-1)}} + L - 1 \\ L - 1 \end{array} \right)^{2^{GL(GL-1)}} \\ \end{array} \right| \ . \end{array}$

Now the computation for a fixed, arbitrary program is over and we turn to the whole class of programs.

We recall a standard, simple combinatorial lemma:

Proposition V.2.1.21. $\forall x \in \mathbb{Q}, m \in \mathbb{N}_{\geq 0}$: $\binom{x+m}{m} = \prod_{j=1}^{m} \frac{x+j}{j}$.

Proof. Let $x \in \mathbb{Q}$ and $m \in \mathbb{N}_{\geq 0}$. Then $\binom{x+m}{m} = \prod_{i < m} \frac{x+m-i}{i+1} = \frac{\prod_{i < m} (x+m-i)}{\prod_{i < m} (i+1)} =$ [changing the index i = m-r in the enumerator and i = h-1 in the denominator] $\frac{\prod_{r=1}^{m} (x+r)}{\prod_{h=1}^{m} h} = \prod_{j=1}^{m} \frac{x+j}{j}$.

This helps to asymptotically bound diamax by a polynomial in *n*:

Theorem V.2.1.22. diamax(n) <
$$G\left(\frac{n}{2^{GL(GL-1)}} + L - 1\right)^{2^{GL(GL-1)}} = O(n^{(L-1)2^{GL(GL-1)}})$$

Proof. In this proof, we use the fact that both the *n*-threaded program and the initial state assumed for the prior claims in § V.2.1 were arbitrary.

We have diamax(n) = max{diam(p) | p is an n-threaded program} = max{(img d_p) { ∞ } | p is an n-threaded program} < [using Lemma V.2.1.14] max{ $\left| \frac{\text{State}}{\approx} \right| = an n-threaded program p: State is the set of states of p and <math>\approx$ is defined as in Definition V.2.1.2 for p} < [using Corollary V.2.1.20] $G\left(\frac{2^{n}}{2^{GL(GL-1)}} + L - 1\right)^{2^{GL(GL-1)}}_{L-1} + C - 1$ = [using Proposition V.2.1.21] $G\left(\prod_{r=1}^{L-1} \frac{2^{n}}{2^{GL(GL-1)}} + r}{r}\right)^{2^{GL(GL-1)}}_{L-1} \leq [for L = 1, the last big product is empty and evaluates to 1, and for <math>L \ge 2, 1 \le r \le L-1$, and $n \ge 1$, we have $\frac{2^{\overline{GL(GL-1)}+r}}{r}_{L-1} = \frac{n}{2^{GL(GL-1)}r} + 1 \le \frac{n}{2^{12(1\cdot2-1)}\cdot 1} + 1 = \frac{n}{2^2} + 1 = \frac{n}{4} + 1 \le 2n$] $G(2n)^{(L-1)\cdot 2^{GL(GL-1)}}$.

To the best of our knowledge, $O(n^{(L-1)2^{GL(GL-1)}})$ is the lowest known polynomial upper bound for diamax. Comparing the degree $(L-1)2^{GL(GL-1)}$ to bounds from [78] from a theoretical viewpoint, we feel it comforting to see that our degree is relatively small: it involves *only one* exponentiation operation. Tightening Theorem V.2.1.22 is an open problem, but we expect that the degree could be lowered further by reducing *E* from Definition V.2.1.10 and considering distance-preserving bisimulations. Sections V.2.2 and V.2.3 will be encouraging: in the setups described there, the degree is provably 1.

Note V.2.1.23. The current degree of the upper bound on diamax has been improved over the previously known degree from [60] by the factor of *L*. In the binary case we obtain diamax $(n) = O(n^{1\cdot 2^{4\cdot 3}}) = O(n^{2^{12}})$, which is an improvement over the $O(n^{2^{17}})$ bound from [64] by the factor of 32 in the degree.

V.2.2. Strongly connected subprograms

Now we show a linear upper bound for families of programs that have a subprogram with a strongly connected transition graph. A graph is *strongly connected* if, for each pair of nodes of the graph, there is a walk from the first node to the second node. Slightly abusing the terminology, we say that a program is *strongly connected* if its transition graph is strongly connected.

Models of real-world programs may possess strongly connected subprograms. This may happen when we produce an abstraction of a system. For example, the coarsest abstract thread [53] uses only one local state and changes the shared memory arbitrarily. Another way in which strongly connected subprograms may occur is via the automatic generation of models inside abstraction-refinement loops: it starts with the coarsest thread. Even more-refined thread abstractions may be strongly connected, e.g., when we model a reactive thread running an infinite loop which responds to the environment by changing the shared state in different ways.

For a program with transition relation \longrightarrow , we write $\sigma \longrightarrow {}^{\leq k} \sigma'$ iff the state σ' is reachable from σ in at most k steps. Formally:

$$\longrightarrow^{\leqslant 0} \stackrel{\text{def}}{=} \operatorname{id}_{\mathsf{State}} \quad \text{and}$$
$$\longrightarrow^{\leqslant k} \stackrel{\text{def}}{=} \longrightarrow^{\leqslant k-1} \cup (\longrightarrow^{\leqslant k-1} \operatorname{s}) \quad (k \in \mathbb{N}_+),$$

where State is the set of program states and $\frac{6}{9}$ is the left composition.

While the cases of low values of *G* (or *L*) will be easy to handle, for the most general case $G \ge 2$ we will need some preparation. Let *C* be the maximal local diameter (cf. Definition and Corollary IV.3.8) for the rest of this section. Now, we determine how long it takes in a strongly connected program to achieve a given shared part in the worst case:

Lemma V.2.2.1. Let *h* be an *m*-threaded strongly connected program with at least two shared states and (g, l) a state of the program. Let $\delta = \min \{C, (\text{if } G = 2 \text{ then } L \text{ else } (G-1)L^m - L^{m-1} + 1), \dim(h) \}$. Then each shared state can be reached from (g, l) in at most δ steps. Formally: $(G \ge 2 \land h \text{ is strongly connected } \land \text{ the transition graph of } h$ is $(\text{State}, \longrightarrow)) \Rightarrow (\forall (g, l) \in \text{State, } g' \in \text{Glob } \exists l' \in \text{Loc}^m \colon (g, l) \longrightarrow {}^{\leqslant \delta} (g', l')).$

Proof. Let $h = (\rightarrow_i)_{i < m}$, $(g, l) \in$ State and $g' \in$ Glob.

If g=g', take l'=l and obtain $(g, l) \longrightarrow^* (g', l')$ in zero steps, and $0 \leq (G-1)L^m - L^{m-1} + 1$ because of $G \geq 2$.

Otherwise, consider the case $g \neq g'$ from now on. In particular, $G \ge 2$ in this case. Since the transition graph is strongly connected, there is a path from (g, l) to some program state $(g', _)$. Among all such paths, we choose a shortest one; let us refer to it as $\sigma = (\sigma^{[j]})_{j \le k}$ for some $k \ge 0$. Since $g \neq g'$, we even have $k \ge 1$. Let $(g^{[j]}, l^{[j]}) = \sigma^{[j]}$ for all $j \le k$. Note that $g^{[k]} = g'$; let $l' = l^{[k]}$.

Now we derive the three bounds on length(σ): C, (if G=2 then L else $(G-1)L^m - L^{m-1} + 1$), and diam(h):

- By the definition of *C*, we have d^{loc}((g, l), 0, (g', l'₀)) ≤ *C*. According to the definition of local distances, there is a walk ŏ = (ŏ^[j])_{j≤k} and l ∈ Loc^m such that ŏ^[0] = (g, l) ∧ ŏ_k = (g', l) ∧ l₀ = l'₀ ∧ k = d^{loc}((g, l), 0, (g', l'₀)). The choice of σ implies length(σ) ≤ length(ŏ), which, in turn, implies k ≤ k ≤ C.
- There is some *i* such that $(g^{[k-1]}, l_i^{[k-1]}) \rightarrow_i (g', l_i^{[k]})$ and $l_i^{[k-1]} = l_i^{[k]}$ for all $\hat{i} \in n \setminus \{i\}$. By minimality of σ , $g^{[j]} \neq g'$ for all j < k; in particular, $g^{[k-1]} \neq g'$.

Case G=2. Within the path $(\sigma^{[j]})_{j < k}$, the shared state is constant. So every transition there is induced by thread *i* (since transitions induced by other threads can be skipped, resulting in a shorter path to a program state with the shared part g', in contradiction to the minimality of σ). Thus, only the local state of thread *i* changes along $(\sigma^{[j]})_{j < k}$. Since a path has no self-intersections, $k \leq L$.

Case G>2. If we had $(g^{[k-1]}, l_i^{[k-1]}) = (g^{[j]}, l_i^{[j]})$ for some j < k-1, we could have taken the same thread transition $((g^{[k-1]}, l_i^{[k-1]}), (g', l_i^{[k]}))$ earlier in σ in contradiction to its minimality. Therefore, $(g^{[k-1]}, l_i^{[k-1]})$ does not occur as a thread state of thread i in $(\sigma^{[j]})_{j < k-1}$. Thus, $\sigma^{[j]} \in ((\text{Glob} \setminus \{g'\}) \times \text{Loc}^m) \setminus (\{g^{[k-1]}\} \times (\text{Loc}^i \times \{l_i^{[k-1]}\} \times \text{Loc}^{m-i-1}))$ for all j < k-1. So, since $(\sigma^{[j]})_{j < k-1}$ is not self-intersecting, it has at most $(G-1)L^m - L^{m-1}$ states. Thus, $k-1 \leq (G-1)L^m - L^{m-1}$, which implies $k \leq (G-1)L^m - L^{m-1} + 1$.

In summary, $k \leq (\text{if } G=2 \text{ then } L \text{ else } (G-1)L^m - L^{m-1} + 1).$

• Note that σ is a shortest path from (g, l) to (g', l'). So $k \leq \text{diam}(h)$.

Summarizing, length(σ) $\leq \min \{C, (\text{if } G = 2 \text{ then } L \text{ else } (G-1)L^m - L^{m-1} + 1), \text{ diam}(h)\}.$

As an aside concerning the proof above, the treatment of Case G>2 does not rely on $G\neq 2$, but also works for G=2. Following this case in the proof above with G=2 in mind, we simply obtain the upper bound $(G-1)L^m - L^{m-1} + 1 = L^m - L^{m-1} + 1 =$ $(L-1)L^{m-1} + 1$, which coincides with L in case $m=1 \lor L=1$ and exceeds L otherwise.

For G>2, it is possible to slightly reduce the upper bound δ from Lemma V.2.2.1 by considering longer suffixes of σ while treating Case G>2 (than suffixes of length 1 as of now); the resulting upper bound would become an increasingly complicated expression, and, therefore, we are not going to do this. Whether it is possible to significantly reduce δ is an open problem.

As we will see, a strongly connected subprogram is in some sense a "universal helper," since, if h is a strongly connected subprogram of program p, then h may "help" to lift certain sequences of local states of p (that are almost walks in the transition graph p induced by a single thread, but the shared states do not match properly) to actual walks in the transition graph of p:

Lemma V.2.2. Let $G \ge 2$ and h be a strongly connected, m-threaded subprogram of an n-threaded program $p = (\rightarrow_i)_{i < n}$ via an embedding f. Let $\delta = \min \{C, (\text{if } G = 2 \text{ then } L \text{ else } (G-1)L^m - L^{m-1} + 1), \dim(h)\}$. Let \longrightarrow be the transition relation of p, $i \in n \setminus (\inf f), (g, l)$ a state of $p, k \in \mathbb{N}_{\ge 0}, \sigma = (\sigma^{[j]})_{j \le k}$ a sequence of local states that starts with l_i and satisfies $\forall j < k : (_, \sigma^{[j]}) \rightarrow_i (_, \sigma^{[j+1]}) \lor \sigma^{[j]} = \sigma^{[j+1]}$. Then there is a program state (\hat{g}, \hat{l}) of p such that $\hat{l}|_{n \setminus ((\inf f) \cup \{i\})} = l|_{n \setminus ((\inf f) \cup \{i\})}, \sigma^{[k]} = \hat{l}_i$, and $(g, l) \rightarrow^{\leq (\delta+1)(L-1)} (\hat{g}, \hat{l})$.

Proof. If $\sigma^{[0]} = \sigma^{[k]}$, the lemma is proven by setting $(\hat{g}, \hat{l}) = (g, l)$. Thus, from now on we consider the case $\sigma^{[0]} \neq \sigma^{[k]}$.

Algorithm 5: Contracting σ into θ . **Input:** σ , k **Program variables:** nonnegative integers s, j, sequence θ over Loc **Output:** s, θ s := k; $\theta := \sigma$: j := 0; while j < s do if there is some t such that $j < t \le s$ and $\theta^{[t]} = \theta^{[j]}$ then let t be the largest integer such that $t \leq s$ and $\theta^{[t]} = \theta^{[j]}$; remove the subsequence $\theta^{[j+1]} \dots \theta^{[t]}$ from θ ; s := s - (t - j);j := j + 1;

Informally, we will now shorten σ , producing a sequence θ containing no repetitions. Formally, the contraction is performed by Algorithm 5.

The following loop invariant holds:

 $\begin{array}{l} \bullet \ \mathsf{j} \leqslant \mathsf{s} = \mathsf{length}(\theta) \ , \\ \bullet \ \theta^{[0]} = \sigma^{[0]} \ \land \ \theta^{[\mathsf{s}]} = \sigma^{[k]} \ , \end{array}$

• for all $\bar{j} < j$, the local state $\hat{\theta}^{[\bar{j}]}$ occurs in θ exactly once, and

• $\forall \overline{j} < s: (\underline{, \theta^{[\overline{j}]}}) \rightarrow_i (\underline{, \theta^{[\overline{j}+1]}}) \lor \theta^{[\overline{j}]} = \theta^{[\overline{j}+1]}$.

Thus, we obtain the following postcondition of the program:

• $j = s = \text{length}(\theta)$,

•
$$\theta^{[0]} = \sigma^{[0]} \wedge \theta^{[s]} = \sigma^{[k]}$$

• for all $\overline{j} < s$, the local state $\theta^{[\overline{j}]}$ occurs in θ exactly once, and

•
$$\forall \bar{j} < s: (_, \theta^{\lfloor J \rfloor}) \rightarrow_i (_, \theta^{\lfloor J^{\perp} \rfloor})$$
.

Since in each iteration s-j strictly decreases and stays nonnegative, the program always terminates.

Let θ be the last value of the program variable θ and s the last value of the program variable s. As all the elements at positions smaller than s occur in θ exactly once, the last element must occur in θ also exactly once. So θ contains no repetitions at all, implying s < L. Since $\theta^{[0]} = \sigma^{[0]} \neq \sigma^{[k]} = \theta^{[s]}$, we must have $s \neq 0$, so $s \ge 1$.

According to the postcondition, there are families $(\check{e}^{[j]})_{j < s}$ and $(\hat{e}^{[j]})_{j < s}$ over Glob such that for each j < s we have $(\check{e}^{[j]}, \theta^{[j]}) \rightarrow_i (\hat{e}^{[j]}, \theta^{[j+1]})$. Let was be the transition relation of h. Since h is strongly connected, Lemma V.2.2.1 implies that one can recursively construct a sequence $\tau = (\tau^{[j]})_{i < s} \in (Loc^m)^s$ of states of h such that $(g, (l_{f(r)})_{r < m}) \longrightarrow^{\leq \delta} (\check{e}^{[0]}, \tau^{[0]}) \text{ and } (\hat{e}^{[j]}, \tau^{[j]}) \longrightarrow^{\leq \delta} (\check{e}^{[j+1]}, \tau^{[j+1]}) \text{ for all } j < 0$ s - 1.

Now consider the sequence $((\bar{g}^{[j]}, \varphi^{[j]}))_{j \leq 2s}$ of program states of p, defined as follows:

• $\bar{g}^{[0]} = g, \varphi^{[0]} = l,$

- for odd j < 2s let $\bar{g}^{[j]} = \check{e}^{[(j-1)/2]}, \varphi_i^{[j]} = \theta^{[(j-1)/2]}, \varphi_{f(r)}^{[j]} = \tau_r^{[(j-1)/2]} (r < m)$, and
- $$\begin{split} \varphi^{[j]}|_{n \setminus ((\inf f) \cup \{i\})} &= l|_{n \setminus ((\inf f) \cup \{i\})}, \\ \bullet \text{ for positive even } j \leq 2s \text{ let } \bar{g}^{[j]} = \hat{e}^{[j/2-1]}, \varphi_i^{[j]} = \theta^{[j/2]}, \varphi_{f(r)}^{[j]} = \tau_r^{[j/2-1]} \ (r < m), \text{ and} \end{split}$$

 $\varphi^{[j]}|_{n\setminus((\operatorname{img} f)\cup\{i\})} = l|_{n\setminus((\operatorname{img} f)\cup\{i\})}.$ Note:

- Since $s \ge 1$, we obtain $(\bar{g}^{[0]}, (\varphi_{f(r)}^{[0]})_{r < m}) = (g, (l_{f(r)})_{r < m}) \xrightarrow{\leq \delta} (\check{e}^{[0]}, \tau^{[0]}) = (\bar{g}^{[1]}, (\varphi_{f(r)}^{[1]})_{r < m}), \varphi_i^{[0]} = l_i = \sigma^{[0]} = \theta^{[0]} = \varphi_i^{[1]}, \varphi^{[0]}|_{n \setminus ((\inf g f) \cup \{i\})} = l|_{n \setminus ((\inf g f) \cup \{i\})}, \text{ and so } (\bar{g}^{[0]}, \varphi^{[0]}) \longrightarrow^{\leq \delta} (\bar{g}^{[1]}, \varphi^{[1]}).$
- For odd j < 2s we have $(\bar{g}^{[j]}, \varphi_i^{[j]}) = (\check{e}^{[(j-1)/2]}, \theta^{[(j-1)/2]}) \rightarrow_i (\hat{e}^{[(j-1)/2]}, \theta^{[(j-1)/2]}) = (\bar{g}^{[j+1]}, \varphi_i^{[j+1]}), \varphi_u^{[j]} = \tau_{f^{-1}(u)}^{[(j-1)/2]} = \varphi_u^{[j+1]} \text{ (for all } u \in \text{ img } f), \varphi_i^{[j]}|_{n \setminus ((\text{img } f) \cup \{i\})} = l|_{n \setminus ((\text{img } f) \cup \{i\})} = \varphi^{[j+1]}|_{n \setminus ((\text{img } f) \cup \{i\})}, \text{ and so } (\bar{g}^{[j]}, \varphi^{[j]}) \rightarrow (\bar{g}^{[j+1]}, \varphi^{[j+1]}).$
- For positive even j < 2s we have $(\bar{g}^{[j]}, (\varphi_{f(r)}^{[j]})_{r < m}) = (\hat{e}^{[j/2-1]}, \tau^{[j/2-1]})$ [since $j \leq 2s 2$, we get $\frac{j}{2} \leq s 1$, and so $\frac{j}{2} 1 < s 1$] $\cdots \leq \delta$ $(\check{e}^{[j/2]}, \tau^{[j/2]}) = (\bar{g}^{[j+1]}, (\varphi_{f(r)}^{[j+1]})_{r < m}), \varphi_i^{[j]} = \theta^{[j/2]} = \varphi_i^{[j+1]}, \varphi^{[j]}|_{n \setminus ((\inf g f) \cup \{i\})} = l|_{n \setminus ((\inf g f) \cup \{i\})} = \varphi^{[j+1]}|_{n \setminus ((\inf g f) \cup \{i\})}$, and so $(\bar{g}^{[j]}, \varphi^{[j]}) \longrightarrow \leq \delta$ $(\bar{g}^{[j+1]}, \varphi^{[j+1]})$. • Finally, $\varphi_i^{[2s]} = \theta^{[s]} = \sigma^{[k]}$ and $\varphi^{[2s]}|_{n \setminus ((\inf g f) \cup \{i\})} = l|_{n \setminus ((\inf g f) \cup \{i\})}$. The above implies $(\bar{g}^{[0]}, \varphi^{[0]}) \longrightarrow (\bar{g}^{[2s]}, \varphi^{[2s]})$; the walk from $(\bar{g}^{[0]}, \varphi^{[0]})$ to $(\bar{g}^{[2s]}, \varphi^{[2s]})$, $\varphi^{[2s]}$.

• Finally, $\varphi_i^{[i]} = \theta^{[i]} = \sigma^{[i]}$ and $\varphi^{[2s]}_{[n}((\inf f) \cup \{i\}) = t_{[n}((\inf f) \cup \{i\})$. The above implies $(\bar{g}^{[0]}, \varphi^{[0]}) \longrightarrow (\bar{g}^{[2s]}, \varphi^{[2s]})$; the walk from $(\bar{g}^{[0]}, \varphi^{[0]})$ to $(\bar{g}^{[2s]}, \varphi^{[2s]})$ goes through all the states of $((\bar{g}^{[j]}, \varphi^{[j]}))_{j \leq 2s}$ in ascending order (and possibly through more states when taking a subwalk from $(\bar{g}^{[j]}, \varphi^{[j]})$ to $(\bar{g}^{[j+1]}, \varphi^{[j+1]})$ for even nonnegative j < 2s). The number of odd positive integers below 2s is $|\{i \mid i \text{ odd } \land 1 \leq i \leq 2s - 1\}| = \frac{(2s-1)-1}{2} + 1 = s$. The number of even positive integers below 2s is $|\{i \mid i \text{ even } \land 2 \leq i \leq 2s - 2\}| = \frac{(2s-2)-2}{2} + 1 = s-1$. Thus, the length of the walk just mentioned is at most $\delta + s + \delta(s-1) = (\delta+1)s \leq (\delta+1)(L-1)$. We set $(\hat{g}, \hat{l}) \stackrel{\text{def}}{=} (\bar{g}^{[2s]}, \varphi^{[2s]})$.

Due to Lemma V.2.2.2, a strongly connected subprogram can "help" more than one thread to change shared states "quickly":

Theorem V.2.2.3. Let $n \ge m \ge 1$, and let h be a strongly connected, m-threaded subprogram of an n-threaded program p. If G=1, then diam $(p) \le (L-1)n$. If L=1, then diam $(p) \le G-1$. If $G \ge 2$, then diam $(p) \le \left(\min\{C, (\text{if } G=2 \text{ then } L \text{ else } (G-1)L^m - L^{m-1} + 1), \operatorname{diam}(h)\} + 1\right)(L-1)(n-m) + \operatorname{diam}(h).$

Proof. If G=1, the strong connectivity of h is irrelevant, since h cannot exchange information with the remainder of the program, and Lemma V.1.1 gives an upper bound of (L-1)n for the diameter of an n-threaded program.

If L=1, Lemma V.1.2 provides us with an upper bound of G-1.

Now we turn to the general case $G \ge 2$.

If n=m, both sides of the inequality in question coincide; thus assume from now on that n>m. Let $(\rightarrow_0, \ldots, \rightarrow_{n-1}) = p$, and let \longrightarrow be the transition relation of p. Let f be the embedding of h into p. Let $\zeta = \left(\min \{C, (\text{if } G=2 \text{ then } L \text{ else } (G-1)L^m - L^{m-1} + 1), \text{diam}(h)\} + 1\right)(L-1).$

Consider program states (g, l), (g', l') of p such that $(g, l) \longrightarrow^* (g', l')$. Let $A = \{i \in n \setminus (\text{img } f) \mid l_i \neq l'_i\}$ be the set of identifiers of threads outside the embedded program h whose initial and final local states differ.

We start by considering the special case that A is empty. Then $l|_{n \setminus (\inf g_f)} = l'|_{n \setminus (\inf g_f)}$. Since h is strongly connected, it exhibits a path τ from $(g, (l_{f(r)})_{r < m})$ to $(g', (l'_{f(r)})_{r < m})$ of length at most diam(*h*). We lift τ to a path $\hat{\tau}$ from (g, l) to (g', l') in the transition graph of *p* by reindexing the local states in τ according to *f* and adding a tuple of local states $(l_r)_{r \in n \setminus (\text{img } f)}$ to each state in τ . Certainly, length $(\hat{\tau}) \leq \text{diam}(h) \leq \zeta(n-m) + \text{diam}(h)$, which proves the lemma.

From now on we consider the other case, viz. that *A* is nonempty. Take any path $\sigma = (\sigma^{[j]})_{j \le k}$ in *p* from (g, l) to (g', l'). Enumerate the elements of *A* in ascending order by $i_0 < \cdots < i_{s-1}$ for s = |A|. We are going to recursively construct nonempty paths $\varphi_0, \ldots, \varphi_{s-1}$ in the transition graph of *p* such that the initial state of φ_0 is (g, l), length $(\varphi_0) \le \zeta$, and for all j < s we have:

- φ_j is nonempty,
- $j > 0 \Rightarrow \text{length}(\varphi_j) < \zeta$,
- the concatenated sequence $\varphi_0 \dots \varphi_j$ is a walk, and
- the final state of φ_j is of the form $\begin{pmatrix} -, & \text{if } r \in \text{img } f, \\ l'_r, & \text{if } r \notin \text{img } f \land r \leqslant i_j, \\ l_r, & \text{if } r \notin \text{img } f \land i_j < r \end{pmatrix}_{r < n}$.

For this purpose, let j < s be arbitrary, and assume that for all j' < j the paths $\varphi_{j'}$ as above are already constructed.

Case j=0. Notice that $\sigma_{i_0}^{[0]} = l_{i_0}$ and $\forall \bar{j} < k : (_, \sigma_{i_0}^{[\bar{j}]}) \rightarrow_{i_0} (_, \sigma_{i_0}^{[\bar{j}+1]}) \lor \sigma_{i_0}^{[\bar{j}]} = \sigma_{i_0}^{[\bar{j}+1]}$. By Lemma V.2.2.2, there is a program state (\hat{g}, \hat{l}) of p such that $(g, l) \longrightarrow^{\leqslant \zeta} (\hat{g}, \hat{l}), \sigma_{i_0}^{[k]} = \hat{l}_{i_0}$, and $\hat{l}|_{n \setminus ((\inf g f) \cup \{i_0\})} = l|_{n \setminus ((\inf g f) \cup \{i_0\})}$. We define φ_0 as a path of length $\leqslant \zeta$ that takes (g, l) to (\hat{g}, \hat{l}) . Certainly, φ_0 is nonempty. Now consider an arbitrary $r \in n \setminus (\inf g f)$.

Case $r \leq i_0$. If $r \notin A$, we have especially $r \in n \setminus ((\inf f) \cup \{i_0\})$ and therefore $\hat{l}_r = l_r = l'_r$. If $r \in A$, we have $r = i_0$ and therefore $\hat{l}_r = \hat{l}_{i_0} = \sigma_{i_0}^{[k]} = l'_{i_0} = l'_r$. Case $i_0 < r$. Then $r \in n \setminus ((\inf f) \cup \{i_0\})$, and so $\hat{l}_r = l_r$.

Case j>0. Let (\check{g},\check{l}) be the final state of φ_{j-1} , which exists because φ_{j-1} is nonempty. Note that $\check{l}_r = l'_r$ for $r \in n \setminus (\operatorname{img} f) \land r \leq i_{j-1}, \check{l}_r = l_r$ for $r \in n \setminus (\operatorname{img} f) \land r > i_{j-1}, \sigma_{i_j}^{[0]} = l_{i_j} = \check{l}_{i_j}$, and $\forall \bar{j} < k \colon (_, \sigma_{i_j}^{[\bar{j}]}) \to_{i_j} (_, \sigma_{i_j}^{[\bar{j}+1]}) \lor \sigma_{i_j}^{[\bar{j}]} = \sigma_{i_j}^{[\bar{j}+1]}$. Lemma V.2.2.2 implies the existence of a program state (\hat{g}, \hat{l}) of p such that $(\check{g}, \check{l}) \longrightarrow^{\leq \zeta} (\hat{g}, \hat{l}), \sigma_{i_j}^{[k]} = \hat{l}_{i_j}$, and $\hat{l}|_{n \setminus ((\operatorname{img} f) \cup \{i_j\})} = \check{l}|_{n \setminus ((\operatorname{img} f) \cup \{i_j\})}$. We define φ_j as a path obtained from an evidence path for $(\check{g}, \check{l}) \longrightarrow^{\leq \zeta} (\hat{g}, \hat{l})$ by stripping the source state. Now let $r \in n \setminus (\operatorname{img} f)$. Case $r \leq i_j$.

Case $r \leq i_{j-1}$. Then $r \in n \setminus ((\operatorname{img} f) \cup \{i_j\})$, and so $\hat{l}_r = \check{l}_r = l'_r$. Case $r > i_{j-1}$. If $r \notin A$, we have $r \in n \setminus ((\operatorname{img} f) \cup \{i_j\})$ and so $\hat{l}_r = \check{l}_r = l_r = l'_r$. If $r \in A$, then $r = i_j$ and so $\hat{l}_r = \hat{l}_{i_j} = \sigma_{i_j}^{\lfloor k \rfloor} = l'_{i_j} = l'_r$.

Case $i_j < r$. Then $r \in n \setminus ((\text{img } f) \cup \{i_j\})$ and $r > i_{j-1}$, so we have $\hat{l}_r = \check{l}_r = l_r$. Since $\hat{l}_{i_j} = l'_{i_j} \neq l_{i_j} = \check{l}_{i_j}$, we obtain that φ_j is nonempty.

After construction we thus obtain a walk of length not exceeding ζs from (g, l) to a state of the form $\left(-, \left(\begin{cases} -, & \text{if } r \in \text{img } f, \\ l'_r, & \text{if } r \notin (\text{img } f) \land r \leq i_{s-1}, \end{cases} \right) \right)$, which is by definition of *s* and

$$\left(\begin{array}{cc} \left(l_r, & \text{if } r \notin (\text{img } f) \land i_{s-1} < r \end{array} \right) \\ A \text{ of the form } \left(-, \left\{ \begin{cases} -, & \text{if } r \in \text{img } f, \\ l'_r, & \text{otherwise.} \end{cases} \right\}_{r < n} \right). \text{ Since } h \text{ is strongly connected, it exhibits} \end{cases}$$

a path τ from the *h*-components of the last state of φ_{s-1} to $(g', (l'_{f(r)})_{r < m})$ of length at most diam(*h*). We lift τ to a path $\hat{\tau}$ from the last state of φ_{s-1} to $(g', (l'_{f(r)})_{r < m})$ in the transition graph of *P* by reindexing the local states in τ according to *f* and adding the constant tuple of local states $(l'_r)_{r \in n \setminus (\lim f)}$ to each state in τ . Certainly, length($\hat{\tau}$) \leq diam(*h*). Let ψ be $\hat{\tau}$ without its initial state. Then the walk $\varphi_0 \dots \varphi_{s-1} \psi$ has length at most $\zeta s +$ diam(*h*) $\leq \zeta (n - m) +$ diam(*h*).

Let us restate the contents of Theorem V.2.2.3 for program families:

Corollary V.2.2.4. *Let* $m \ge 1$ *and* $(p_n)_{n \ge m}$ *be a family of multithreaded programs such that*

- the program p_m is strongly connected, and
- for all $n \ge m$, the program p_n has exactly n threads, and
- for all $n \ge m$, the program p_m is a subprogram of p_n .

If G=1, then diam $(p_n) \leq (L-1)n$. If L=1, then diam $(p_n) \leq G-1$. If $G \geq 2$, then for $d = \operatorname{diam}(p_m)$ and $\zeta = \left(\min\left\{C, (\text{if } G=2 \text{ then } L \text{ else } (G-1)L^m - L^{m-1} + 1), d\right\} + 1\right)(L-1)$ we have $\forall n \geq m$: diam $(p_n) \leq \zeta n - \zeta m + d$.

Note V.2.2.5 (On the tightness of upper bounds mentioned in Theorem V.2.2.3 and Corollary V.2.2.4). In case G=1, the upper the bound (L-1)n is tight, as can be demonstrated by a program in which the graph of transitions of some thread is a directed Hamiltonian cycle on Glob×Loc, and the transitions graphs of the other threads are non-circular directed Hamiltonian paths on Glob×Loc.

In case L=1, the upper the bound G-1 is also tight, as can be demonstrated by a program in which the graph of transitions of some thread is a directed Hamiltonian cycle on Glob×Loc, and the thread transition relations of the other threads are empty.

In case $G, L \ge 2$ and m=1, the upper bound $\left(\min \left\{C, (\text{if } G=2 \text{ then } L \text{ else } (G-1)L), \right\}\right)$

 $\begin{aligned} \mathsf{diam}(h) &\Big\} + 1 \Big) (L-1)(n-1) + \mathsf{diam}(h) &\leq ((G-1)L+1)(L-1)(n-1) + \mathsf{diam}(h) &\leq \\ (GL-L+1)(L-1)(n-1) + GL-1 &= (GL-L+1)(L-1)n - (GL^2 - L^2 + L - GL + L - 1) \\ + GL-1 &= (GL-L+1)(L-1)n - GL^2 + L^2 - 2L + GL + 2GL &= (GL-L+1) \\ (L-1)n + (-GL+L-2+2G)L &= (GL-L+1)(L-1)n + ((2-L)G + L - 2)L &= \\ (GL-L+1)(L-1)n + (2-L)(G-1)L \text{ is likely to be tight. We expect that it can be demonstrated by an adaptation of the family from the proof of Theorem V.1.3, in which thread 0 additionally obtains a reverse transition for each transition it already has; we skip the corresponding proof in this paper. \end{aligned}$

For the most general case of $G, L, m \ge 2$, the question whether the bound is tight is open.

Other examples for Theorem V.2.2.3 and Corollary V.2.2.4 can be obtained by adding more threads to N2T6B, N2T7B, or N3T9 from § III: each of these programs is strongly connected.

Notice that the upper bounds in Theorem V.2.2.3 and Corollary V.2.2.4 are (sub-) linear in *n*, as the coefficient with which *n* is multiplied can be bounded above by (C+1)(L-1), which does not depend on the family of programs.

Also remark that for $G \ge 2$, the if-then-else expression inside the aforementioned coefficient can be simplified to its "else" branch which is at least as large as the "then" branch. The cost paid is a loosening of the inequality for G=2.

V.2.3. Probabilistic Analysis

(We thank Steffen Borgwardt for the basic insight of this section.)

We proceed by considering a random process of creating a multithreaded program in which each transition of each thread is chosen independently with a probability that depends only on the thread transition.

Due to Lemmas V.1.1 and V.1.2, in case $1 \in \{G, L\}$ the diameter of an *n*-threaded program is at most linear in *n* with probability 1 regardless of the random process. So from here to the end of this section, we consider the general case $G, L \ge 2$.

First, we show that the existence of a thread with a special set of transitions leads to a polynomial bound on the diameter which is linear in n.

Lemma V.2.3.1. If the graph of thread transitions of some thread in a program is strongly connected, then the diameter of the program does not exceed (GL-L+1)(L-1)n + (2-L)(G-1)L. Formally: for every n-threaded program $p = (\rightarrow_i)_{i < n}$, we have $(\exists i < n \forall g, g' \in \text{Glob}, l, l' \in \text{Loc}: (g, l) \rightarrow_i^* (g', l')) \Rightarrow \text{diam}(p) \leq (GL-L+1)(L-1)n + (2-L)(G-1)L$.

Proof. Consider an *n*-threaded program $p = (→_i)_{i < n}$ such that for some i < n, the graph (Glob×Loc, $→_i$) is strongly connected. Notice that this graph has GL nodes, so any path (which is, by definition, not self-intersecting) in this graph has at most GL - 1 edges. In particular, if *h* is the program consisting of only the thread $→_i$, then diam(h) $\leq GL - 1$. Theorem V.2.2.3 implies diam(p) \leq (min{(G-1)L, GL-1}+1)(L-1)(n-1)+GL-1 = (min{GL-L, GL-1}+1)(L-1)(n-1)+GL-1 \leq ((GL-L)+1)(L-1)(n-1)+GL-1 = (GL-L+1)(L-1)n - (<math>GL-L+1)(L-1)n - (<math>GL-L+1)(L-1) $n - (<math>GL^2 - L^2 - L$

Lemma V.2.3.1 is our main ingredient in proving a linear diameter of programs for which the number of threads becomes sufficiently large—given a rather general probability distribution for the existence of thread transitions as mentioned before.

Theorem V.2.3.2. Let π : $(\text{Glob}\times\text{Loc})^2 \to [0,1]$ be a probability distribution on thread transitions for which $\forall g, g' \in \text{Glob}, l, l' \in \text{Loc } \exists k \in \mathbb{N}_{\geq 0}, (\tilde{l}_i)_{i=0}^k \in (\text{Loc}^n)^{k+1}, (\tilde{g}_i)_{i=0}^k \in \text{Glob}^{k+1}$: $(g, l) = (\tilde{g}_0, \tilde{l}_0) \land (g', l') = (\tilde{g}_k, \tilde{l}_k) \land \forall i < k : \pi((\tilde{g}_i, \tilde{l}_i), (\tilde{g}_{i+1}, \tilde{l}_{i+1})) > 0$. Then $\lim_{n\to\infty} \text{Prob}(\text{diam}(an n-threaded random program}) \leq (GL-L+1)(L-1)n + (2-L)(G-1)L) = 1$.

Proof. The probability for a thread to satisfy the prerequisites for Lemma V.2.3.1, i.e., to have a strongly connected graph of thread transitions, is strictly positive. With growing n, the probability that a random n-threaded program does not contain such a thread approaches 0.

Note V.2.3.3. In the above claims, the bound (GL - L + 1)(L-1)n + (2-L)(G-1)L on the diameter is linear in the number of threads, and can be simplified to a mathematically larger but conceptually easier expression GL^2n .

Proof. Let e = (GL-L+1)(L-1)n + (2-L)(G-1)L. Case L=1. Then $e = 0n + (G-1) < GL^2n$.

Case $L \ge 2$. Then $e < (GL)Ln + 0 = GL^2n$.

The probability distribution above is given, e.g., when the probability of each thread transition is positive, no matter how small it is.

Informally, the consequence is that trying to find programs with "large" diameter by generating *n*-threaded programs, for instance, uniformly at random would most likely fail for large *n*: we would likely get only at-most–linear values. In contrast, the search from [64], for example, is nonrandom, structured, and informed.

Theorem V.2.3.2 models the risks of a large number of threads being affected externally. Examples are radiation in high-performance computing and row-hammering attacks on server memory [49]. Assuming that the operating system with the scheduler is stored in better-protected memory (which is a reasonable requirement on system builders) and that user-space threads are less protected, and assuming sufficiently long running-times, irreparable bit-flips are likely to turn the threads into random pieces of code while still maintaining their parallel execution. In particular, Theorem V.2.3.2 is an indication that if an "error" program state ever becomes reachable from the current execution state due to bit-flips, it is also likely to become reachable "quickly," i.e., in time linear in n. Our argument even extends to deterministic threads (in which each thread state has at most one successor, and which can be actually executed as opposed to their nondeterministic counterparts) if we additionally assume that the probability of each thread transition is strictly below 1: then, a program asymptotically almost surely contains a thread whose transition graph is a Hamiltonian cycle on Glob \times Loc (such a cycle is both deterministic and strongly connected), and we can apply Theorem V.2.2.3.

Of course, the usual caveats apply: it is a simplification of the reality that the probability of a thread transition depends only on the thread transition itself and that the executions are sequentially consistent in the interleaving semantics. So a closer examination of hardware-near execution models might be required to find out whether the above claim applies to more-realistic machine code, revealing additional risks of long-running, massively parallel software.

VI. Complexity of (non-)reachability

Now we determine the complexity of proving or refuting the reachability of a target program state or a target thread state from a source program state.

For the purpose of defining these reachability problems as formal languages, we assume without loss of generality the following:

- A shared state is an ordinal below G, and it is encoded in binary occupying exactly $\lfloor \log G \rfloor + 1$ bits.
- A local state is an ordinal below *L*, and it is encoded in binary occupying exactly $\lfloor \log L \rfloor + 1$ bits.
- A thread identifier is an ordinal below n, and it is encoded in binary occupying exactly [log n] + 1 bits.

If some binary number actually needs fewer bits than allocated, it is padded with zeros.

In the language definitions below, "a program over G and L" is a program such that its set of shared states is G, and its set of local states is L (we emphasize this to create a textual difference to the language definitions in § II.3):

 $\begin{aligned} &\mathsf{PReach} \stackrel{\mathrm{def}}{=} \{(p, s, s') \mid p \text{ is a program over } G \text{ and } L \land s, s' \in \mathsf{State}_p \land \mathsf{d}_p(s, s') < \infty \}, \\ &\mathsf{PNonReach} \stackrel{\mathrm{def}}{=} \{(p, s, s') \mid p \text{ is a program over } G \text{ and } L \land s, s' \in \mathsf{State}_p \land \mathsf{d}_p(s, s') = \infty \}, \\ &\mathsf{PReach}^{\mathsf{loc}} \stackrel{\mathrm{def}}{=} \left\{(p, s, i, \tau) \middle| \begin{array}{l} p \text{ is a program over } G \text{ and } L \land s \in \mathsf{State}_p \\ \land i < n \land \tau \in \mathsf{Glob} \times \mathsf{Loc} \land \mathsf{d}_p^{\mathsf{loc}}(s, i, \tau) < \infty \end{array} \right\}, \\ &\mathsf{and} \end{aligned}$

 $\mathsf{PNonReach}^{\mathsf{loc}} \stackrel{\mathrm{def}}{=} \left\{ (p, s, i, \tau) \middle| \begin{array}{l} p \text{ is a program over } G \text{ and } L \land s \in \mathsf{State}_p \\ \land i < n \land \tau \in \mathsf{Glob} \times \mathsf{Loc} \land \mathsf{d}_p^{\mathsf{loc}}(s, i, \tau) = \infty \end{array} \right\}.$

The letter P starting the names of the languages above indicates that G and L are the hidden constant *p*arameters of these languages; the number of threads is implicitly existentially quantified in the class terms.

First, we deal with program-state-to-program-state reachability:

Theorem VI.1. PReach, PNonReach \in NSpace $(\log n)$.

Proof. From a high-level view, our algorithm is going to perform a nondeterministic search in the Petri net obtained by a symmetry reduction of an input program using state confusion \approx in its reformulation from Corollary V.2.1.12. The Turing machine *M* that we are going to construct will implement this search.

Given an input on the read-only Turing tape, M first syntactically checks whether the input is a properly encoded triple of a program and two of its states. Then M determines the number of threads n and stores it in binary on the working tape. If n=1, the problem is solved by looking up in a constant table; thus, we assume $n \ge 2$ from now on.

To explain our procedure further, we let $(\rightarrow_i)_{i < n} = p$ be the program, (g, l) = s the source program state, and (g', l') = s' the target one. We fix the standard permutations ι_i (i < n) from Definition V.2.1.1. Notice that the value of a standard permutation for given i < n and $a \in Loc$ can be computed on the working tape using $O(\lfloor \log n \rfloor + 1)$ space (where *L* is considered constant). Next, we allocate $L2^{GL(GL-1)}$ counters of $\lceil \log(n+1) \rceil$ bits each on the working tape of *M*. We will refer to these counters as $c_{\Rightarrow,a}$ for $\Rightarrow \in E$ and $a \in Loc$, where E has been defined in Definition V.2.1.10. In the following state exploration, a newly reached program state (\hat{g}, \hat{l}) will be tracked in these counters: $c_{\exists,a}$ will store $|\{t < n \mid \rightarrow_t = \Rightarrow \land \iota_t(\hat{l}_t) = a\}|$ for each $\Rightarrow \in E$ and $a \in Loc$. (Note that the query $\rightsquigarrow_t = \Rightarrow \land \iota_t(\hat{l}_t) = a$ can be decided by constructing \rightsquigarrow_t and $\iota_t(\hat{l}_t)$ on the working tape and comparing them to \Rightarrow and *a*; one can reuse temporary space on the working tape for doing this for all t < n, consuming $O(|\log n| + 1)$ space in total, where G and L are considered constant.) The machine also allocates $\lceil \log(G+1) \rceil$ bits for storing the shared state \hat{g} . Initially, the shared state g will be stored there, while each counter $c_{\exists,a}$ will be initialized to $|\{t < n \mid \rightarrow_t = \Rightarrow \land \iota_t(l_t) = a\}|$ for $\Rightarrow \in E$ and $a \in \text{Loc.}$ In addition, we allocate an extra counter, called v, which stores the number of visited equivalence classes wrt. \approx ; this counter is initialized to 1.

After initialization, M performs a nondeterministic search in a loop as follows. First of all, M checks whether v exceeds $G(2n)^{(L-1)2^{GL(GL-1)}}$. If so, M halts, since it has visited some equivalence class twice (according to the proof of Theorem V.2.1.22). Otherwise, M continues and checks whether s' (or any state equivalent to s') has been reached. To do this, M checks, according to Corollary V.2.1.12, whether g' is equal to the current shared part and l' has exactly as many threads of different classes as described by the counters $c_{\Rightarrow,a}$ for $\Rightarrow \in E$ and $a \in \text{Loc}$. If it is the case, M accepts: then an execution from s to s' exists. Otherwise, M nondeterministically chooses a thread index i < n, chooses a non–self-loop thread transition $((\hat{g}, a), (\hat{g}', a')) \in \rightarrow_i \backslash D$, and constructs the "normalized" form $((\hat{g}, \iota_i(a)), (\hat{g}', \iota_i(a'))) \in \sim_i$. Examining the counters and the shared-state variable, M determines whether the thread transition is applicable: M tests whether the shared-state variable stores \hat{g} and whether $c_{\rightarrow_i,\iota_i(a)} > 0$. If the result of this test is negative, M halts, otherwise M updates the shared-state variable and the counters according to the thread transition. Such an update stores \hat{g}' , decrements $c_{\rightarrow_i,\iota_i(a)}$, and increments $c_{\sim_i,\iota_i(a')}$. Moreover, M increments v. After that, M goes to the loop start. To show that all program states reachable from s are explored by M, note that M examines an overapproximating abstraction:

- confusable states are not distinguished, and
- at each loop iteration, all applicable non-loop thread transitions are available for the nondeterministic choice, so *M* considers all successors of the current state.

To show that only program states that are reachable from *s* are explored, notice that, in each loop iteration, if a currently considered program state \tilde{s} is reachable from *s*, all program states that are confusable with \tilde{s} are also reachable from *s* according to Lemma V.2.1.13. So the analysis has no opportunity to jump to an unreachable program state from \tilde{s} .

Thus, M explores exactly the states reachable from s.

Throughout any execution of M, the sum $\sum_{\substack{\Rightarrow \in E, a \in Loc}} c_{\Rightarrow,a}$ remains constant; therefore, $O(\log n)$ space suffices for each of the $L2^{GL(GL-1)}$ counters. Taking into account also

 $O(\log n)$ space suffices for each of the $L2^{OL(GL^{-1})}$ counters. Taking into account also the shared-state variable of size $\lceil \log(G+1) \rceil$ on the working tape and temporary counters of for navigating through the input as well as for computing the values of ι , M consumes $O(\log n)$ space on the working tape regardless of the computation branch and always halts (whether accepting or not). Since the input size is proportional to n, we have shown PReach $\in NSpace(\log n)$.

Since NSpace($\log n$) is closed under complementation [44, 86], the non-reachability problem also belongs to NSpace($\log n$).

The nondeterministic logarithmic space complexity of reachability is low but occurs sometimes; see, e.g., [4, Fig. 2].

For program-state-to-thread-state reachability, recall that low-complexity problems tend to be sensitive to the input format. So we now provide the details of the encoding of PReach^{loc} and PNonReach^{loc} into bitstrings. A thread transition relation is encoded using a bitstring of length $|(\text{Glob} \times \text{Loc})^2| = G^2 L^2$. An *n*-threaded program *p* is stored as a list of thread transition relations in a self-delimited way. (For example, insert 0 between each pair of thread transition relations and append 1 at the end. This encoding uses $(G^2 L^2 + 1)n$ bits. Using a slightly more compact self-delimiting encoding [57] would not change the complexity class.) An element of Loc^n is represented as a string of $n(\lfloor \log L \rfloor + 1)$ bits assuming that *n* is known. A program state $(g, l) \in \text{Glob} \times \text{Loc}^n$ is formed by prepending the encoding of *g* to the encoding of *l*, again assuming that *n* is known. The number *i* from the third component of the quadruple $(p, s, i, \tau) \in$ PReach^{loc} \cup PNonReach^{loc} is stored in binary and not in a self-delimiting way in the bits right after *s*. The encoding of *i* is followed by an encoding of τ using the final $(\lfloor \log G \rfloor + 1) + (\lfloor \log L \rfloor + 1)$ bits.

We expect that slight encoding variations (e.g., in the self-delimiting encoding of p) will not change the complexity class.

Theorem VI.2. PReach^{loc}, $PNonReach^{loc} \in NC^1$.

Proof. We will describe of a logspace-uniform family of circuits that will recognize PReach^{loc} similarly to how the machine *M* from the proof of Theorem VI.1 does it. Given $m \in \mathbb{N}_{\geq 0}$, we now describe a bounded–fan-in circuit with *m* inputs, polynomial size in *m*, and $O(\log m)$ depth that recognizes all words in PReach^{loc} of length *m*. A part of this circuit guesses n < m by a balanced "or" tree and then checks, by a logarithmically deep circuit, that *p* is indeed a description of an *n*-threaded program (with the aforementioned self-delimited encoding, a balanced "and" tree can check that positions $(G^2L^2 + 1)j - 1$ contain zeros for positive j < n and one for j = n). For

each valid choice of *n*, interpret the $\lfloor \log G \rfloor + 1 + (\lfloor \log L \rfloor + 1)n$ bits following the description of *p* as a description of $s \in \text{Glob} \times \text{Loc}^n$. Then interpret all the bits at positions $(G^2L^2+1)n+\lfloor \log G \rfloor+1+(\lfloor \log L \rfloor+1)n=(G^2L^2+\lfloor \log L \rfloor+2)n+\lfloor \log G \rfloor+1$ till $m - ((\lfloor \log G \rfloor + 1) + (\lfloor \log L \rfloor + 1)) - 1 = m - \lfloor \log G \rfloor - \lfloor \log L \rfloor - 3$ as the binary encoding of *i*. Interpret the remaining positions $m - \lfloor \log G \rfloor - \lfloor \log L \rfloor - 2$ till m - 1 as τ . Check that i < n by a logarithmically deep circuit. (For example, convert *n* into binary by a recursive three-integers-to-two-integers addition, find the most significant bit position in which the binary representations of *n* and *i* differ, and check that in that position, the bit of the representation of *n* is larger than the corresponding bit of *i*.)

Now, we fix an arbitrary pair $(n, i) \in \mathbb{N}_+ \times \mathbb{N}_{\geq 0}$ such that i < n. We are left with the problem of describing an NC¹ circuit that determines whether $d_p^{\text{loc}}(s, i, \tau) < \infty$. We are going to show how a Turing machine with a read-only input tape and a spacebounded read-write output tape, which we will construct and call M^{loc} , would decide this problem. In the following explanation, $(\rightarrow_i)_{i < n} = p$ will be the program, (g, l) =s the source program state, and $(g', l') = \tau$ the target thread state. The machine M^{loc} allocates $2^{GL(GL-1)}$ binary counters on the working tape; each binary counter has a width of $\lfloor \log C \rfloor + 1$ bits, where C is the maximal local diameter (cf. Definition and Corollary IV.3.8). These counters will be referred to as $\tilde{c}_{\Rightarrow} (\Rightarrow \in E)$ and initialized by $\tilde{c}_{\rightrightarrows} := \min\{C, |\{t < n \mid \rightsquigarrow_t = \rightrightarrows\}\} \mid \{ (\rightrightarrows \in E) \}$. (Again, counting can be done with a recursive three-integers-to-two-integers addition, which is performed till the bit width of C is exceeded; taking the minimum can be done by finding the most significant bit in which two numbers differ.) The machine also copies the shared state g, the normalized thread transition relation \rightsquigarrow_i , and the "normalized" target thread state $\tilde{\tau} = (g', \iota_i(l'))$ to the working tape. In the abstract, M^{loc} would need to explore the state space of a program that starts in the shared state g and has $\tilde{c}_{\Rightarrow} \leq C$ threads starting in the local state l_0 and having the transition relation \Rightarrow (for $\Rightarrow \in E$); the goal is to decide on the reachability of $\tilde{\tau}$ in any thread with the transition relation \rightsquigarrow_i . Instead of actually performing the search, M^{loc} uses the fact that the number of decision questions of the above kind is finite (namely, below $G \cdot (C+1)^{2^{GL(GL-1)}} \cdot 2^{GL(GL-1)} \cdot GL$), so M^{loc} simply looks up in a table stored in the machine state. (Notice: although we do not know the actual yes/no answers to these decision questions, we still know that such a finite table of answers exists.)

To see that M^{loc} is sound with respect to the reachability question for the original program p, note that if M^{loc} answers "reachable", then the thread state τ of the i^{th} thread is indeed reachable from s in p: adding more threads beyond C that do not take any steps would not destroy reachability.

To see that M^{loc} is complete with respect to the reachability question for the original program p, note that if in p there is a walk from the program state s to the thread state τ of the thread i, there is also a path of length not exceeding C from s to the thread state τ of the thread i. Therefore, at most C threads of any equivalence class from η/\sim take steps in this path; all the other threads do not perform any steps. Throwing out these other "lazy" threads, we obtain a path from a "sub-state" of s to the thread state τ of thread i in a program in which there are no more than C threads of any equivalence class from η/\sim . The existence of this path will be reported by M^{loc} due to its construction.

Thus, M^{loc} recognizes PReach^{loc}.

Since M^{loc} takes bounded working-tape space, M^{loc} can be converted into a finite automaton, which uses zero space on the working tape. The existence of an accepting path in the finite automaton from the initial state to the final state can be decided with an NC¹ circuit using the standard recursive divide-and-conquer construction.

As for PNonReach^{loc}, the proof is almost the same as above, except that M^{loc} , instead of asking $d_p^{\text{loc}}(s, i, \tau) < \infty$, now asks $d_p^{\text{loc}}(s, i, \tau) = \infty$.

We do not expect the complexity to drop significantly below NC¹, since checking whether a unary-encoded number is larger than a binary-encoded number (n > i in the problem formulation) cannot be performed by a finite automaton.

VII. Discussion and conclusion

In the present paper, we tackled the case of arbitrary but constant shared and local state space. We derived the bounds $(GL-L+1)(L-1)n + (2-L)(G-1)L \leq \text{diamax}(n) < G\binom{n/t+L-1}{L-1}^t$, where $t = 2^{GL(GL-1)}$, on the maximum diameter of an *n*-threaded program with *G* shared states and *L* local states for all *n*. Notice that the exponent does not depend on the number of threads. The lower bound was proven by constructing an infinite family of explicit programs such that the *n*th program has *n* threads and diameter (GL-L+1)(L-1)n + (2-L)(G-1)L. The upper bound is both a strengthening and a generalization of the corresponding bound from [64]. The mathematical computations behind these bounds are tight in the following sense: if better yet simple bounds exist, they would require genuinely new proof ideas. Moreover, we discovered the following results:

- a polynomial upper bound for the diameter for a subclass of multithreaded programs; this bound is linear in *n* and, in certain cases, agrees with the lower bound;
- a polynomial upper bound for a rather general class of probability distributions and a randomly chosen program; this bound is linear in *n* and matches the lower bound;
- an upper bound on the local diameter that does not depend on *n*; the function mapping *G* and *L* to the least upper bound is computable, and the computation algorithm can be readily extracted from the proofs;
- (non-)reachability of target program states from source program states is decidable in NSpace(log *n*);
- (non-)reachability of target thread states from source program states is decidable in NC¹ (which strengthens and generalizes the corresponding result from [64]).

(Sub-)polynomial characterizations of this type are desirable as they, besides being aesthetically pleasing, give hope for practical algorithms.

(Besides, in the unparametrized case, we formally proved PSpace-completeness of reachability and nonreachability of program states and thread states in multithreaded programs with respect to logspace reductions.)

So far, no infinite family of *n*-threaded programs with constant shared and local space sizes and superlinear diameter (in *n*) is known. This is in stark discrepancy to the presence of examples of sequential programs whose diameter is exponential in the number of variables and in extreme discrepancy to the presence of Petri nets computing non-primitive recursive functions [69]. The polynomial bound on diamax implies that the distances in the transition graph of a program are at most polylogarithmic in the size of this graph (whereas the general upper bound on the distances in an arbitrary graph is linear rather than polylogarithmic).

From a purely theoretical viewpoint, searching for counterexamples (to certain non-reachability properties) in certain programs with depth-first search (DFS) can be improved now: the search depth can be bounded by $O(n^c)$ for some c > 1 not depending on *n* for nonlocal properties and by a constant for local properties. Thus, the worst-case time for look-ups in the DFS stack can be improved from linear to logarithmic or constant in *n*. If all reachable states up to depth $O(n^c)$ for nonlocal properties are explored, full

coverage holds even if the bug finder at hand thinks that more reachable states might still exist, but does not know it for sure due to, for example, not having the ability or enough space to store already visited states. Similarly, if all reachable states up to some constant depth independent of n are explored, full coverage for local non-reachability properties is achieved, making bounded verification complete. These search-depths bounds for non-local and local properties are asymptotically negligible relative to the number of states. Moreover, if a good upper bound on diamax (or C for local properties) is ever provided, the modulus of the difference between this upper bound and the (e.g., default worst-case) search depth of a bug finder might be used to better estimate the quality of the bug finder. This would help to coarsely rank the bug finders.

Low complexity of (non-)reachability has some asymptotic ramifications in terms of classical complexity theory (again, *n* being the only variable). First, reachability queries are probably efficiently parallelizable in *n* due to $NC^1 \subseteq NSpace(\log(\text{input size})) \subseteq NC^2$. Second, finding bugs in even huge input programs is probably tractable due to using only $O(\log^2 n)$ additional memory cells (because of $NC^1 \subseteq NSpace(\log n) \subseteq DSpace(\log^2 n)$).

From an algorithmic viewpoint, our proofs of the complexity bounds demonstrate that symmetry reduction in tools is more than just a heuristic: when suitably performed, it diminishes the resource consumption also in the worst case.

To the practical tool builder, on the one hand, our results show that implementations could consider paying a little bit more attention to avoiding the state-space explosion in n in the worst case (since the exponential blow-up in n is theoretically avoidable in the setting described). On the other hand, large or unknown constants in our upper bounds emphasize a well-known warning: theoretical advances primarily say only that certain improvements could probably be achieved asymptotically in principle, not that any advance translates into practice directly.

Concerning research on verification, our results show that, in certain natural mathematical setups, the exponential state-explosion phenomenon in the number of threads has reduced or no bearing on the reachability analysis for parallel programs. Further, if a modeling step bounds the sizes of the shared and thread-local state spaces in the abstraction when they are unbounded in the concrete, unexpected mathematical artifacts may emerge. One such artifact is the state-explosion problem turning out to be asymptotically a nonproblem, informally speaking. Depending on the application, this artifact might be considered helpful (e.g., if small values of the fixed parameters turn out to allow for specialized but fast verifiers, for instance, using caching) or detrimental (e.g., hidden large constants might fool the developers). Also, the widely stated claim that the exponential blow-up of the state space in the number of threads is a major obstacle to verification should not be *blindly* interpreted in a mathematically straightforward way. Sometimes this blow-up is an empirical phenomenon occurring in the tools that do not perform symmetry reduction properly or at all, and sometimes the set of programs on which the state explosion is observed simply does not have constant local and shared state spaces.

As a next theoretical-research step, we plan to tighten the aforementioned inequalities. While we do not know whether diamax can be majorized, for instance, by a linear function, we expect that the upper bound could be lowered in principle; one way could start with determining a possibly large class of threads that do not occur in diamax-realizing programs and subtracting this class while defining *E* in Definition V.2.1.10 and another way could be using distance-preserving bisimulations to reduce the index of \approx . An interesting research direction is searching for an explicit upper bound on the maximal local diameter *C* (perhaps, using [75]). Locating the general and local

reachability problems in the hierarchy of parametric complexity classes is a further, orthogonal line of research. Finally, a mathematical study of the state-space explosion problem in the number of parallel components is important also for other models of parallel computation.

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